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"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "<u>Embedded - Microcontrollers</u>"

| Details                    |                                                                                 |
|----------------------------|---------------------------------------------------------------------------------|
| Product Status             | Obsolete                                                                        |
| Core Processor             | M16C/60                                                                         |
| Core Size                  | 16-Bit                                                                          |
| Speed                      | 20MHz                                                                           |
| Connectivity               | I <sup>2</sup> C, IEBus, SIO, UART/USART                                        |
| Peripherals                | DMA, POR, PWM, Voltage Detect, WDT                                              |
| Number of I/O              | 71                                                                              |
| Program Memory Size        | 64KB (64K x 8)                                                                  |
| Program Memory Type        | FLASH                                                                           |
| EEPROM Size                | 4K x 8                                                                          |
| RAM Size                   | 4K x 8                                                                          |
| Voltage - Supply (Vcc/Vdd) | 2.7V ~ 5.5V                                                                     |
| Data Converters            | A/D 24x10b                                                                      |
| Oscillator Type            | Internal                                                                        |
| Operating Temperature      | -20°C ~ 85°C (TA)                                                               |
| Mounting Type              | Surface Mount                                                                   |
| Package / Case             | 85-TFLGA                                                                        |
| Supplier Device Package    | 85-TFLGA (7x7)                                                                  |
| Purchase URL               | https://www.e-xfl.com/product-detail/renesas-electronics-america/m30280f8wg-u5b |

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# M16C/28 Group (M16C/28, M16C/28B) SINGLE-CHIP 16-BIT CMOS MCU

REJ03B0201-0050 Rev.0.50 2006.09.15

# 1. Overview

The M16C/28 Group (M16C/28 and M16C/28B) MCU are single-chip control MCU, fabricated using high-performance silicon gate CMOS technology with the M16C/60 series CPU core. The M16C/28 Group (M16C/28 and M16C/28B) are housed in 64-pin and 80-pin plastic molded LQFP packages and also in 85-pin plastic molded TFLGA (Thin Fine Pitch Land Grid Array) package. With a 1-Mbyte address space, this MCU combines advanced instruction manipulation capabilities to process complex instructions by less bytes and execute instructions at higher speed. It includes a multiplier and DMAC adequate for office automation, communication devices and other high-speed processing applications.

The M16C/28 has Normal-ver., T-ver., and V-ver.. The M16C/28B has Normal-ver. only.

This hardware manual describes the Normal-ver. only. Please contact Renesas Technology Corp. for T-ver./V-ver. information.

# 1.1 Applications

Audio, cameras, office equipment, communication equipment, portable equipment, home appliances (inverter solution), motor control, industrial equipment, etc.

Table 1.2 M16C/28 Group (M16C/28, M16C/28) (64-Pin Package)

|                 | Item                         | Performance                                                                            |  |  |
|-----------------|------------------------------|----------------------------------------------------------------------------------------|--|--|
| CPU             | Number of basic instructions | 91 instructions                                                                        |  |  |
|                 | Minimum instruction          | 41.7 ns (f(BCLK) = 24 MHZ, VCC = 4.2 V to 5.5 V) (M16C/28B)                            |  |  |
|                 | excution time                | 50 ns (f(BCLK) = 20 MHZ, VCC = 3.0V to 5.5V) (M16C/28, M16C/28B)                       |  |  |
|                 |                              | 100 ns (f(BCLK) = 10 MHZ, VCC = 2.7V to 5.5V) (M16C/28, M16C/28B)                      |  |  |
|                 | Operation mode               | Single chip mode                                                                       |  |  |
|                 | Address space                | 1M bytes                                                                               |  |  |
|                 | Memory capacity              | See Table 1.3                                                                          |  |  |
| Peripheral      | I/O Port                     | Input/Output : 55 lines                                                                |  |  |
| Function        | Multifunction timer          | TimerA:16 bits x 5 channels, TimerB:16 bits x 3 channels                               |  |  |
|                 |                              | Three-phase Motor Control Timer                                                        |  |  |
|                 |                              | TimerS (Input Capture/Output Compare)                                                  |  |  |
|                 |                              | : 16bit base timer x 1 channel (Input/Output x 8 channels )                            |  |  |
| l               | Serial I/O                   | 2 channels (UART0, UART1)                                                              |  |  |
|                 |                              | UART, clock synchronous                                                                |  |  |
|                 |                              | 1 channel (UART2)                                                                      |  |  |
|                 |                              | UART, clock synchronous, I <sup>2</sup> C bus <sup>(1)</sup> , or IEbus <sup>(2)</sup> |  |  |
|                 |                              | 1 channels (SI/O3, SI/O4)                                                              |  |  |
|                 |                              | Clock synchronous                                                                      |  |  |
|                 |                              | 1 channel (Multi-Master I <sup>2</sup> C bus <sup>(1)</sup> )                          |  |  |
|                 | A/D converter                | 10 bits x 13 channels                                                                  |  |  |
|                 | DMAC                         | 2 channels                                                                             |  |  |
|                 | Watchdog timer               | 15 bits x 1 (with prescaler)                                                           |  |  |
|                 | Interrupt                    | 24 internal and 8 external sources, 4 software sources, 7 levels                       |  |  |
|                 | Clock generation circuit     | 4 circuits                                                                             |  |  |
|                 |                              | • Main clock(*)                                                                        |  |  |
|                 |                              | • Sub-clock(*)                                                                         |  |  |
|                 |                              | On-chip oscillator     PLL frequency synthesizer                                       |  |  |
|                 |                              | (*) Equipped with a built-in feedback resistor                                         |  |  |
|                 | Oscillation Stop Detect      | Main clock oscillation stop, re-oscillation detect function                            |  |  |
|                 | Function                     | Wall Gook oscillation stop, ic oscillation detect function                             |  |  |
|                 | Voltage detection circuit    | Available                                                                              |  |  |
| Electrical      | Power supply voltage         | Vcc = 4.2 V to 5.5 V (f(BCLK) = 24 MHz) (M16C/28)                                      |  |  |
| Characteristics | Tower supply voltage         | Vcc = 3.0 V to 5.5 V (f(BCLK) = 20 MHz) (M16C/28, M16C/28B)                            |  |  |
| Onaracionolica  |                              | Vcc = 2.7 V to 5.5 V (f(BCLK) = 10 MHz) (M16C/28, M16C/28B)                            |  |  |
|                 | Power consumption            | 16 mA (VCC = 5 V, f(BCLK) = 20 MHz)                                                    |  |  |
|                 | . one, concumpation          | 25 μA (f(XCIN) = 32 KHz on RAM)                                                        |  |  |
|                 |                              | 3.0 $\mu$ A (Vcc = 3 V, f(XCIN) = 32 KHz, in wait mode)                                |  |  |
|                 |                              | 0.7 μA (Vcc = 3 V, in stop mode)                                                       |  |  |
| Flash Memory    | Program/erase supply voltage | 2.7 V to 5.5 V                                                                         |  |  |
| ĺ               | Program and erase endurance  | 100 times (all space) or 1,000 times (Blocks 0 to 5)                                   |  |  |
|                 |                              | /10,000 times (Block A, Block B <sup>(3)</sup> )                                       |  |  |
| Operating Am    | bient Temperature            | -20 to 85C°/-40 to 85C° <sup>(3)</sup>                                                 |  |  |
| Package         | - 1                          | 64-pin plastic mold LQFP                                                               |  |  |
| NOTES:          |                              | o . p p.socio mois Est i                                                               |  |  |

#### NOTES:

- 1. I<sup>2</sup>C bus is a trademark of Koninklijke Philips Electronics N. V.
- 2. IEBus is a trademark of NEC Electronics Corporation.
- 3. Refer to **Table 1.5** to **1.7** for number of program/erase.
- 4. Use PLL frequency synthesizer to use M16C/28B at f(BCLK) = 24 MHz.



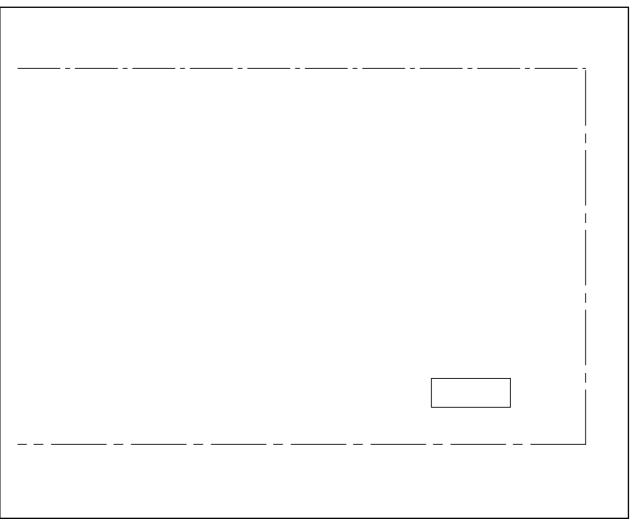


Figure 1.2 M16C/28 Group (M16C/28, M16C/28B), 64-Pin Block Diagram

### 1.4 Product Information

**Tables 1.3** and **1.4** list the M16C/28 Group product information and **Figure 1.3** shows the product numbering system. The specifications are partially different between normal-ver.and T/ V-ver..

Table 1.3 M16C/28 Product List -Normal-ver.

As of September, 2006

| Type Number    | Type Number |             | RAM<br>Capacity | Package Type              | Remarks         | Product Code   |
|----------------|-------------|-------------|-----------------|---------------------------|-----------------|----------------|
| M30280F6WG     | (N)         | 48 K + 4 K  | 4 K             |                           |                 |                |
| M30280F8WG     | (N)         | 64 K + 4 K  | 4 K             | PTLG0085JB-A (85F0G)      |                 |                |
| M30280FAWG     | (N)         | 96 K + 4 K  | 8 K             |                           |                 |                |
| M30280F6HP     | (N)         | 48 K + 4 K  | 4 K             |                           |                 |                |
| M30280F8HP     | (N)         | 64 K + 4 K  | 4 K             | PLQP0080KB-A (80P6Q-A)    | Floob           |                |
| M30280FAHP     | (N)         | 96 K + 4 K  | 8 K             | FLQF0000KB-A (00F0Q-A)    | Flash<br>Memory | U3, U5, U7, U9 |
| M30280FCHP     | (N)         | 128 K + 4 K | 12 K            |                           |                 |                |
| M30281F6HP     | (N)         | 48 K + 4 K  | 4 K             |                           |                 |                |
| M30281F8HP     | (N)         | 64 K + 4 K  | 4 K             | PLQP0064KB-A (64P6Q-A)    |                 |                |
| M30281FAHP     | (N)         | 96 K + 4 K  | 8 K             | 1 EQ1 000+NB-A (0+1 0Q-A) |                 |                |
| M30281FCHP     | (N)         | 128 K + 4 K | 12 K            |                           |                 |                |
| M30280M8-XXXHP | (N)         | 64 K        | 4 K             |                           |                 |                |
| M30280MA-XXXHP | (N)         | 96 K        | 8 K             | PLQP0080KB-A (80P6Q-A)    |                 |                |
| M30280MC-XXXHP | (N)         | 128 K       | 12 K            |                           | Mask            | U3, U5         |
| M30281M8-XXXHP | (N)         | 64 K        | 4 K             |                           | ROM             | 00,00          |
| M30281MA-XXXHP | (N)         | 96 K        | 8 K             | PLQP0064KB-A (64P6Q-A)    |                 |                |
| M30281MC-XXXHP | (N)         | 128 K       | 12 K            |                           |                 |                |

(N): New

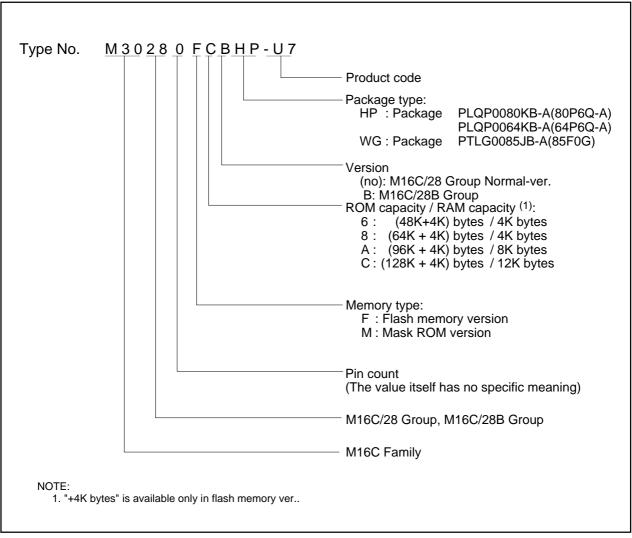
Table 1.4 M16C/28B Product List -Normal-ver.

As of September, 2006

| Type Number |     | ROM<br>Capacity | RAM<br>Capacity | Package Type           | Remarks | Product Code |
|-------------|-----|-----------------|-----------------|------------------------|---------|--------------|
| M30280FCBHP | (D) | 128 K + 4 K     | 12 K            | PLQP0080KB-A (80P6Q-A) | Flash   | U7           |
| M30281FCBHP | (D) | 128 K + 4 K     | 12 K            | m o m o m /            |         | 07           |

(D): Under development





**Figure 1.3 Product Numbering System** 

Table 1.5 Product Code (Flash Memory-ver.) - M16C/28 Normal-ver., 64-Pin<sup>(1)</sup>/80-Pin<sup>(1)</sup>/85-Pin Package

| Product |           | Internal ROM<br>(User Program Space) |                      | Internal ROM<br>(Data Space)      |                      | Operating Ambient |  |
|---------|-----------|--------------------------------------|----------------------|-----------------------------------|----------------------|-------------------|--|
| Code    | Package   | Program and<br>Erase<br>Endurance    | Temperature<br>Range | Program and<br>Erase<br>Endurance | Temperature<br>Range | Temperature       |  |
| U3      |           | 100                                  | 0 to 60℃             | 100                               | 0 to 60℃             | -40 to 85℃        |  |
| U5      | Lead free | 100                                  |                      | 100                               | 0 10 00 0            | -20 to 85℃        |  |
| U7      | Leau liee |                                      |                      | 10,000                            | -40 to 85℃           | -40 to 85℃        |  |
| U9      |           | 1,000                                |                      | 10,000                            | -20 to 85℃           | -20 to 85℃        |  |

#### NOTE:

Table 1.6 Product Code (Flash Memory-ver.) - M16C/28B Normal-ver., 64-Pin/85-Pin Package

| Product<br>Code | _         | Internal ROM<br>(User Program Space) |                      | Internal ROM<br>(Data Space)      |                      | Operating Ambient |  |
|-----------------|-----------|--------------------------------------|----------------------|-----------------------------------|----------------------|-------------------|--|
|                 | Package   | Program<br>and Erase<br>Endurance    | Temperature<br>Range | Program<br>and Erase<br>Endurance | Temperature<br>Range | Temperature       |  |
| U7              | Lead-free | 1,000                                | 0 to 60℃             | 10,000                            | -40 to 85℃           | -40 to 85℃        |  |

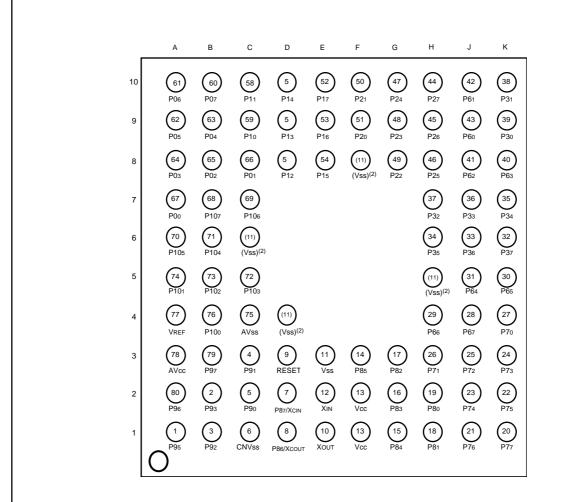
Table 1.7 Product Code (Mask ROM ver.) - M16C/28B Normal-ver., 64-Pin/80-Pin/85-Pin Package

| Product<br>Code | Package   | Operating Ambient<br>Temperature |
|-----------------|-----------|----------------------------------|
| U3              | Lead-free | -40 to 85℃                       |
| U5              | Load-life | -20 to 85℃                       |

<sup>1.</sup> The lead contained products, D3, D5, D7 and D9, are put together with U3, U5, U7 and U9 respectively. Lead-free (Sn-Ag-Cu plating) products can be mounted by both conventional Sn-Pb paste and Lead-free paste.

# 1.5 Pin Assignment

Figures 1.5 to 1.7 show the pin Assignments (top view).



#### NOTES:

- The numbers in each grid (circle) show the pin numbers of the M30280FAHP (80P6Q-A package)
- 2. Connect grids written as (Vss) to Vss(GND) or leave them open.
- Set PACR2 to PACR0 bits in the PACR register to "0112" before you input and output it after resetting to each pin. When the PACR register is not set, the input and output function of some pins are disabled.

Package: PTLG0085JB-A(85F0G)

Figure 1.5 Pin Assignment (Top View) of 85-pin Package

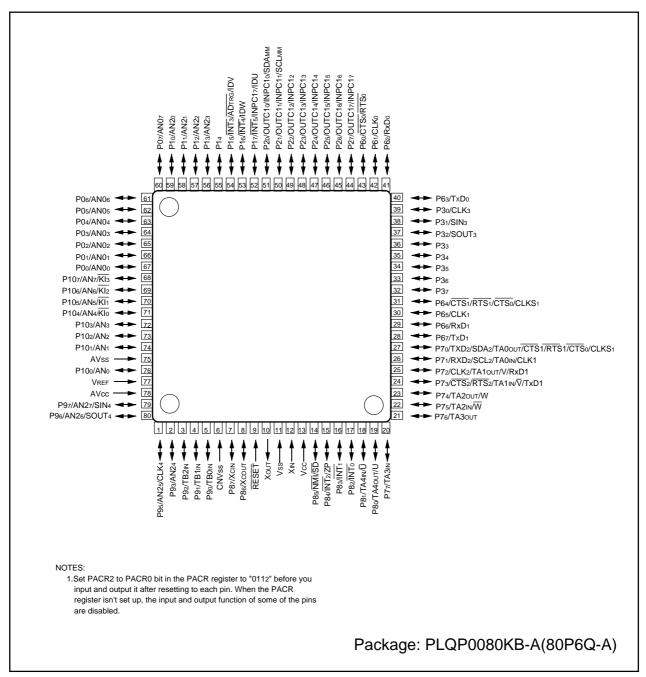


Figure 1.5 Pin Assignment (Top View) of 80-Pin Package

Table 1.9 Pin Characteristics for 80-Pin Package

| Pin<br>No. | Control<br>Pin | Port | Interrupt<br>Pin | Timer Pin         | Timer S Pin | UART Pin                                 | Multi-master<br>I <sup>2</sup> C bus Pin | Analog Pin |
|------------|----------------|------|------------------|-------------------|-------------|------------------------------------------|------------------------------------------|------------|
| 1          |                | P95  |                  |                   |             | CLK4                                     |                                          | AN25       |
| 2          |                | P93  |                  |                   |             |                                          |                                          | AN24       |
| 3          |                | P92  |                  | TB2IN             |             |                                          |                                          |            |
| 4          |                | P91  |                  | TB1IN             |             |                                          |                                          |            |
| 5          |                | P90  |                  | TBOIN             |             |                                          |                                          |            |
| 6          | CNVss          |      |                  |                   |             |                                          |                                          |            |
| 7          | XCIN           | P87  |                  |                   |             |                                          |                                          |            |
| 8          | Хсоит          | P86  |                  |                   |             |                                          |                                          |            |
| 9          | RESET          |      |                  |                   |             |                                          |                                          |            |
| 10         | Хоит           |      |                  |                   |             |                                          |                                          |            |
| 11         | Vss            |      |                  |                   |             |                                          |                                          |            |
| 12         | XIN            |      |                  |                   |             |                                          |                                          |            |
| 13         | Vcc            |      |                  |                   |             |                                          |                                          |            |
| 14         |                | P85  | NMI              | SD                |             |                                          |                                          |            |
| 15         |                | P84  | ĪNT2             | ZP                |             |                                          |                                          |            |
| 16         |                | P83  | ĪNT <sub>1</sub> |                   |             |                                          |                                          |            |
| 17         |                | P82  | ĪNT <sub>0</sub> |                   |             |                                          |                                          |            |
| 18         |                | P81  |                  | TA4IN / Ū         |             |                                          |                                          |            |
| 19         |                | P80  |                  | TA40UT / <b>U</b> |             |                                          |                                          |            |
| 20         |                | P77  |                  | ТАзім             |             |                                          |                                          |            |
| 21         |                | P76  |                  | ТАзоит            |             |                                          |                                          |            |
| 22         |                | P75  |                  | TA2IN / W         |             |                                          |                                          |            |
| 23         |                | P74  |                  | TA20UT / W        |             |                                          |                                          |            |
| 24         |                | P73  |                  | TA1IN / V         |             | CTS2 / RTS2 / TxD1                       |                                          |            |
| 25         |                | P72  |                  | TA10UT / V        |             | CLK2 / RxD1                              |                                          |            |
| 26         |                | P71  |                  | TAOIN             |             | RxD2 / SCL2 / CLK1                       |                                          |            |
| 27         |                | P70  |                  | ТАооит            |             | TxD2 / SDA2 / RTS1 / CTS1 / CTS0 / CLKS1 |                                          |            |
| 28         |                | P67  |                  |                   |             | TxD1                                     |                                          |            |
| 29         |                | P66  |                  |                   |             | RxD1                                     |                                          |            |
| 30         |                | P65  |                  |                   |             | CLK1                                     |                                          |            |
| 31         |                | P64  |                  |                   |             | RTS1 / CTS1/ CTS0 /<br>CLKS1             |                                          |            |
| 32         |                | P37  |                  |                   |             |                                          |                                          |            |
| 33         |                | P36  |                  |                   |             |                                          |                                          |            |
| 34         |                | P35  |                  |                   |             |                                          |                                          |            |
| 35         |                | P34  |                  |                   |             |                                          |                                          |            |
| 36         |                | P33  |                  |                   |             |                                          |                                          |            |
| 37         |                | P32  |                  |                   |             | <b>S</b> оитз                            |                                          |            |
| 38         |                | P31  |                  |                   |             | SIN3                                     |                                          |            |
| 39         |                | P30  |                  |                   |             | CLK3                                     |                                          |            |
| 40         |                | P63  |                  |                   |             | TxD0                                     | 1                                        |            |

Table 10 Pin Characteristics for 64-Pin Package (Continued)

| Pin<br>No. | Control<br>Pin | Port | Interrupt<br>Pin | Timer Pin | Timer S Pin     | UART Pin | Multi-master<br>I <sup>2</sup> C bus Pin | Analog Pin      |
|------------|----------------|------|------------------|-----------|-----------------|----------|------------------------------------------|-----------------|
| 41         |                | P23  |                  |           | OUTC13 / INPC13 |          |                                          |                 |
| 42         |                | P22  |                  |           | OUTC12 / INPC12 |          |                                          |                 |
| 43         |                | P21  |                  |           | OUTC11 / INPC11 |          | SCLMM                                    |                 |
| 44         |                | P20  |                  |           | OUTC10 / INPC10 |          | SDAMM                                    |                 |
| 45         |                | P17  | ĪNT5             | IDU       | INPC17          |          |                                          |                 |
| 46         |                | P16  | ĪNT4             | IDW       |                 |          |                                          |                 |
| 47         |                | P15  | ĪNT3             | IDV       |                 |          |                                          | ADTRG           |
| 48         |                | P03  |                  |           |                 |          |                                          | AN03            |
| 49         |                | P02  |                  |           |                 |          |                                          | AN02            |
| 50         |                | P01  |                  |           |                 |          |                                          | AN01            |
| 51         |                | P00  |                  |           |                 |          |                                          | AN00            |
| 52         |                | P107 | KIз              |           |                 |          |                                          | AN7             |
| 53         |                | P106 | Kl <sub>2</sub>  |           |                 |          |                                          | AN <sub>6</sub> |
| 54         |                | P105 | KI <sub>1</sub>  |           |                 |          |                                          | AN <sub>5</sub> |
| 55         |                | P104 | KI <sub>0</sub>  |           |                 |          |                                          | AN4             |
| 56         |                | P103 |                  |           |                 |          |                                          | AN <sub>3</sub> |
| 57         |                | P102 |                  |           |                 |          |                                          | AN <sub>2</sub> |
| 58         |                | P101 |                  |           |                 |          |                                          | AN <sub>1</sub> |
| 59         | AVss           |      |                  |           |                 |          |                                          |                 |
| 60         |                | P100 |                  |           |                 |          |                                          | AN <sub>0</sub> |
| 61         | VREF           |      |                  |           |                 |          |                                          |                 |
| 62         | AVcc           |      |                  |           |                 |          |                                          |                 |
| 63         |                | P93  |                  |           |                 |          |                                          | AN24            |
| 64         |                | P92  |                  | TB2IN     |                 |          |                                          |                 |

# 3. Memory

**Figure 3.1** is a memory map of the M16C/28 Group (M16C/28, M16C/28B). M16C/28 Group provides 1-Mbyte address space from addresses 0000016 to FFFFF16. The internal ROM is allocated lower addresses beginning with address FFFFF16. For example, 64 Kbytes internal ROM is allocated addresses F000016 to FFFFF16.

Two 2-Kbyte internal ROM areas, block A and block B, are available in the flash memory version. The blocks are allocated addresses F00016 to FFFF16.

The fixed interrupt vector tables are allocated addresses FFFDC16 to FFFFF16. It stores the starting address of each interrupt routine. See the section on interrupts for details.

The internal RAM is allocated higher addresses beginning with address 0040016. For example, 4-Kbytes internal RAM is allocated addresses 0040016 to 013FF16. Besides storing data, it becomes stacks when the subroutine is called or an interrupt is acknowledged.

SFR, consisting of control registers for peripheral functions such as I/O port, A/D converter, serial I/O, timers is allocated addresses 0000016 to 003FF16. All blank spaces within SFR are reserved and cannot be accessed by users.

The special page vector table is allocated to the addresses FFE0016 to FFFDB16. This vector is used by the JMPS or JSRS instruction. For details, refer to the *M16C/60 and M16C/20 Series Software Manual*.

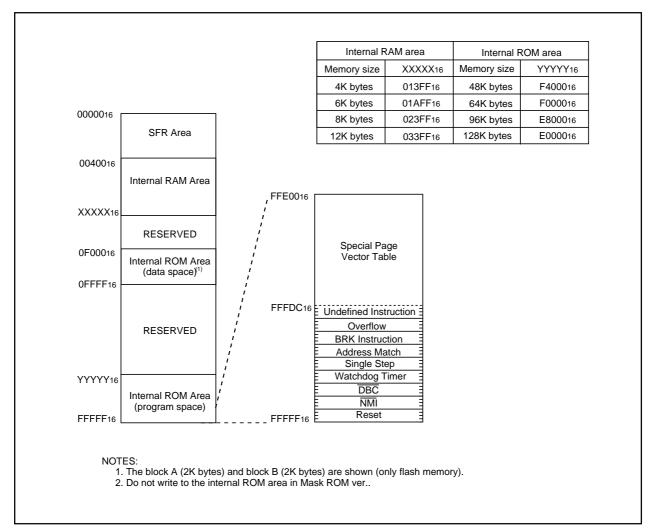


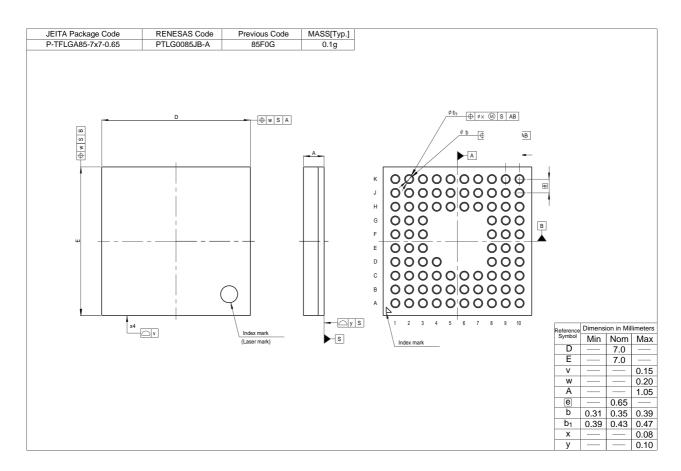
Figure 3.1 Memory Map

Table 4.3 SFR Information(3)<sup>(1)</sup>

| ddress                                   | Register                                                | Symbol   | After Reset |
|------------------------------------------|---------------------------------------------------------|----------|-------------|
|                                          |                                                         |          |             |
|                                          |                                                         |          |             |
| 01B016                                   |                                                         |          |             |
| 01B1 <sub>16</sub>                       |                                                         |          |             |
| 01B2 <sub>16</sub>                       |                                                         |          |             |
| 01B316                                   | Flash memory control register 4 (2)                     | FMR4     | 010000002   |
| 01B4 <sub>16</sub><br>01B5 <sub>16</sub> | Flash memory control register 1 (2)                     | FMR1     | 00000000    |
| 01B516<br>01B616                         | Flash memory control register 1 (2)                     | FMRT     | 000XXX0X2   |
| 01B716                                   | Flash memory control register 0 (2)                     | FMR0     | 000000012   |
| 01B8 <sub>16</sub>                       | Tradit montery control register of                      | 1 1/1/10 | 000000012   |
| 01B916                                   |                                                         |          |             |
|                                          |                                                         |          |             |
| :                                        |                                                         |          |             |
| 0040                                     |                                                         | 1,000    | V0000004-   |
| 021016                                   | Low-power Consumption Control 0                         | LPCC0    | X00000012   |
| 021116                                   |                                                         |          |             |
| 021216                                   |                                                         |          |             |
| 021416                                   |                                                         |          |             |
| 021516                                   |                                                         |          |             |
| 021616                                   |                                                         |          |             |
| 021716                                   |                                                         |          |             |
| 021816                                   |                                                         |          |             |
| 021916                                   |                                                         |          |             |
|                                          |                                                         |          |             |
| :                                        |                                                         |          |             |
| 025016                                   |                                                         |          |             |
| 025116                                   |                                                         |          |             |
| 025216                                   |                                                         |          |             |
| 025316                                   |                                                         |          |             |
| 025416                                   |                                                         |          |             |
| 025516                                   |                                                         |          |             |
| 025616                                   |                                                         |          |             |
| 025716                                   |                                                         |          |             |
| 0258 <sub>16</sub>                       |                                                         |          |             |
| 025A16                                   |                                                         |          |             |
| 025B <sub>16</sub>                       |                                                         |          |             |
| 025C <sub>16</sub>                       | On-chip oscillator control register                     | ROCR     | X00001012   |
| 025D16                                   | Pin assignment control register                         | PACR     | 0016        |
| 025E16                                   | Peripheral clock select register                        | PCLKR    | 000000112   |
| 025F16                                   | Low-power Consumption Control 1                         | LPCC1    | 0016        |
|                                          |                                                         |          |             |
| :                                        |                                                         |          |             |
| 02E016                                   | I2C0 data shift register                                | 200      | XX16        |
| 02E016<br>02E116                         | I <sup>2</sup> C0 data shift register                   | S00      | XX16        |
| 02E116                                   | I <sup>2</sup> C0 address register                      | S0D0     | 0016        |
| 02E316                                   | I <sup>2</sup> C0 control register 0                    | S1D0     | 0016        |
| 02E416                                   | I <sup>2</sup> C0 clock control register                | \$20     | 0016        |
| 02E516                                   | I <sup>2</sup> C0 start/stop condition control register | S2D0     | 000110102   |
| 02E616                                   | I <sup>2</sup> C0 control register 1                    | S3D0     | 001100002   |
| 02E716                                   | I <sup>2</sup> C0 control register 2                    | S4D0     | 0016        |
| 02E816                                   | I <sup>2</sup> C0 status register                       | S10      | 0001000X2   |
| 02E916                                   |                                                         |          |             |
| 02EA <sub>16</sub>                       |                                                         |          |             |
|                                          |                                                         |          |             |
| :                                        |                                                         |          |             |
| 02FE <sub>16</sub>                       |                                                         |          |             |
|                                          |                                                         |          |             |

Note 1:The blank spaces are reserved. No access is allowed. Note 2:This register is included in the flash memory version.

X : Undefined



| REVISION HISTORY | M16C/28 Group (M16C/28, M16C/28B) Shortsheet |
|------------------|----------------------------------------------|
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|------|----------|------|---------------|--|--|
|      |          | Page |               |  |  |
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