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What is "[Embedded - Microcontrollers](#)"?

"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	LVD, POR, PWM, WDT
Number of I/O	29
Program Memory Size	20KB (20K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	1K x 8
Voltage - Supply (Vcc/Vdd)	1.8V ~ 5.5V
Data Converters	A/D 8x8/10b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	-
Package / Case	-
Supplier Device Package	-
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f634k-chip32

Consumer and Industrial MCU Family



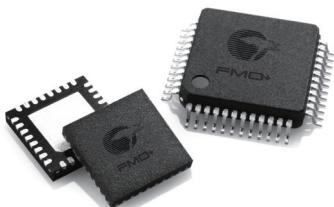
FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



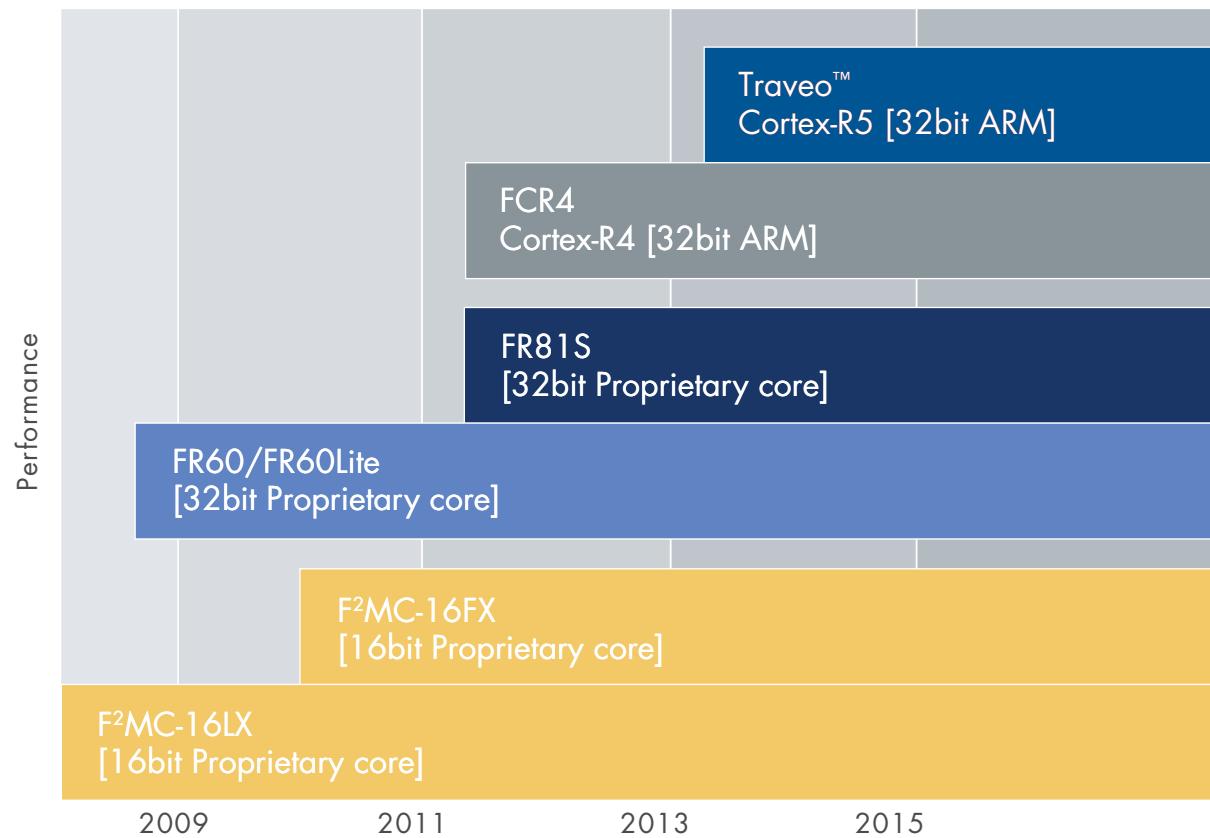
FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

Automotive MCU Core Roadmap



Automotive MCU Family

TRAVEO FAMILY

The Cypress Traveo™ family expands the company's automotive application coverage, scalability and high performance into one line-up and at the same time adds new features to fulfill the latest requirements of the automotive industry. Based on the powerful ARM® Cortex®-R5 and R5F core in single and dual core operations, it offers state-of-the-art real-time performance, safety and security features.

The family supports the latest in-car networks and offers high performance graphics engines optimized for a minimum memory footprint and embeds dedicated features to increase data security in the car.

High-level Traveo Features

- ARM Cortex-R5 core
- Dual core
- Embedded twin-motor control with internal R/D converter
- Embedded 2D and 3D graphics engines
- High performance embedded flash memory
- Cypress HyperBus™: High speed serial interfaces to connect external memory
- Qualified for automotive use (AEC-Q100)
- Software support for Autosar, graphics drivers and more

FR FAMILY

Cypress 32bit MCU families have been designed in close co-operation with major automotive customers worldwide and inherit the high-performance core of Cypress's proprietary FR MCU architecture. They support communication interfaces such as CAN, FlexRay and LIN as well as up to 2MB on-chip memory capacity. The latest members (MB915xx) also include a single precision Floating Point Unit (FPU) providing additional computing power required by complex control algorithms.

This high computing performance combined with powerful peripheral functions such as on-chip graphics controller and motor control macros, offers a higher grade of flexibility and lower cost for automotive as well as industrial applications.

Many devices offer an external bus interface which can be connected to Cypress's stand alone FlexRay controller or to the latest generation of graphics controllers in order to build full-featured dashboards, driver information systems or advanced body systems.

FCR4 FAMILY

The FCR4 family has been specifically designed to offer an innovative, scalable solution for hybrid instrument clusters, which combine traditional meters and graphical displays. The devices offer a powerful architecture based on the ARM Cortex-R4 core and Cypress's 2D graphics engine.

High-level FCR4 Features

- ARM Cortex-R4 core
- Embedded 2D graphics engine
- 2MB flash, 64KB E2Flash
- Up to 208KB RAM
- Features including real-time clock/auto calibration, sound generator and I2S

F2MC-16FX FAMILY

Cypress 16-bit flexible microcontroller series offers a scalable family concept approach to a variety of automotive and industrial applications. The scalable flash/ROM/RAM sizes with different mixtures of peripherals saves development time and costs. CAN and LIN support, on-chip LCD controller, SMC (stepper motor controller), I²C bus interface, analog input channels, external bus interface, selectable port levels for CMOS, TTL and Automotive Levels are some of the enhanced features. A security feature is incorporated, preventing unauthorized reading of the contents of the flash memory.



FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC(V)	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbytes]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase Inverter	Note	Evaluation Device
HIGH-PERFORMANCE GROUP																																				
MB9BD10T	MB9BFD16T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	2	2ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Ethernet-MAC x 2ch	On-chip Debug (SWJ-DP/ETM)											
	MB9BFD16S		LQFP-144										122	24(3)																						
	MB9BFD17T		LQFP-176 BGA-192										154	32(3)																						
	MB9BFD17S		LQFP-144										122	24(3)																						
	MB9BFD18T		LQFP-176 BGA-192										154	32(3)																						
	MB9BFD18S		LQFP-144										122	24(3)																						
MB9B610T	MB9BF616T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Ethernet-MAC x 2ch	On-chip Debug (SWJ-DP/ETM)											
	MB9BF616S		LQFP-144										122	24(3)																						
	MB9BF617T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF617S		LQFP-144										122	24(3)																						
	MB9BF618T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF618S		LQFP-144										122	24(3)																						
MB9B510T	MB9BF516T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Ethernet-MAC x 2ch	On-chip Debug (SWJ-DP/ETM)											
	MB9BF516S		LQFP-144										122	24(3)																						
	MB9BF517T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF517S		LQFP-144										122	24(3)																						
	MB9BF518T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF518S		LQFP-144										122	24(3)																						
MB9B410T	MB9BF416T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	2	2ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF416S		LQFP-144										122	24(3)																						
	MB9BF417T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF417S		LQFP-144										122	24(3)																						
	MB9BF418T		LQFP-176 BGA-192										154	32(3)																						
MB9B310T	MB9BF316T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF316S		LQFP-144										122	24(3)																						
	MB9BF317T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF317S		LQFP-144										122	24(3)																						
	MB9BF318T		LQFP-176 BGA-192										154	32(3)																						
	MB9BF318S		LQFP-144										122	24(3)																						
MB9B210T	MB9BF216T	144	LQFP-176 BGA-192	2.7 to 5.5	✓	FLASH	512K	64K	-	8	32	✓	154	32(3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 3	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Ethernet-MAC x 1ch	On-chip Debug (SWJ-DP/ETM)											
	MB9BF216S		LQFP-144										122	24(3)																						
	MB9BF217T		LQFP-176 BGA-192																																	

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/(unit)]	12bit AD Converter [bit/(unit)]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320T	MB9BF328S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 16ch (UART/CSIO/I ² C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF328T*												122	154	-	24(2)	10bit x 2																		
	MB9BF329S*		LQFP-144	1.5M +64K	192K								122	122	-	24(2)	10bit x 2																		
	MB9BF329T*		LQFP-176 BGA-192										154	154	-	24(2)	10bit x 2																		
MB9B120T	MB9BF128S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 16ch (UART/CSIO/I ² C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF128T*												122	154	-	24(2)	10bit x 2																		
	MB9BF129S*		LQFP-144	1.5M +64K	192K								122	122	-	24(2)	10bit x 2																		
	MB9BF129T*		LQFP-176 BGA-192										154	154	-	24(2)	10bit x 2																		
MB9A420L	MB9AF421K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I ² C/LIN Selectable)	1	-	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)						
	MB9AF421L		LQFP-64 QFN-64								19		51	-	8(1)	10bit x 1																			
MB9A120L	MB9AF121K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I ² C/LIN Selectable)	1	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)						
	MB9AF121L		LQFP-64 QFN-64								19		51	-	8(1)	10bit x 1																			

* In development; **Planning

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CQ} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																	On-chip Debug (SWJ-DP)					
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						

8FX – 8bit Microcontrollers

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC/Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
MB9D560	MB9DF564MA	200	TEQFP -208	1.1 to 1.3	Main Flash +Work Flash	(640K + 64K) x2	64K x2		16	8	-	(1152K + 64K) x2	128K x2	125	32(1)	8(2)	10bit x 2	32bitFree-Run Timer x 5ch 32bitInput Capture x 3unit 6ch 16bitFree-Run Timer x 20ch 16bit Input Capture x 8unit 15ch 16bitOutput Compare x 12unit 24ch Waveform Generator x 4unit 24ch	16bit Base Timer x 12ch (PWM/PPG/Reload/PWC Selectable)	4	-	Multi Function Serial x 5ch (LIN/UART/SIO Selectable)	3	-	-	-	-	✓	ARM Cortex-R5 CAN: 64msb, RDC x 2unit, Motor vector accelerato r x 2unit Models with A suffix on part number have no built-in FlexRay. Models with G suffix on part number have built-in FlexRay.	On-Chip Debug						
	MB9DF564MG					(896K + 64K) x2	96K x2																													
	MB9DF565MA					(1152K + 64K) x2	128K x2																													
	MB9DF565MG					(640K + 64K) x2	64K x 2																													
	MB9DF566MA					(896K + 64K) x2	96K x 2																													
	MB9DF566MG					(1152K + 64K) x2	128K x 2																													
	MB9DF564ML					(640K + 64K) x 2	64K x 2																													
	MB9DF564MQ					(896K + 64K) x 2	96K x 2																													
	MB9DF565ML					(1152K + 64K) x 2	128K x 2																													
	MB9DF565MQ					(640K + 64K) x 2	64K x 2																													
	MB9DF566ML		TEQFP -176			(896K + 64K) x 2	96K x 2																													
	MB9DF566MQ					(1152K + 64K) x 2	128K x 2																													
	MB9DF564LA					(640K + 64K) x 2	64K x 2																													
	MB9DF564LG					(896K + 64K) x 2	96K x 2																													
	MB9DF565LA					(1152K + 64K) x 2	128K x 2																													
	MB9DF565LG					(640K + 64K) x 2	64K x 2																													
	MB9DF566LA					(896K + 64K) x 2	96K x 2																													
	MB9DF566LG					(1152K + 64K) x 2	128K x 2																													
	MB9DF564LL					(896K + 64K) x 2	96K x 2																													
	MB9DF564LQ					(896K + 64K) x 2	96K x 2																													
	MB9DF565LL					(1152K + 64K) x 2	128K x 2																													
	MB9DF565LQ					(640K + 64K) x 2	64K x 2																													
	MB9DF566LL					(896K + 64K) x 2	96K x 2																													
	MB9DF566LQ					(1152K + 64K) x 2	128K x 2																													

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3110	S6J3118HA	96	TEQFP -144	4.5 to 5.25	Main Flash + Work Flash	Instruction: 16 Data: 16	576K + 48K 832K + 48K 1088K + 48K 1600K + 112K 2112K + 256K 3136K + 112K 4160K + 112K 1600K + 112K 2112K + 256K 3136K + 112K 4160K + 112K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB TC-RAM: 48KB System - RAM: 16KB Backup - RAM: 8KB TC-RAM: 64KB System - RAM: 16KB Backup - RAM: 8KB 192K 256K 320K 64KB 192K 256K 320K 64KB	56(2) 116 - - - - 64(2) 150	16 16 - - - - 12 6 12 - RTC x 1ch	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	Multi Function Serial x 4ch (LIN/UART/SIO Selectable)	CAN - FD x 1ch	- - - - - - - - - -	ARM Cortex-R5, SHE(Secure Hardware Extension), On-Chip Debug	ARM Cortex-R5, SHE(Secure Hardware Extension),																				
	S6J3119HA																																			
	S6J311AHA																																			
	S6J311BHA																																			
	S6J311CHA																																			
	S6J311DHA																																			
	S6J311EHA																																			
	S6J311BJA																																			
	S6J311CJA																																			
	S6J311DJA																																			
	S6J311EJA																																			

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C][ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3120	S6J3128HA	112	TEQFP -144	4.5 to 5.25	-	Main Flash +Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	16	16	A24/D16	112	50(2)	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 10ch (LIN/UART/SIO Selectable)	CAN - FD x 3ch	-	-	32 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 4ch, Sound generator x 3ch	On-Chip Debug						
	S6J3129HA																																			
	S6J312AHA																																			
S6J3200	S6J32AAKS	160	TEQFP -208	1.1 to 1.3 3.0 to 3.6 4.5 to 5.5	✓	Main Flash +Work Flash	1088K + 112K	TC-RAM: 64KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 1024KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	30 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 1ch	On-Chip Debug							
	S6J32AAKU																																			
	S6J32AALS																																			
	S6J32AALU																																			
	S6J32BAKS																																			
	S6J32BAKU																																			
	S6J32BALS																																			
	S6J32BALU																																			
	S6J32CAKS																																			
	S6J32CAKU																																			
	S6J32CALS																																			
	S6J32CALU																																			
	S6J32DAKS																																			
	S6J32DAKU																																			
	S6J32DALS																																			
	S6J32DALU																																			

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch (MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

FR Family – 32bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device					
AUTOMOTIVE																																			
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F673A						64.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675A						128.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675R						256.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683A						64.5K +32K	10K	16K	-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683R						128.5K +32K	10K		-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F685A						256.5K +32K	10K		-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695R																																		



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