

Welcome to [E-XFL.COM](#)

What is "[Embedded - Microcontrollers](#)"?

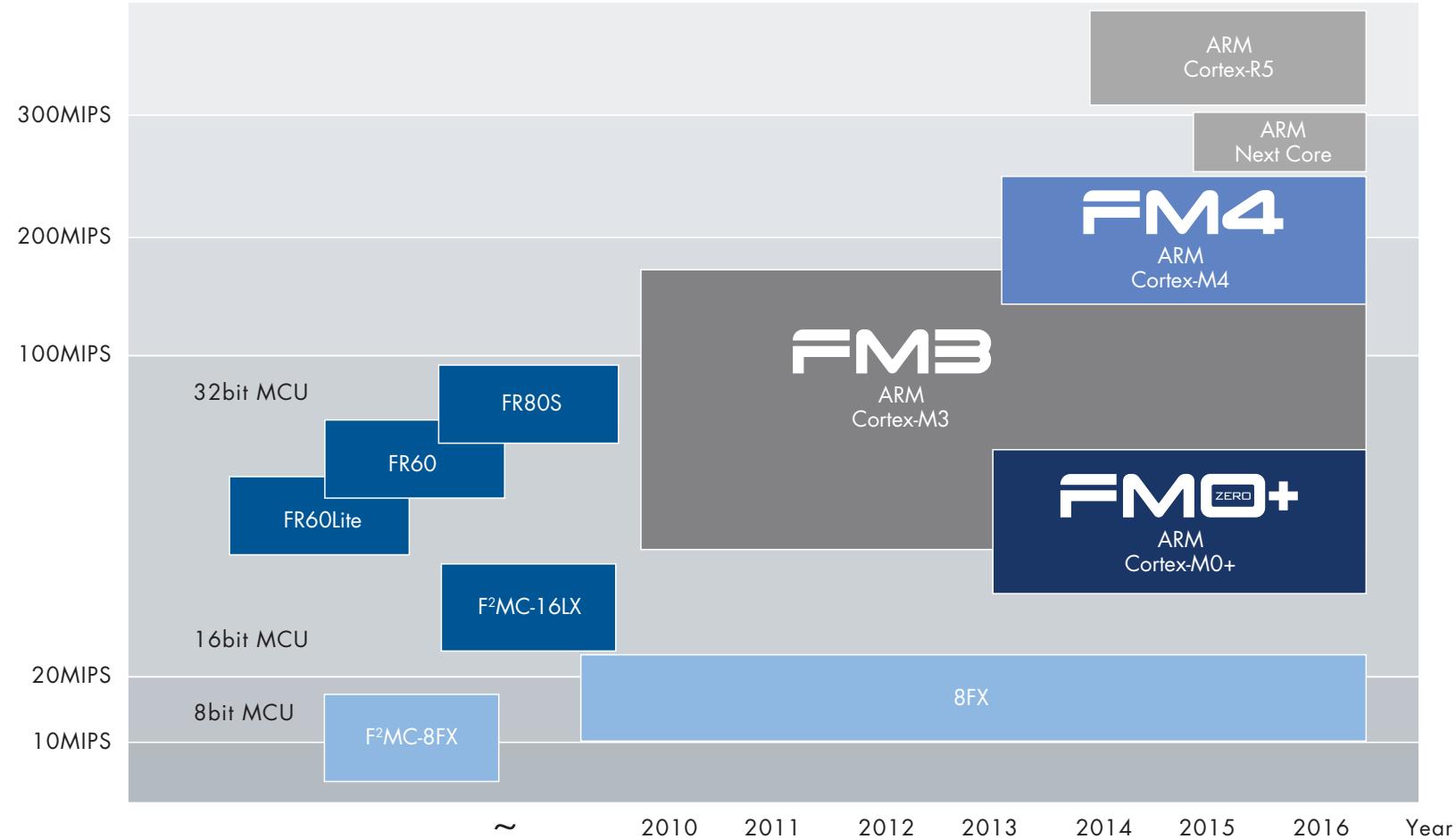
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	LVD, POR, PWM, WDT
Number of I/O	29
Program Memory Size	20KB (20K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	1K x 8
Voltage - Supply (Vcc/Vdd)	2.4V ~ 5.5V
Data Converters	A/D 8x8/10b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	32-LQFP
Supplier Device Package	32-LQFP (7x7)
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f634knpmc-g-101-sne2

Consumer and Industrial MCU Core Roadmap



Consumer and Industrial MCU Family



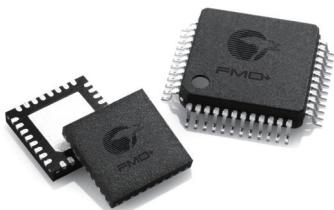
FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbytes]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
S6E2HG	S6E2HG6E0A	160	LQFP-80										63	16 (3)																						
	S6E2HG6F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																				Dual Timer, Real Timer Clock, Unique ID, DSTC x 256ch, SDC	On-Chip Debug (SWJ-DP/ETM)		
	S6E2HG6G0A		LQFP-120	BGA-121									100																							
	S6E2HG4E0A	160	LQFP-80										63	16 (3)																						
	S6E2HG4F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																							
	S6E2HG4G0A		LQFP-120	BGA-121									100																							
S6E2HE	S6E2HE6E0A	160	LQFP-80										63	16 (3)																						
	S6E2HE6F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																							
	S6E2HE6G0A		LQFP-120	BGA-121									100																							
	S6E2HE4E0A	160	LQFP-80										63	16 (3)																						
	S6E2HE4F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																							
	S6E2HE4G0A		LQFP-120	BGA-121									100																							
S6E2H4	S6E2H46E0A	160	LQFP-80										63	16 (3)																						
	S6E2H46F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																							
	S6E2H46G0A		LQFP-120	BGA-121									100																							
	S6E2H44E0A	160	LQFP-80										63	16 (3)																						
	S6E2H44F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																							
	S6E2H44G0A		LQFP-120	BGA-121									100																							
S6E2H1	S6E2H16E0A	160	LQFP-80										63	16 (3)																						
	S6E2H16F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																							
	S6E2H16G0A		LQFP-120	BGA-121									100																							
	S6E2H14E0A	160	LQFP-80										63	16 (3)																						
	S6E2H14F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																							
	S6E2H14G0A		LQFP-120	BGA-121									100																							
S6E2GM w/Security	S6E2GM8HHA	180	LQFP-144										121	24 (3)																						
	S6E2GM8JHA		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																					
	S6E2GM6HHA		LQFP-144										121	24 (3)																						
	S6E2GM6JHA		LQFP-176										153	32 (3)																						
S6E2GM	S6E2GM8H0A	180	LQFP-144										121	24 (3)																						
	S6E2GM8J0A		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																					
	S6E2GM6H0A		LQFP-144										121	24 (3)																						
	S6E2GM6J0A		LQFP-176										153	32 (3)																						
S6E2GK w/Security	S6E2GK8HHA	180	LQFP-144										121	24 (3)																						
	S6E2GK8JHA		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																					
	S6E2GK6HHA		LQFP-144										121	24 (3)																						
	S6E2GK6JHA		LQFP-176										153	32 (3)																						
S6E2GK	S6E2GK8H0A	180	LQFP-144										121	24 (3)																						
	S6E2GK8J0A		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																					
	S6E2GK6H0A		LQFP-144										121	24 (3)																						
	S6E2GK6J0A		LQFP-176										153	32 (3)																						

* In development; **Planning

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	Communication	LD Controller [seg x com]	Three-phase inverter	Note	Evaluation Device
S6E2GH	S6E2GH8H0A	180	LQFP-144	2.7 to 5.5	✓	Main Flash	1024K	192K	-	8	32	✓	121 153 121 153	-	24 (3) 32 (3) 24 (3) 32 (3)	Multi-Function Timer x2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 6ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	Multi Function Serial x 10ch (UART/CSIO/I²C/LIN Selectable)	1	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTCx256ch, Tiny I2S, Smart Card I/F, SDC	On-Chip Debug (SWJ-DP/ ETM)													
	S6E2GH8J0A		LQFP-176																																			
	S6E2GH6H0A		LQFP-144																																			
	S6E2GH6J0A		LQFP-176																																			
S6E2G3	S6E2G38H0A	180	LQFP-144	2.7 to 5.5	✓	Main Flash	1024K	192K	-	8	32	✓	121 153 121 153	-	24 (3) 32 (3) 24 (3) 32 (3)	Multi-Function Timer x2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 6ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	Multi Function Serial x 10ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTCx256ch, Tiny I2S, Smart Card I/F	On-Chip Debug (SWJ-DP/ ETM)													
	S6E2G38J0A		LQFP-176																																			
	S6E2G36H0A		LQFP-144																																			
	S6E2G36J0A		LQFP-176																																			
S6E2G2 w/security	S6E2G28HHA	180	LQFP-144	2.7 to 5.5	✓	Main Flash	1024K	192K	-	8	32	✓	121 153 121 153	-	24 (3) 32 (3) 24 (3) 32 (3)	Multi-Function Timer x2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 6ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	Multi Function Serial x 10ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTCx256ch, Tiny I2S, Smart Card I/F, ETH, Chipher	On-Chip Debug (SWJ-DP/ ETM)													
	S6E2G28JHA		LQFP-176																																			
	S6E2G26HHA		LQFP-144																																			
	S6E2G26JHA		LQFP-176																																			
S6E2G2	S6E2G28H0A	180	LQFP-144	2.7 to 5.5	✓	Main Flash	1024K	192K	-	8	32	✓	121 153 121 153	-	24 (3) 32 (3) 24 (3) 32 (3)	Multi-Function Timer x2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 6ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	Multi Function Serial x 10ch (UART/CSIO/I²C/LIN Selectable)	-	2ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTCx256ch, Tiny I2S, Smart Card I/F, ETH, Chipher	On-Chip Debug (SWJ-DP/ ETM)													
	S6E2G28J0A		LQFP-176																																			
	S6E2G26H0A		LQFP-144																																			
	S6E2G26J0A		LQFP-176																																			
S6E2DH	S6E2DH5G0A	160	LQFP-120 TQFP-120 BGA-161	2.7-3.6	✓	Main Flash	384K	36K	-	8	16	✓	98 90 154	-	24 (3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/Input Capture 4ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG6ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	Dual Timer, Real Timer Clock, Unique ID, DSTC x128ch, CAN-FD 1ch, SDC, I2S 2ch, High Speed Quad SPI x1, Hyper Bus x1, GDC	On-Chip Debug (SWJ-DP/ETM)													
	S6E2DH5GJA		LQFP-120 (SIP)																																			
	S6E2DH5J0A		LQFP-176																																			
S6E2DF	S6E2DF5G0A	160	LQFP-120 TQFP-120 BGA-161	2.7-3.6	✓	Main Flash	384K	36K	-	8	16	✓	98 90 154	-	24 (3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/Input Capture 4ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG6ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	Dual Timer, Real Timer Clock, Unique ID, DSTC x128ch, CAN-FD 1ch, I2S 2ch, High Speed Quad SPI x1, Hyper Bus x1, GDC	On-Chip Debug (SWJ-DP/ETM)													
	S6E2DF5GJA		LQFP-120 (SIP)																																			
	S6E2DF5J0A		LQFP-176																																			
S6E2D5	S6E2D55G0A	160	LQFP-120 TQFP-120 BGA-161	2.7-3.6	✓	Main Flash	384K	36K	-	8	16	✓	98 90 154	-	24 (3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/Input Capture 4ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG6ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	Dual Timer, Real Timer Clock, Unique ID, DSTC x128ch, I2S 2ch, High Speed Quad SPI x1, Hyper Bus x1, GDC	On-Chip Debug (SWJ-DP/ETM)													
	S6E2D55GJA		LQFP-120 (SIP)																																			
	S6E2D55J0A		LQFP-176																																			
S6E2D3	S6E2D35G0A	160	LQFP-120 TQFP-120 BGA-161	2.7-3.6	✓	Main Flash	384K	36K	-	8	16	✓	98 90 154	-	24 (3)	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/Input Capture 4ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG6ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	Dual Timer, Real Timer Clock, Unique ID, DSTC x128ch, I2S 2ch, High Speed Quad SPI x1, Hyper Bus x1, GDC	On-Chip Debug (SWJ-DP/ETM)													
	S6E2D35GJA		LQFP-120 (SIP)																																			
	S6E2D35J0A		LQFP-176																																			

FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
S6E2C1-E	S6E2C10H2A	200	LQFP-144	2.7 to 5.5	✓	-	0M	256K	-	8	32	✓	120	-	24 (3)	12 x 2	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 9ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTC x 256ch, SDC, I2S 1ch(without 144pin), HDMI-CEC 2ch, High Speed Quad SPI x1(without 144pin), Programmable CRC, Chipher	On-Chip Debug (SWJ-DP/ETM/ HTM)								
	S6E2C10J2A		LQFP-176 BGA-192																																	
	S6E2C10L2A		LQFP-216																																	
MB9B560R	MB9BF566M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF566N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF566R		LQFP-120 BGA-144																																	
	MB9BF567M		LQFP-80																																	
	MB9BF567N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF567R		LQFP-120 BGA-144																																	
	MB9BF568M		LQFP-80																																	
	MB9BF568N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF568R		LQFP-120 BGA-144																																	
MB9B460R	MB9BF466M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF466N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF466R		LQFP-120 BGA-144																																	
	MB9BF467M		LQFP-80																																	
	MB9BF467N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF467R		LQFP-120 BGA-144																																	
	MB9BF468M	160	LQFP-80	160	✓	Main Flash +Work Flash	1024K +32K	128K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF468N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF468R		LQFP-120 BGA-144																																	
MB9B360R	MB9BF366M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF366N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF366R		LQFP-120 BGA-144																																	
	MB9BF367M		LQFP-80																																	
	MB9BF367N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF367R		LQFP-120 BGA-144																																	
	MB9BF368M		LQFP-80																																	
	MB9BF368N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF368R		LQFP-120 BGA-144																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM4 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VDD [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit ADC Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320M	MB9BF321K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF321L																																		
	MB9BF321M																																		
	MB9BF322K																																		
	MB9BF322L																																		
	MB9BF322M																																		
	MB9BF324K																																		
	MB9BF324L																																		
	MB9BF324M																																		
MB9B120M	MB9BF121K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF121L																																		
	MB9BF121M																																		
	MB9BF122K																																		
	MB9BF122L																																		
	MB9BF122M																																		
	MB9BF124K																																		
	MB9BF124L																																		
	MB9BF124M																																		
MB9B120J	MB9BF121J	72	LQFP-32 QFN-32	2.7 to 5.5	✓	FLASH	64K	8K	-	4	7	-	23	-	8(1)	-	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	-	✓	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SW-DP)						
MB9B520T	MB9BF528S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF528T*																																		
	MB9BF529S*																																		
	MB9BF529T*																																		
MB9B420T	MB9BF428S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF428T*																																		
	MB9BF429S*																																		
	MB9BF429T*																																		

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer			Serial			Communication			Note	Evaluation Device
BASIC GROUP																											
MB9A110A	MB9AF111LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	Dual Timer	On-chip Debug (SWJ-DP)
	MB9AF111MA		LQFP-80																								
	MB9AF111NA		LQFP-100 QFP-100 BGA-112																								
	MB9AF112LA		LQFP-64 QFN-64																								
	MB9AF112MA		LQFP-80																								
	MB9AF112NA		LQFP-100 QFP-100 BGA-112																								
	MB9AF114LA		LQFP-64 QFN-64																								
	MB9AF114MA		LQFP-80																								
	MB9AF114NA		LQFP-100 QFP-100 BGA-112																								
	MB9AF115MA		LQFP-80																								
	MB9AF115NA		LQFP-100 QFP-100 BGA-112**																								
	MB9AF116MA		LQFP-80																								
	MB9AF116NA		LQFP-100 QFP-100 BGA-112**																								
MB9A310K	MB9AF311K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock	On-chip Debug (SWJ-DP)	
	MB9AF312K																										
MB9A110K	MB9AF111K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Multi Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	✓	Dual Timer, Real Time Clock	On-chip Debug (SWJ-DP)

* In development; **Planning

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																		On-chip Debug (SWJ-DP)				
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						

8FX – 8bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC/V	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
STANDARD																																				
MB95650L	MB95F652E	16	TSSOP-24 SOP-24 QFN-32	1.8 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	8K	256					21																							
	MB95F652L						12K	512	-	-	6	-	20																							
	MB95F653E						20K	1024					20																							
	MB95F653L						36K						21																							
	MB95F654E												20																							
	MB95F654L												21																							
	MB95F656E												20																							
	MB95F656L																																			
MB95810K	MB95F814K	16	LQFP-64	2.88 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	20K	512					12(1)																							
	MB95F816K						36K	1K	-	-	12	-	58																							
	MB95F818K						60K	2K	-	-																										

* In development; **Planning

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3110	S6J3118HA	96	TEQFP -144	4.5 to 5.25	Main Flash + Work Flash	Instruction: 16 Data: 16	576K + 48K 832K + 48K 1088K + 48K 1600K + 112K 2112K + 256K 3136K + 112K 4160K + 112K 1600K + 112K 2112K + 256K 3136K + 112K 4160K + 112K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB TC-RAM: 48KB System - RAM: 16KB Backup - RAM: 8KB TC-RAM: 64KB System - RAM: 16KB Backup - RAM: 8KB 192K 256K 320K 64KB 192K 256K 320K 64KB	56(2) 116 - - - - 64(2) 150	16 16 - - - - 12 6 12 - RTC x 1ch	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	Multi Function Serial x 4ch (LIN/UART/SIO Selectable)	CAN - FD x 1ch	- - - - - - - - - -	ARM Cortex-R5, SHE(Secure Hardware Extension), On-Chip Debug	ARM Cortex-R5, SHE(Secure Hardware Extension),																				
	S6J3119HA																																			
	S6J311AHA																																			
	S6J311BHA																																			
	S6J311CHA																																			
	S6J311DHA																																			
	S6J311EHA																																			
	S6J311BJA																																			
	S6J311CJA																																			
	S6J311DJA																																			
	S6J311EJA																																			

Traveo Family – 32bit Microcontrollers

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch (MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

FR Family – 32bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device			
AUTOMOTIVE																																	
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F673A						64.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug	
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	128.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F675A						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F675R						384.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F683A						64.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug	
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F685A						256.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug	
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1			
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1			
	MB96F695R						384.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-</											



Cypress Semiconductor Corporation

198 Champion Court, San Jose CA 95134

phone +1 408.943.2600 fax +1 408.943.6848

toll free +1 800.858.1810 (U.S. only) Press "1" to reach your local sales representative

© 2015 Cypress Semiconductor Corporation. All rights reserved. All other trademarks are the property of their respective owners.

Doc# 002-06949 Rev.*A