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#### What is "[Embedded - Microcontrollers](#)"?

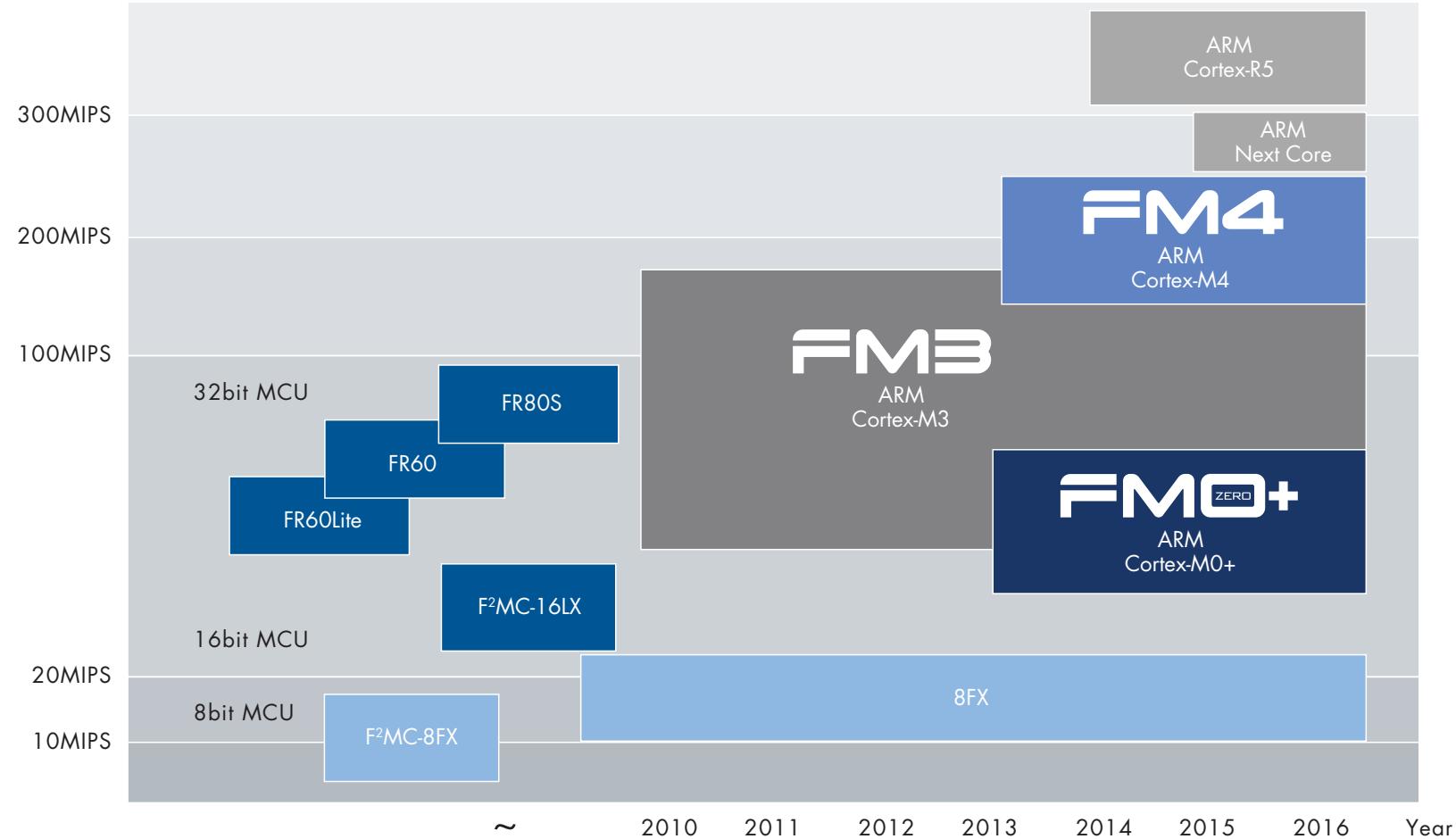
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

#### Applications of "[Embedded - Microcontrollers](#)"

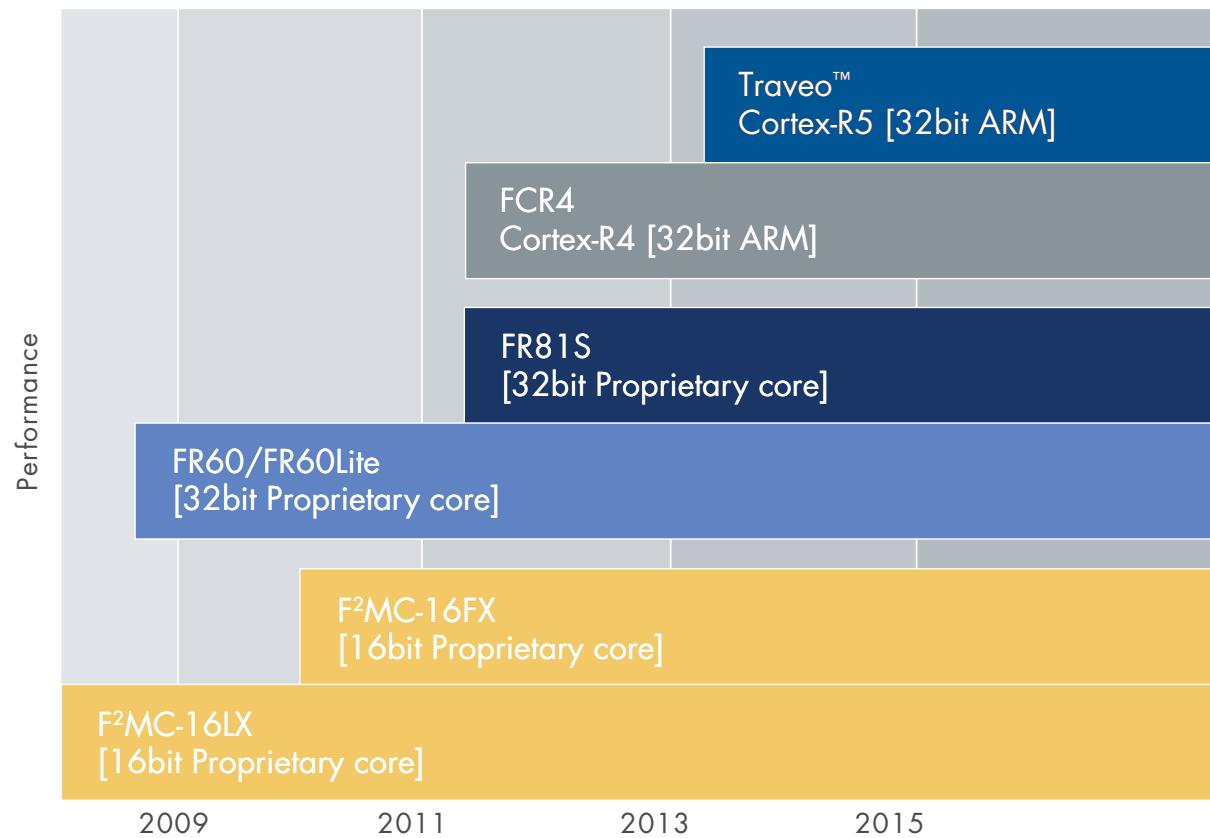
##### Details

Product Status	Obsolete
Core Processor	F <sup>2</sup> MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I <sup>2</sup> C, LINbus, SIO, UART/USART
Peripherals	POR, PWM, WDT
Number of I/O	28
Program Memory Size	36KB (36K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	1K x 8
Voltage - Supply (Vcc/Vdd)	2.4V ~ 5.5V
Data Converters	A/D 8x8/10b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	32-LQFP
Supplier Device Package	32-LQFP (7x7)
Purchase URL	<a href="https://www.e-xfl.com/product-detail/infineon-technologies/mb95f636hnpmc-g-sne2">https://www.e-xfl.com/product-detail/infineon-technologies/mb95f636hnpmc-g-sne2</a>

## Consumer and Industrial MCU Core Roadmap



## Automotive MCU Core Roadmap











## FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device					
MB9BF160L	MB9BF164K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	15(2)	8(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I <sup>2</sup> C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)												
	MB9BF164L		LQFP-64 QFN-64																																						
	MB9BF165K		LQFP-48 QFN-48																																						
	MB9BF165L		LQFP-64 QFN-64																																						
	MB9BF166K		LQFP-48 QFN-48				512K +32K	64K	15	-	16	16	33	48	8(2)	15(2)	8(2)	15(2)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I <sup>2</sup> C/LIN Selectable)	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
	MB9BF166L		LQFP-64 QFN-64																																						





## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/(unit)]	12bit AD Converter [bit/(unit)]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320T	MB9BF328S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- 1ch (USB-Host/ USB-Function Selectable)	- ✓	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF328T*												122	154	-	24(2)	10bit x 2																		
	MB9BF329S*		LQFP-144	1.5M +64K	192K								122	122	-																				
	MB9BF329T*		LQFP-176 BGA-192	154	-								-																						
MB9B120T	MB9BF128S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2																		
	MB9BF128T*												122	154	-	24(2)	10bit x 2																		
	MB9BF129S*		LQFP-144	1.5M +64K	192K								122	-	-																				
	MB9BF129T*		LQFP-176 BGA-192	154	-								-																						
MB9A420L	MB9AF421K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	36	-	8(1)	10bit x 1		Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	1 - - - - ✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)											
	MB9AF421L										19		51	-																					
MB9A120L	MB9AF121K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	36	-	8(1)	10bit x 1		Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- - - - ✓	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)											
	MB9AF121L										19		51	-																					

\* In development; \*\*Planning

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>CQ</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

\* In development; \*\*Planning

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer			Serial			Communication			Note		
BASIC GROUP																												
MB9A110A	MB9AF111LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	-	Dual Timer	On-chip Debug (SWJ-DP/ETM)
	MB9AF111MA		LQFP-80																									
	MB9AF111NA		LQFP-100 QFP-100 BGA-112																									
	MB9AF112LA		LQFP-64 QFN-64																									
	MB9AF112MA		LQFP-80																									
	MB9AF112NA		LQFP-100 QFP-100 BGA-112																									
	MB9AF114LA		LQFP-64 QFN-64																									
	MB9AF114MA		LQFP-80																									
	MB9AF114NA		LQFP-100 QFP-100 BGA-112																									
	MB9AF115MA		LQFP-80																									
	MB9AF115NA		LQFP-100 QFP-100 BGA-112**																									
	MB9AF116MA		LQFP-80																									
	MB9AF116NA		LQFP-100 QFP-100 BGA-112**																									
MB9A310K	MB9AF311K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock	On-chip Debug (SWJ-DP)		
	MB9AF312K																											
MB9A110K	MB9AF111K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Multi Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	✓	Dual Timer, Real Time Clock	On-chip Debug (SWJ-DP)	

\* In development; \*\*Planning



## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																		On-chip Debug (SWJ-DP)				
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						



## FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VC <sub>G</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DIMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Convertor [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device	
S6E1C3	S6E1C32D0A	40	LQFP-64	1.65 to 3.6	✓	Main Flash	128K	16K			12		54														Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTCx64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C31D0A						64K	12K			9		38																			
	S6E1C32C0A						128K	16K			5		20																			
	S6E1C31C0A		LQFP-32	WLCSP-26			128K	16K			7	-	24																			
	S6E1C32B0A						64K	12K			6		4																			
	S6E1C31B0A						128K	16K			12		54																			
	S6E1C32D0A		QFN-64	QFN-48			64K	12K			9		38																			
	S6E1C31D0A						128K	16K			6		8																			
	S6E1C32C0A						64K	12K			7		24																			
	S6E1C31C0A						128K	16K			6		8																			
	S6E1C32B0A						64K	12K			7		24																			
	S6E1C31B0A						128K	16K			6		8																			
S6E1C1	S6E1C12D0A	40	LQFP-64	1.65 to 3.6	✓	Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTCx64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)			
	S6E1C11D0A						64K	12K			9		38																			
	S6E1C12C0A						128K	16K			6		24																			
	S6E1C11C0A		LQFP-32	WLCSP-26			128K	16K			7		6																			
	S6E1C12B0A						64K	12K			12		54																			
	S6E1C11B0A						128K	16K			9		38																			
	S6E1C12D0A						64K	12K			7		24																			
	S6E1C11D0A		QFN-64	QFN-48			128K	16K			6		8																			
	S6E1C12C0A						64K	12K			9		38																			
	S6E1C11C0A						128K	16K			6		6																			
	S6E1C12B0A						64K	12K			7		24																			
	S6E1C11B0A						128K	16K			6		8																			



## Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3110	S6J3118HA	96	TEQFP -144	4.5 to 5.25	Main Flash +Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	116	56(2)	16	16	-	116	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	RTC x 1ch	Multi Function Serial x 4ch (LIN/UART/SIO Selectable)	CAN - FD x 1ch	-	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),	On-Chip Debug		
	S6J3119HA					832K + 48K	TC-RAM: 48KB System - RAM: 16KB Backup - RAM: 8KB																													
	S6J311AHA					1088K + 48K	TC-RAM: 64KB System - RAM: 16KB Backup - RAM: 8KB																													
	S6J311BHA					1600K + 112K	192K																													
	S6J311CHA					2112K + 112K	256K																													
	S6J311DHA					3136K + 112K	320K																													
	S6J311EHA					4160K + 112K	TC-RAM: 64KB System - RAM: 256KB Backup - RAM: 64KB																													
	S6J311BJA					1600K + 112K	192K																													
	S6J311CJA					2112K + 112K	256K																													
	S6J311DJA					3136K + 112K	320K																													
	S6J311EJA					4160K + 112K	TC-RAM: 64KB System - RAM: 256KB Backup - RAM: 64KB																													



