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What is "[Embedded - Microcontrollers](#)"?

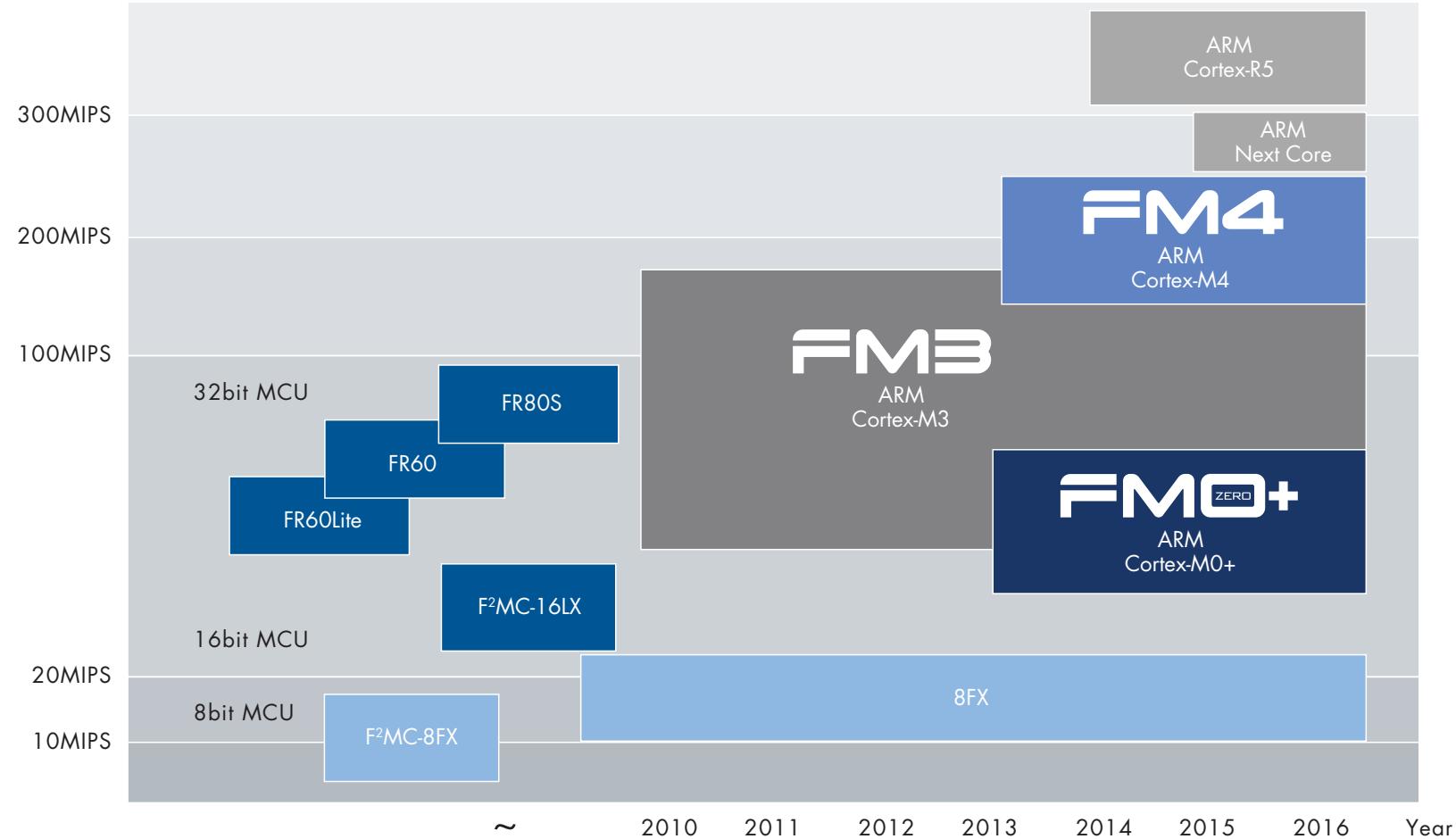
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	POR, PWM, WDT
Number of I/O	28
Program Memory Size	36KB (36K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	1K x 8
Voltage - Supply (Vcc/Vdd)	2.4V ~ 5.5V
Data Converters	A/D 8x8/10b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	32-LQFP
Supplier Device Package	32-LQFP (7x7)
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f636hnpmc-g116snere2

Consumer and Industrial MCU Core Roadmap



Automotive MCU Family

TRAVEO FAMILY

The Cypress Traveo™ family expands the company's automotive application coverage, scalability and high performance into one line-up and at the same time adds new features to fulfill the latest requirements of the automotive industry. Based on the powerful ARM® Cortex®-R5 and R5F core in single and dual core operations, it offers state-of-the-art real-time performance, safety and security features.

The family supports the latest in-car networks and offers high performance graphics engines optimized for a minimum memory footprint and embeds dedicated features to increase data security in the car.

High-level Traveo Features

- ARM Cortex-R5 core
- Dual core
- Embedded twin-motor control with internal R/D converter
- Embedded 2D and 3D graphics engines
- High performance embedded flash memory
- Cypress HyperBus™: High speed serial interfaces to connect external memory
- Qualified for automotive use (AEC-Q100)
- Software support for Autosar, graphics drivers and more

FR FAMILY

Cypress 32bit MCU families have been designed in close co-operation with major automotive customers worldwide and inherit the high-performance core of Cypress's proprietary FR MCU architecture. They support communication interfaces such as CAN, FlexRay and LIN as well as up to 2MB on-chip memory capacity. The latest members (MB915xx) also include a single precision Floating Point Unit (FPU) providing additional computing power required by complex control algorithms.

This high computing performance combined with powerful peripheral functions such as on-chip graphics controller and motor control macros, offers a higher grade of flexibility and lower cost for automotive as well as industrial applications.

Many devices offer an external bus interface which can be connected to Cypress's stand alone FlexRay controller or to the latest generation of graphics controllers in order to build full-featured dashboards, driver information systems or advanced body systems.

FCR4 FAMILY

The FCR4 family has been specifically designed to offer an innovative, scalable solution for hybrid instrument clusters, which combine traditional meters and graphical displays. The devices offer a powerful architecture based on the ARM Cortex-R4 core and Cypress's 2D graphics engine.

High-level FCR4 Features

- ARM Cortex-R4 core
- Embedded 2D graphics engine
- 2MB flash, 64KB E2Flash
- Up to 208KB RAM
- Features including real-time clock/auto calibration, sound generator and I2S

F2MC-16FX FAMILY

Cypress 16-bit flexible microcontroller series offers a scalable family concept approach to a variety of automotive and industrial applications. The scalable flash/ROM/RAM sizes with different mixtures of peripherals saves development time and costs. CAN and LIN support, on-chip LCD controller, SMC (stepper motor controller), I²C bus interface, analog input channels, external bus interface, selectable port levels for CMOS, TTL and Automotive Levels are some of the enhanced features. A security feature is incorporated, preventing unauthorized reading of the contents of the flash memory.



FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)								
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase inverter	Note	Evaluation Device
MB9BF160L	MB9BF164K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	33	8(2)	15(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)										
	MB9BF164L		LQFP-64 QFN-64																																	
	MB9BF165K		LQFP-48 QFN-48																																	
	MB9BF165L		LQFP-64 QFN-64																																	
	MB9BF166K		LQFP-48 QFN-48																																	
	MB9BF166L		LQFP-64 QFN-64																																	

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CQ} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

8FX – 8bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC/V	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
STANDARD																																				
MB95650L	MB95F652E	16	TSSOP-24 SOP-24 QFN-32	1.8 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	8K	256					21																							
	MB95F652L						12K	512	-	-	6	-	20																							
	MB95F653E						20K	1024					20																							
	MB95F653L						36K						21																							
	MB95F654E												20																							
	MB95F654L												21																							
	MB95F656E												20																							
	MB95F656L																																			
MB95810K	MB95F814K	16	LQFP-64	2.88 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	20K	512					12(1)																							
	MB95F816K						36K	1K	-	-	12	-	58																							
	MB95F818K						60K	2K	-	-																										

* In development; **Planning

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3110	S6J3118HA	96	TEQFP -144	4.5 to 5.25	Main Flash +Work Flash	576K + 48K	TC- RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	832K + 48K	TC- RAM: 48KB System - RAM: 16KB Backup - RAM: 8KB	1088K + 48K	TC- RAM: 64KB System - RAM: 16KB Backup - RAM: 8KB	1600K + 112K	192K	Instruction: 16 Data: 16	16	16	-	116	56(2)	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	-	RTC x 1ch	Multi Function Serial x 4ch (LIN/UART/SIO Selectable)	CAN - FD x 1ch	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),	On-Chip Debug	
	S6J3119HA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	64(2)	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),					
	S6J311AHA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311BHA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311CHA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311DHA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311EHA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311BJA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311CJA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311DJA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						
	S6J311EJA					2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	System - RAM: 256KB Backup - RAM: 64KB	1600K + 112K	192K	2112K + 112K	256K	3136K + 112K	320K	4160K + 112K	64KB	150	-	-	-	-	-	-	-	-	ARM Cortex-R5, SHE(Secure Hardware Extension),						



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