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What is "[Embedded - Microcontrollers](#)"?

"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	LVD, POR, PWM, WDT
Number of I/O	21
Program Memory Size	8KB (8K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	256 x 8
Voltage - Supply (Vcc/Vdd)	1.8V ~ 5.5V
Data Converters	A/D 6x8/12b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	24-SOIC (0.295", 7.50mm Width)
Supplier Device Package	24-SOP
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f652enpf-g-sne2

Automotive MCU Family

TRAVEO FAMILY

The Cypress Traveo™ family expands the company's automotive application coverage, scalability and high performance into one line-up and at the same time adds new features to fulfill the latest requirements of the automotive industry. Based on the powerful ARM® Cortex®-R5 and R5F core in single and dual core operations, it offers state-of-the-art real-time performance, safety and security features.

The family supports the latest in-car networks and offers high performance graphics engines optimized for a minimum memory footprint and embeds dedicated features to increase data security in the car.

High-level Traveo Features

- ARM Cortex-R5 core
- Dual core
- Embedded twin-motor control with internal R/D converter
- Embedded 2D and 3D graphics engines
- High performance embedded flash memory
- Cypress HyperBus™: High speed serial interfaces to connect external memory
- Qualified for automotive use (AEC-Q100)
- Software support for Autosar, graphics drivers and more

FR FAMILY

Cypress 32bit MCU families have been designed in close co-operation with major automotive customers worldwide and inherit the high-performance core of Cypress's proprietary FR MCU architecture. They support communication interfaces such as CAN, FlexRay and LIN as well as up to 2MB on-chip memory capacity. The latest members (MB915xx) also include a single precision Floating Point Unit (FPU) providing additional computing power required by complex control algorithms.

This high computing performance combined with powerful peripheral functions such as on-chip graphics controller and motor control macros, offers a higher grade of flexibility and lower cost for automotive as well as industrial applications.

Many devices offer an external bus interface which can be connected to Cypress's stand alone FlexRay controller or to the latest generation of graphics controllers in order to build full-featured dashboards, driver information systems or advanced body systems.

FCR4 FAMILY

The FCR4 family has been specifically designed to offer an innovative, scalable solution for hybrid instrument clusters, which combine traditional meters and graphical displays. The devices offer a powerful architecture based on the ARM Cortex-R4 core and Cypress's 2D graphics engine.

High-level FCR4 Features

- ARM Cortex-R4 core
- Embedded 2D graphics engine
- 2MB flash, 64KB E2Flash
- Up to 208KB RAM
- Features including real-time clock/auto calibration, sound generator and I2S

F2MC-16FX FAMILY

Cypress 16-bit flexible microcontroller series offers a scalable family concept approach to a variety of automotive and industrial applications. The scalable flash/ROM/RAM sizes with different mixtures of peripherals saves development time and costs. CAN and LIN support, on-chip LCD controller, SMC (stepper motor controller), I²C bus interface, analog input channels, external bus interface, selectable port levels for CMOS, TTL and Automotive Levels are some of the enhanced features. A security feature is incorporated, preventing unauthorized reading of the contents of the flash memory.



FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
S6E2C1-E	S6E2C10H2A	200	LQFP-144	2.7 to 5.5	✓	-	0M	256K	-	8	32	✓	120	-	24 (3)	12 x 2	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 9ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTC x 256ch, SDC, I2S 1ch(without 144pin), HDMI-CEC 2ch, High Speed Quad SPI x1(without 144pin), Programmable CRC, Chipher	On-Chip Debug (SWJ-DP/ETM/ HTM)								
	S6E2C10J2A		LQFP-176 BGA-192																																	
	S6E2C10L2A		LQFP-216																																	
MB9B560R	MB9BF566M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF566N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF566R		LQFP-120 BGA-144																																	
	MB9BF567M		LQFP-80																																	
	MB9BF567N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF567R		LQFP-120 BGA-144																																	
	MB9BF568M		LQFP-80																																	
	MB9BF568N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF568R		LQFP-120 BGA-144																																	
MB9B460R	MB9BF466M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF466N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF466R		LQFP-120 BGA-144																																	
	MB9BF467M		LQFP-80																																	
	MB9BF467N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF467R		LQFP-120 BGA-144																																	
	MB9BF468M		LQFP-80																																	
MB9B360R	MB9BF468N	160	LQFP-100 QFP-100 BGA-112	2.7 to 5.5	✓	Main Flash +Work Flash	768K +32K	96K	-	8	16	✓	63	-	16(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF366N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF366R		LQFP-120 BGA-144																																	
	MB9BF367M		LQFP-80																																	
	MB9BF367N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF367R		LQFP-120 BGA-144																																	
	MB9BF368M		LQFP-80																																	
	MB9BF368N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF368R		LQFP-120 BGA-144																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B460L	MB9BF464K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	15(2)	8(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I2C/LIN Selectable)	1	–	–	–	–	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF464L		LQFP-64 QFN-64																																	
	MB9BF465K		LQFP-48 QFN-48																																	
	MB9BF465L		LQFP-64 QFN-64																																	
	MB9BF466K		LQFP-48 QFN-48																																	
	MB9BF466L		LQFP-64 QFN-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)								
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VDD [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit ADC Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320M	MB9BF321K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF321L																																		
	MB9BF321M																																		
	MB9BF322K																																		
	MB9BF322L																																		
	MB9BF322M																																		
	MB9BF324K																																		
	MB9BF324L																																		
	MB9BF324M																																		
MB9B120M	MB9BF121K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF121L																																		
	MB9BF121M																																		
	MB9BF122K																																		
	MB9BF122L																																		
	MB9BF122M																																		
	MB9BF124K																																		
	MB9BF124L																																		
	MB9BF124M																																		
MB9B120J	MB9BF121J	72	LQFP-32 QFN-32	2.7 to 5.5	✓	FLASH	64K	8K	-	4	7	-	23	-	8(1)	-	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	-	✓	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SW-DP)						
MB9B520T	MB9BF528S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF528T*																																		
	MB9BF529S*																																		
	MB9BF529T*																																		
MB9B420T	MB9BF428S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF428T*																																		
	MB9BF429S*																																		
	MB9BF429T*																																		

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/(unit)]	12bit AD Converter [bit/(unit)]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320T	MB9BF328S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I ² C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF328T*																																		
	MB9BF329S*		LQFP-144	LQFP-176 BGA-192	1.5M +64K	192K	-	8	32	✓	122	122	-	24(2)	10bit x 2																				
	MB9BF329T*		LQFP-176 BGA-192																																
MB9B120T	MB9BF128S*	60	LQFP-144	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	✓	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I ² C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF128T*																																		
	MB9BF129S*		LQFP-144	LQFP-176 BGA-192	1.5M +64K	192K	-	8	32	✓	122	122	-	24(2)	10bit x 2																				
	MB9BF129T*		LQFP-176 BGA-192																																
MB9A420L	MB9AF421K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I ² C/LIN Selectable)	1	-	-	-	-	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)						
	MB9AF421L		LQFP-48 LQFP-52 QFN-48																																
MB9A120L	MB9AF121K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	FLASH	64K	4K	-	-	14	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I ² C/LIN Selectable)	-	-	-	-	-	-	✓	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)						
	MB9AF121L		LQFP-64 QFN-64																																

* In development; **Planning

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																	On-chip Debug (SWJ-DP)					
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chnut]	12bit AD Converter [chnut]	DA Convert [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWIC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1A	S6E1A11B0A	40	LQFP-32 QFN-32	2.7 to 5.5	✓	Flash	56K 6K 88K	6K - 2	8 - 37	- - -	23 37 23 37	5(1) 8(1) 5(1) 8(1)	- - - -	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 4ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 1	1	Multi Function Serial x 3ch (UART/CSIO/I ² C/LIN Selectable)	-	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, Fast I/O	On-chip Debug (SW-DP/MTB)										
	S6E1A11C0A		LOFP-48 LOFP-52 QFN-48																																	
	S6E1A12B0A		LOFP-32 QFN-32																																	
	S6E1A12C0A		LOFP-48 LOFP-52 QFN-48																																	
S6E1B8 w/security	S6E1B86GHA	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K 256K + 40K 512K + 40K 256K + 40K 512K + 40K 256K + 40K	64K 32K 64K 32K 64K 32K	- - - - - -	24 - 24 - - -	102 82 82 65 65	24 - 23 - 16 16	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM /44 SEG x 4 COM 32 SEG x 8 COM /36 SEG x 4 COM 20 SEG x 8 COM /24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F, Chipher	On-Chip Debug (SW-JDP/ Boundary SCAN)														
	S6E1B84GHA																																			
	S6E1B86FHA																																			
	S6E1B84FHA																																			
	S6E1B86EHA																																			
	S6E1B84EHA																																			
S6E1B8	S6E1B86G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K 256K + 40K 512K + 40K 256K + 40K 512K + 40K 256K + 40K	64K 32K 64K 32K 64K 32K	- - - - - -	24 - 24 - - -	102 82 82 65 65	24 - 23 - 16 16	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM /44 SEG x 4 COM 32 SEG x 8 COM /36 SEG x 4 COM 20 SEG x 8 COM /24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)														
	S6E1B84G0A																																			
	S6E1B86F0A																																			
	S6E1B84F0A																																			
	S6E1B86E0A																																			
	S6E1B84E0A																																			
S6E1B3	S6E1B36G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K 256K + 40K 512K + 40K 256K + 40K 512K + 40K 256K + 40K	64K 32K 64K 32K 64K 32K	- - - - - -	24 - 24 - - -	102 82 82 65 65	24 - 23 - 16 16	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM /44 SEG x 4 COM 32 SEG x 8 COM /36 SEG x 4 COM 20 SEG x 8 COM /24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)														
	S6E1B34G0A																																			
	S6E1B36F0A																																			
	S6E1B34F0A																																			
	S6E1B36E0A																																			
	S6E1B34E0A																																			
S6E1B1	S6E1B16G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K 256K + 40K 512K + 40K 256K + 40K 512K + 40K 256K + 40K	64K 32K 64K 32K 64K 32K	- - - - - -	24 - 24 - - -	102 82 82 65 65	24 - 23 - 16 16	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM /44 SEG x 4 COM 32 SEG x 8 COM /36 SEG x 4 COM 20 SEG x 8 COM /24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)														
	S6E1B14G0A																																			
	S6E1B16F0A																																			
	S6E1B14F0A																																			
	S6E1B16E0A																																			
	S6E1B14E0A																																			

* In development; **Planning

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DIMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Convertor [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1C3	S6E1C32D0A	40	LQFP-64	1.65 to 3.6	✓	Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C31D0A						64K	12K			9		38																		
	S6E1C32C0A						128K	16K			5		20																		
	S6E1C31C0A		LQFP-32	WLCSP-26			128K	16K			7	-	24																		
	S6E1C32B0A						64K	12K			6		6																		
	S6E1C31B0A						128K	16K			12		54																		
	S6E1C32D0A		QFN-64	QFN-48			64K	12K			9		38																		
	S6E1C31D0A						128K	16K			7		24																		
	S6E1C32C0A						64K	12K			12		54																		
	S6E1C31C0A						128K	16K			9		38																		
	S6E1C32B0A						64K	12K			7		24																		
	S6E1C31B0A						128K	16K			12		54																		
S6E1C1	S6E1C12D0A	40	LQFP-64	1.65 to 3.6		Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C11D0A						64K	12K			9		38																		
	S6E1C12C0A						128K	16K			7		24																		
	S6E1C11C0A		LQFP-32	WLCSP-26			128K	16K			12		54																		
	S6E1C12B0A						64K	12K			9		38																		
	S6E1C11B0A						128K	16K			7		24																		
	S6E1C12D0A		QFN-64	QFN-48			128K	16K			12		54																		
	S6E1C11D0A						64K	12K			9		38																		
	S6E1C12C0A						128K	16K			7		24																		
	S6E1C11C0A						128K	16K			12		54																		
	S6E1C12B0A						64K	12K			9		38																		
	S6E1C11B0A						128K	16K			7		24																		

8FX – 8bit Microcontrollers

8FX – 8bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC/V	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
STANDARD																																				
MB95650L	MB95F652E	16	TSSOP-24 SOP-24 QFN-32	1.8 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	8K	256					21 20 21																				Low-voltage Detection Reset, Clock Supervisor Clock Supervisor Low-voltage Detection Reset, Clock Supervisor Clock Supervisor Low-voltage Detection Reset, Clock Supervisor Clock Supervisor Low-voltage Detection Reset, Clock Supervisor Clock Supervisor	On-chip Debug		
	MB95F652L						12K	512	-	-	6	-	20 21 20 21 20 21			6(1)	-																			
	MB95F653E						20K	1024					20 21 20 21 20 21																							
	MB95F653L						36K						20 21 20 21 20 21																							
	MB95F654E																																			
	MB95F654L																																			
	MB95F656E																																			
	MB95F656L																																			
MB95810K	MB95F814K	16	LQFP-64	2.88 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	20K	512					58		12(1)	-	-	-													Low-voltage Detection Reset, Clock Supervisor	On-chip Debug				
	MB95F816K						36K	1K	-	-	12	-																								
	MB95F818K						60K	2K																												

* In development; **Planning

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C][ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3120	S6J3128HA	112	TEQFP -144	4.5 to 5.25	-	Main Flash + Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	16	16	A24/D16	112	50(2)	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 10ch (LIN/UART/SIO Selectable)	CAN - FD x 3ch	-	-	32 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 4ch, Sound generator x 3ch	On-Chip Debug						
	S6J3129HA																																			
	S6J312AHA																																			
S6J3200	S6J32AAKS	160	TEQFP -208	1.1 to 1.3 3.0 to 3.6 4.5 to 5.5	✓	Main Flash + Work Flash	1088K + 112K	TC-RAM: 64KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 1024KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	30 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 1ch	On-Chip Debug							
	S6J32AAKU																																			
	S6J32AALS																																			
	S6J32AALU																																			
	S6J32BAKS																																			
	S6J32BAKU																																			
	S6J32BALS																																			
	S6J32BALU																																			
	S6J32CAKS																																			
	S6J32CAKU																																			
	S6J32CALS																																			
	S6J32CALU																																			
	S6J32DAKS																																			
	S6J32DAKU																																			
	S6J32DALS																																			
	S6J32DALU																																			

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch (MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

FR Family – 32bit Microcontrollers

FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Timer	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PW/C Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																
MB91570	MB91F575B	80	LQFP-144	4.5 to 5.5	Main Flash +Work Flash	✓	576K +64K	48K	1088K +64K	72K	-	16	16	✓	40(1)	-	8bit x 2	12	6	12	Reload Timer x 7ch (3ch for Periodic Timer Interrupt Occurrence) + PPG Timer x 24ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 4ch (LIN/UART/SIO/I2C Selectable. However, I2C only supports ch0/ch1)	6	3	-	-	32 x 4	-	CAN: 32/64Msg-buffer, LIN, Sound generator, SMC x 6ch From among the RAM, 8KB can be used as BackUpRAM For models with H suffix on part number, the initial status of the clock supervisor is off	On-chip Debug
	MB91F575BS					-																										
	MB91F575BH					-																										
	MB91F575BH5					-																										
	MB91F577B					-																										
	MB91F577BS					-																										
	MB91F577BH					-																										
	MB91F577BHS					-																										
	MB91F578C					-																										
	MB91F578CS					-																										
	MB91F578CH					-																										
	MB91F578CHS					-																										
	MB91F579C					-																										
	MB91F579CS					-																										
	MB91F579CH					-																										
	MB91F579CHS					-																										
	MB91F579CHM					-																										
	MB91F579CHSM					-																										
	MB91F579CHSM					-																										
	MB91F583ASG	128	LQFP-64	4.5 to 5.5	Main Flash +Work Flash	320K +64K	32K	-	8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 2ch (LIN/UART/SIO/I2C Selectable)	1	-	-	-	-	-	-	CAN: 64Msg-buffer, LIN, FPU, MPU, with no built-in RDC, Models with H or K suffix on part number have no built-in FlexRay.	On-chip Debug						
	MB91F583ASH																															
	MB91F583ASJ																															
	MB91F583ASK																															
	MB91F584ASG																															
	MB91F584ASH																															
	MB91F584ASJ																															
	MB91F584ASK																															
	MB91F585ASG																															
	MB91F585ASH																															
	MB91F585ASJ																															
	MB91F585ASK																															
	MB91F586AMH	LQFP-100	4.5 to 5.5	Main Flash +Work Flash	320K +64K	32K	-	76	8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 4ch (LIN/UART/SIO/I2C Selectable)	2	-	-	-	-	-	-	CAN: 64Msg-buffer, LIN, FPU, MPU, with no built-in RDC, Models with H or K suffix on part number have no built-in FlexRay.	On-chip Debug						
	MB91F586AMJ																															
	MB91F586AMK																															
	MB91F586AMU																															
	MB91F586AMC																															
	MB91F586AMK																															
	MB91F586LA																															

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device					
AUTOMOTIVE																																			
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F673A						64.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675A						128.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675R						256.5K +32K	10K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683A						64.5K +32K	10K	16K	-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683R						128.5K +32K	10K		-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F685A						256.5K +32K	10K		-	4	16	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695R																																		