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What is "[Embedded - Microcontrollers](#)"?

"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	POR, PWM, WDT
Number of I/O	20
Program Memory Size	8KB (8K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	256 x 8
Voltage - Supply (Vcc/Vdd)	1.8V ~ 5.5V
Data Converters	A/D 6x8/12b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	24-SOIC (0.295", 7.50mm Width)
Supplier Device Package	24-SOP
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f652lnpf-g-sne2

Consumer and Industrial MCU Family



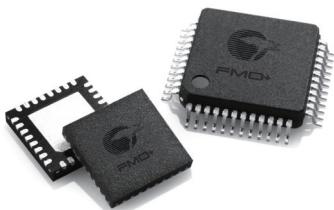
FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



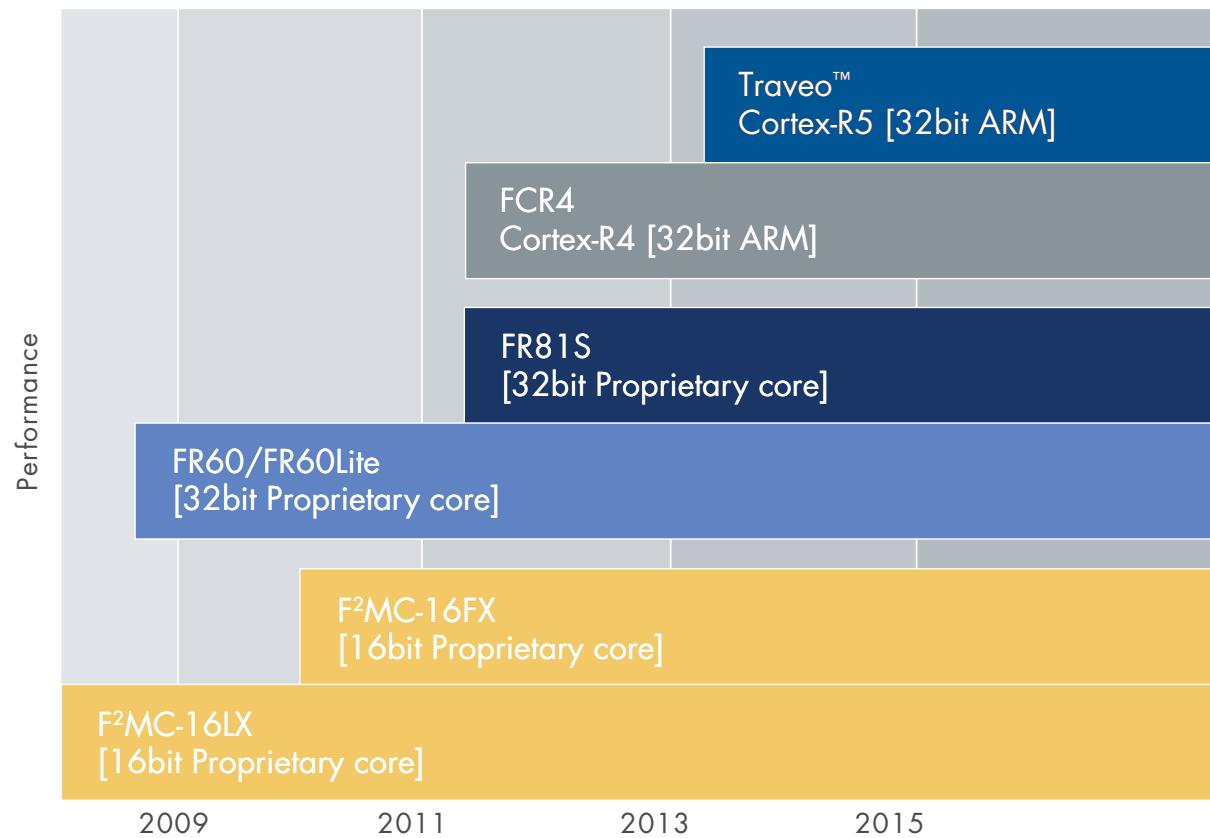
FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

Automotive MCU Core Roadmap



FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)								
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CQ} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	128K	8	8	11	- 16	51 66	9(2) 12(3)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer	On-chip Debug (SWJ-DP)										
	MB9AF311MA		LQFP-80																														
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																														
	MB9AF312LA		LQFP-64 QFN-64																														
	MB9AF312MA		LQFP-80																														
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																														
	MB9AF314LA		LQFP-64 QFN-64																														
	MB9AF314MA		LQFP-80																														
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																														
	MB9AF315MA		LQFP-80																														
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																														
	MB9AF316MA		LQFP-80																														
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																														

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{DD} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
ULTRA LOW LEAK GROUP																																					
MB9AAA0N	MB9AFAA1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AFAA1M		LQFP-80																																		
	MB9AFAA1N		LQFP-100 QFP-100																																		
	MB9AFAA2L		LQFP-64 QFN-64																																		
	MB9AFAA2M		LQFP-80																																		
	MB9AFAA2N		LQFP-100 QFP-100																																		
MB9A1A0N	MB9AF1A1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AF1A1M		LQFP-80																																		
	MB9AF1A1N		LQFP-100 QFP-100																																		
	MB9AF1A2L		LQFP-64 QFN-64																																		
	MB9AF1A2M		LQFP-80																																		
	MB9AF1A2N		LQFP-100 QFP-100																																		
MB9A130LA	MB9AF131LB	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	8K					8	52	8(1)	8(1)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AF131KB		LQFP-48 QFN-48																																		
	MB9AF132LB		LQFP-64 QFN-64																																		
	MB9AF132KB		LQFP-48 QFN-48																																		

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converters [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWIC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1A	S6E1A11B0A	40	LQFP-32 QFN-32	2.7 to 5.5	✓	Flash	56K	6K	-	2	8	-	23	37	-	5(1)	-	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 4ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 3ch (UART/CSIO/I ² C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, Fast I/O	On-chip Debug (SW-DP/MTB)							
	S6E1A11C0A		LOFP-48 LOFP-52 QFN-48																																	
	S6E1A12B0A		LOFP-32 QFN-32																																	
	S6E1A12C0A		LOFP-48 LOFP-52 QFN-48																																	
S6E1B8 w/security	S6E1B86GHA	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 40 SEG x 8 COM / 36 SEG x 4 COM 32 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F, Cipher	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84GHA																																			
	S6E1B86FHA																																			
	S6E1B84FHA																																			
	S6E1B86EHA																																			
	S6E1B84EHA																																			
S6E1B8	S6E1B86G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84G0A																																			
	S6E1B86F0A																																			
	S6E1B84F0A																																			
	S6E1B86E0A																																			
	S6E1B84E0A																																			
S6E1B3	S6E1B36G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B34G0A																																			
	S6E1B36F0A																																			
	S6E1B34F0A																																			
	S6E1B36E0A																																			
	S6E1B34E0A																																			
S6E1B1	S6E1B16G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B14G0A																																			
	S6E1B16F0A																																			
	S6E1B14F0A																																			
	S6E1B16E0A																																			
	S6E1B14E0A																																			

* In development; **Planning

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DIMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Convertor [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1C3	S6E1C32D0A	40	LQFP-64	1.65 to 3.6	✓	Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C31D0A						64K	12K			9		38																		
	S6E1C32C0A						128K	16K			5		20																		
	S6E1C31C0A		LQFP-32	WLCSP-26			128K	16K			7	-	24																		
	S6E1C32B0A						64K	12K			-																				
	S6E1C31B0A						128K	16K			12		54																		
	S6E1C32D0A		QFN-64	QFN-48			64K	12K			-																				
	S6E1C31D0A						128K	16K			9		38																		
	S6E1C32C0A						64K	12K			128K		16K																		
	S6E1C31C0A						64K	12K			-																				
	S6E1C32B0A						128K	16K			7		24																		
	S6E1C31B0A						64K	12K			-																				
S6E1C1	S6E1C12D0A	40	LQFP-64	1.65 to 3.6		Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C11D0A						64K	12K			9		38																		
	S6E1C12C0A						128K	16K			64K		12K																		
	S6E1C11C0A		LQFP-32	WLCSP-26			128K	16K			7		24																		
	S6E1C12B0A						64K	12K			-																				
	S6E1C11B0A						128K	16K			12		54																		
	S6E1C12D0A		QFN-64	QFN-48			64K	12K			-																				
	S6E1C11D0A						128K	16K			9		38																		
	S6E1C12C0A						64K	12K			-																				
	S6E1C11C0A						128K	16K			7		24																		
	S6E1C12B0A						64K	12K			-																				
	S6E1C11B0A						128K	16K			64K		12K																		

Traveo Family – 32bit Microcontrollers

Traveo Family – 32bit Microcontrollers

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	FreeRun Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device	
CAN/AUTOMOTIVE																																
MB91520	MB91F522B	80	LQFP-64	2.7 to 5.5	Main Flash +Work Flash	* See "part number suffix" in Note	320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K	56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K	- 16 16 - 76 96	44 56 16(1) 21(1) 16(1) 26(1)	- 8bit x 1 8bit x 2 16bit x 6 32bit x 4 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6	13(1) 13(1) 16(1) 16(1) 16(1) 26(1)	16bit x 3 32bit x 1 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6 16bit x 3 32bit x 6	+ PPG Timer x 21ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 27ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2 2 3	Multi Function Serial x 8ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 9ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	- - - - ✓	LIN, FPU, MPU, Tuning RAM MB91520B/D/F/J/K/L series: CAN: 64Msg-buffer x 2ch, 128Msg-buffer x 1ch, MB91520R/U/M/Y series: CAN: 128Msg-buffer x 6ch, FlexRay: 1 unit	Details of part number suffix <table border="1"><tr><td></td><td>Dual clock system</td></tr><tr><td>xWC</td><td>xJC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table> <table border="1"><tr><td></td><td>Single clock system</td></tr><tr><td>xSC</td><td>xHC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table>		Dual clock system	xWC	xJC	CSV initial value	ON OFF		Single clock system	xSC	xHC	CSV initial value	ON OFF	On-chip Debug
	Dual clock system																															
xWC	xJC																															
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MB91F523B																																
MB91F523K	LQFP-80																															
MB91F524K																																
MB91F525K	LQFP-100																															
MB91F526K																																
MB91F526D	LQFP-120																															
MB91F522F																																
MB91F523F	80	2.7 to 5.5	Main Flash +Work Flash	* See "part number suffix" in Note	320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K	56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K	- 16 16 - 76 96	44 56 16(1) 21(1) 16(1) 26(1)	- 8bit x 1 8bit x 2 16bit x 6 32bit x 4 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6	13(1) 13(1) 16(1) 16(1) 16(1) 26(1)	16bit x 3 32bit x 1 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6 16bit x 3 32bit x 6	+ PPG Timer x 21ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 27ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2 2 3	Multi Function Serial x 8ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 9ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	- - - - ✓	LIN, FPU, MPU, Tuning RAM MB91520B/D/F/J/K/L series: CAN: 64Msg-buffer x 2ch, 128Msg-buffer x 1ch, MB91520R/U/M/Y series: CAN: 128Msg-buffer x 6ch, FlexRay: 1 unit	Details of part number suffix <table border="1"><tr><td></td><td>Dual clock system</td></tr><tr><td>xWC</td><td>xJC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table> <table border="1"><tr><td></td><td>Single clock system</td></tr><tr><td>xSC</td><td>xHC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table>		Dual clock system	xWC	xJC	CSV initial value	ON OFF		Single clock system	xSC	xHC	CSV initial value	ON OFF	On-chip Debug		
	Dual clock system																															
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MB91F524F																																
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MB91F526J																																
MB91F522J																																
MB91F523J																																
MB91F524J																																
MB91F525J																																
MB91F526J																																
MB91F527R	LQFP-144 TEQFP-144																															
MB91F527R																																
MB91F527U	LQFP-176 TEQFP-176																															
MB91F527Y																																
MB91F528Y	128	BGA-416																														
MB91F528Y																																
MB91550	MB91F552	80	LQFP-64	4.5 to 5.5	- Main Flash +Work Flash	192K+64K	24K	-	8 4	-	30	-	8(1) 4ch S/H(1)	-	-	1	1	Reload Timer x 5ch + PWM Timer x 6ch (2ch x 3 pairs) + PWC Timer x 2ch + Base Timer x 4ch (Reload/PPG/PWM/PWC Selectable)	-	-	Multi Function Serial x 3ch (UART/SIO/LIN Selectable)	-	1	-	-	-	-	Comparator: 3ch, Slope Compensation: 1ch	On-Chip Debug			

* In development; **Planning

FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Timer	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PW/C Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																
MB91570	MB91F575B	80	LQFP-144	4.5 to 5.5	-	-	576K +64K	48K				109																	CAN: 32/64Msg-buffer, LIN, Sound generator, SMC x 6ch From among the RAM, 8KB can be used as BackUpRAM For models with H suffix on part number, the initial status of the clock supervisor is off	On-chip Debug		
	MB91F575BS				-	-	1088K +64K	72K				111																				
	MB91F575BH				-	-	1600K +64K	112K				109																				
	MB91F577B				-	-	2112K +64K	144K	-	16	16	116	40(1)	-	8bit x 2	12	6	12	Reload Timer x 7ch (3ch for Periodic Timer Interrupt Occurrence) + PPG Timer x 24ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 4ch (LIN/UART/SIO/I2C Selectable. However, I2C only supports ch0/ch1)	6	3	-	-	32 x 4	-				
	MB91F577BS				-	-	1600K +64K	112K				109																				
	MB91F577BH				-	-	2112K +64K	144K				111																				
	MB91F577BHS				-	-	1600K +64K	112K				109																				
	MB91F578C				-	-	2112K +64K	144K				111																				
	MB91F578CS				-	-	1600K +64K	112K				109																				
	MB91F578CH				-	-	2112K +64K	144K				111																				
MB91570	MB91F578CHS				-	-	1600K +64K	112K				109																				
	MB91F579C		LQFP-208	4.5 to 5.5	-	-	2112K +64K	144K				111																				
	MB91F579CS				-	-	1600K +64K	112K				109																				
	MB91F579CH				-	-	2112K +64K	144K				111																				
	MB91F579CHS				-	-	2112K +64K	144K				109																				
	MB91F579CM				-	-	2112K +64K	144K				111																				
MB91580	MB91F583ASG	128	LQFP-64	4.5 to 5.5	-	Main Flash +Work Flash	320K +64K	32K				8(1)															On-chip Debug					
	MB91F583ASH				-		448K +64K	48K	-	8	8	-	44	-	8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 2ch (LIN/UART/SIO/I2C Selectable)	1	-	-	-	-				
	MB91F583ASJ				-		576K +64K	48K				7(1)																				
	MB91F583ASK				-		576K +64K	48K				8(1)																				
	MB91F584ASG				-		576K +64K	48K				8(1)																				
	MB91F584ASH		LQFP-100	4.5 to 5.5	-	Main Flash +Work Flash	320K +64K	32K				8(1)														On-chip Debug						
	MB91F584ASJ				-		448K +64K	48K				7(1)																				
	MB91F584ASK				-		576K +64K	48K				8(1)																				
	MB91F585ASG				-		576K +64K	48K				8(1)																				
	MB91F585ASJ				-		576K +64K	48K				8(1)																				
MB91580	MB91F585AMG	128	LQFP-144	4.5 to 5.5	-	Main Flash +Work Flash	320K +64K	32K				8(1)														On-chip Debug						
	MB91F585AMH				-		448K +64K	48K				7(1)																				
	MB91F585AMJ				-		576K +64K	48K				8(1)																				
	MB91F585AMK				-		576K +64K	48K				8(1)																				
	MB91F585AMH				-		576K +64K	48K				8(1)																				
	MB91F585AMJ				-		576K +64K	48K				8(1)																				
	MB91F585AMK				-		576K +64K	48K				8(1)																				
	MB91F585AMH				-		576K +64K	48K				8(1)																				
	MB91F585AMJ				-		576K +64K	48K				8(1)																				
	MB91F585AMK				-		576K +64K	48K				8(1)																				
MB91580	MB91F586LA				-	LQFP-144	832K +64K	64K				98														On-chip Debug						
	MB91F586LB				-		1088K +64K	128K				98																				
	MB91F586LC				-		1088K +64K	128K				98																				
	MB91F586LD				-		1088K +64K	128K				98																				
	MB91F587LA				-		1088K +64K	128K				98																				
	MB91F587LB				-		1088K +64K	128K				98																				
	MB91F587LC				-		1088K +64K	128K				98																				
	MB91F587LD				-		1088K +64K	128K				98																				
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	MB91F587LD				-		1088K +64K	128K				98																				
	MB91F587LD				-		1088K +64K	128K				98																				
	MB91F587LD																															

FR Family – 32bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device			
AUTOMOTIVE																																	
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F643A						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96640	MB96F643R	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F645A						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F645R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F646R						384.5K +32K	28K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F647R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F653A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96650	MB96F653R	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F655A						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F655R						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F673A						64.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug		
	MB96F673R						128.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F675A						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F675R						384.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F683A						64.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4														

