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What is "[Embedded - Microcontrollers](#)"?

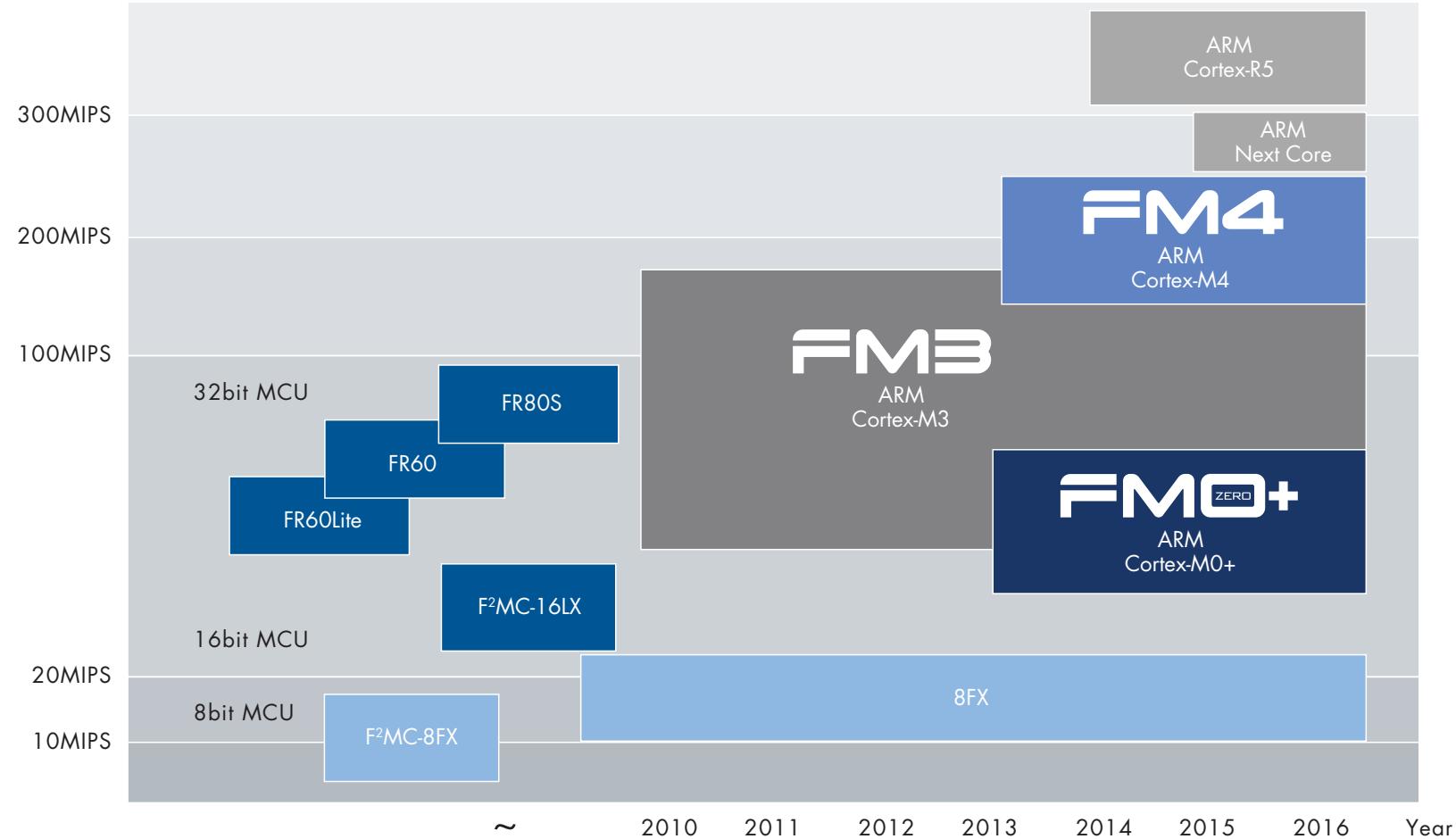
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	POR, PWM, WDT
Number of I/O	20
Program Memory Size	12KB (12K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	512 x 8
Voltage - Supply (Vcc/Vdd)	1.8V ~ 5.5V
Data Converters	A/D 6x8/12b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	24-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	24-TSSOP
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f653lnpft-g-snre2

Consumer and Industrial MCU Core Roadmap



Consumer and Industrial MCU Family



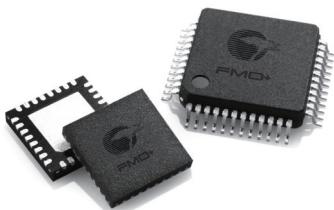
FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



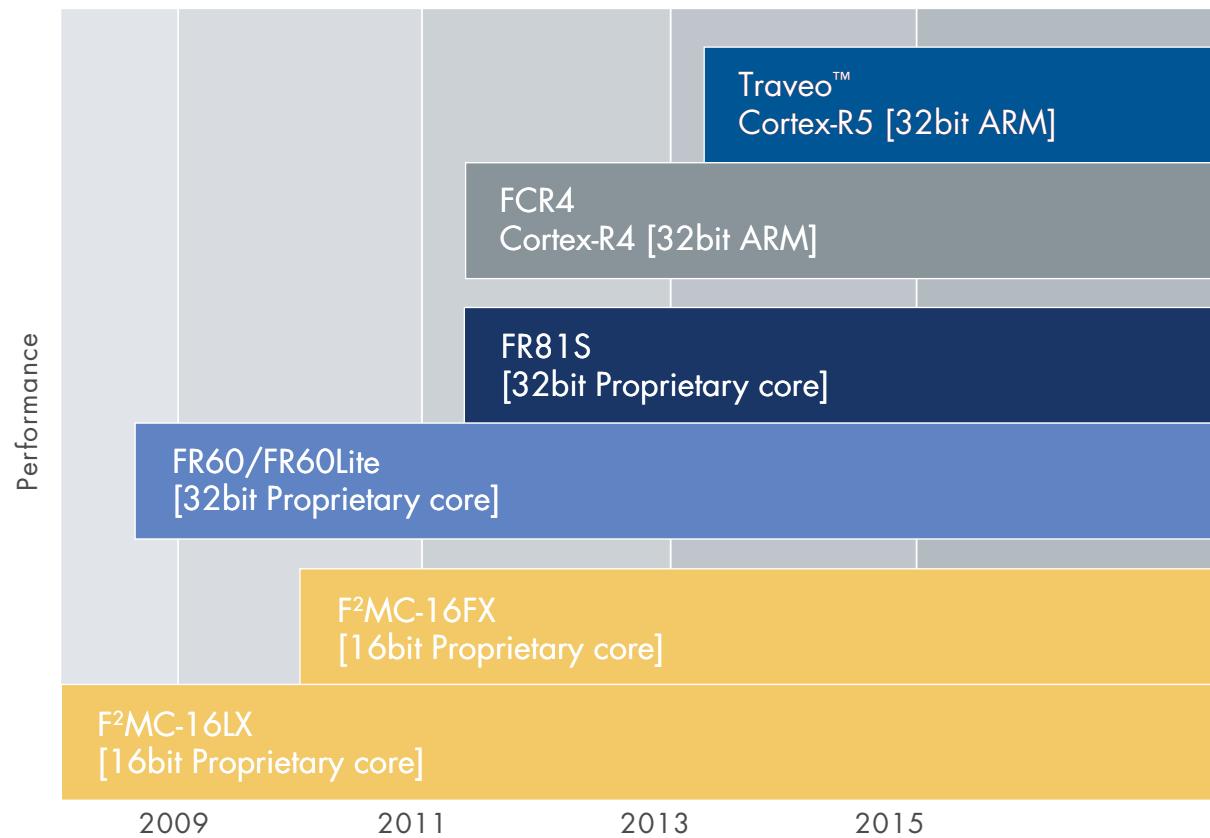
FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

Automotive MCU Core Roadmap



FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)								
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM4 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	i _C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
HIGH-PERFORMANCE GROUP																																				
MB9B500B	MB9BF504NB	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF504RB		LQFP-120																																	
	MB9BF505NB		LQFP-100 BGA-112																																	
	MB9BF505RB		LQFP-120																																	
	MB9BF506NB		LQFP-100 BGA-112																																	
	MB9BF506RB		LQFP-120																																	
MB9B400A	MB9BF404NA	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF404RA		LQFP-120																																	
	MB9BF405NA		LQFP-100 BGA-112																																	
	MB9BF405RA		LQFP-120																																	
	MB9BF406NA		LQFP-100 BGA-112																																	
	MB9BF406RA		LQFP-120																																	
MB9B300B	MB9BF304NB	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF304RB		LQFP-120																																	
	MB9BF305NB		LQFP-100 BGA-112																																	
	MB9BF305RB		LQFP-120																																	
	MB9BF306NB		LQFP-100 BGA-112																																	
	MB9BF306RB		LQFP-120																																	
MB9B100A	MB9BF102NA	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	128K 256K 384K 512K	16K 32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF102RA		LQFP-120																																	
	MB9BF104NA		LQFP-100 BGA-112																																	
	MB9BF104RA		LQFP-120																																	
	MB9BF105NA		LQFP-100 BGA-112																																	
	MB9BF105RA		LQFP-120																																	
	MB9BF106NA		LQFP-100 BGA-112																																	
	MB9BF106RA		LQFP-120																																	
MB9B520M	MB9BF521K	72	LQFP-48 QFN-48	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)													
	MB9BF521L		LQFP-64 QFN-64																																	
	MB9BF521M		LQFP-80 BGA-96																																	
	MB9BF522K		LQFP-48 QFN-48																																	
	MB9BF522L		LQFP-64 QFN-64																																	
	MB9BF522M</																																			

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CQ} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

* In development; **Planning

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

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Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																	On-chip Debug (SWJ-DP)					
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16		83		24(2)																						

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{DD} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
ULTRA LOW LEAK GROUP																																					
MB9AAA0N	MB9AFAA1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)							
	MB9AFAA1M		LQFP-80																																		
	MB9AFAA1N		LQFP-100 QFP-100																																		
	MB9AFAA2L		LQFP-64 QFN-64																																		
	MB9AFAA2M		LQFP-80																																		
	MB9AFAA2N		LQFP-100 QFP-100																																		
MB9A1A0N	MB9AF1A1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)							
	MB9AF1A1M		LQFP-80																																		
	MB9AF1A1N		LQFP-100 QFP-100																																		
	MB9AF1A2L		LQFP-64 QFN-64																																		
	MB9AF1A2M		LQFP-80																																		
	MB9AF1A2N		LQFP-100 QFP-100																																		
MB9A130LA	MB9AF131LB	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	8K					8	52	8(1)	8(1)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	Real Time Clock	On-chip Debug (SWJ-DP)		
	MB9AF131KB		LQFP-48 QFN-48																																		
	MB9AF132LB		LQFP-64 QFN-64																																		
	MB9AF132KB		LQFP-48 QFN-48																																		

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converters [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWIC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1A	S6E1A11B0A	40	LQFP-32 QFN-32	2.7 to 5.5	✓	Flash	56K	6K	-	2	8	-	23	37	-	5(1)	-	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 4ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 3ch (UART/CSIO/I ² C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, Fast I/O	On-chip Debug (SW-DP/MTB)							
	S6E1A11C0A		LOFP-48 LOFP-52 QFN-48																																	
	S6E1A12B0A		LOFP-32 QFN-32																																	
	S6E1A12C0A		LOFP-48 LOFP-52 QFN-48																																	
S6E1B8 w/security	S6E1B86GHA	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 40 SEG x 8 COM / 36 SEG x 4 COM 32 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F, Cipher	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84GHA																																			
	S6E1B86FHA																																			
	S6E1B84FHA																																			
	S6E1B86EHA																																			
	S6E1B84EHA																																			
S6E1B8	S6E1B86G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84G0A																																			
	S6E1B86F0A																																			
	S6E1B84F0A																																			
	S6E1B86E0A																																			
	S6E1B84E0A																																			
S6E1B3	S6E1B36G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B34G0A																																			
	S6E1B36F0A																																			
	S6E1B34F0A																																			
	S6E1B36E0A																																			
	S6E1B34E0A																																			
S6E1B1	S6E1B16G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM 32 SEG x 8 COM / 36 SEG x 4 COM 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B14G0A																																			
	S6E1B16F0A																																			
	S6E1B14F0A																																			
	S6E1B16E0A																																			
	S6E1B14E0A																																			

* In development; **Planning

8FX – 8bit Microcontrollers

Series Name	Product Name	LCD CONTROLLERS																		Note	Evaluation Device									
		Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbytes]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [cht/unit]	12bit AD Converter [cht/unit]	DA Converter [bit/x ch]	Output Compare [ch]	Timer	Serial	Communication										
MB95610H	MB95F613H	16	LQFP-80	2.4 to 5.5	✓	Dual Op. Flash	12K	512			8		40	41			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2	-	1	2	-	52x 4 or 48x 8	-	Clock Supervisor	On-chip Debug				
	MB95F613K						20K	1024					40	41										Clock Supervisor						
	MB95F614H						36K	1024					40	41										Clock Supervisor						
	MB95F614K						40	41					40	41										Clock Supervisor						
	MB95F616H						40	41					40	41										Clock Supervisor						
	MB95F616K						40	41					40	41										Clock Supervisor						
MB95710M	MB95F714J	16	LQFP-80	1.8 to 5.5	✓	Dual Op. Flash	20K	512			8		75	74			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2	-	1	2	-	40x 4 or 36x 8	-	Low-voltage Detection Reset, Clock Supervisor	On-chip Debug				
	MB95F714M						36K	1K					75	74										Clock Supervisor						
	MB95F716J						60K	2K					75	74										Low-voltage Detection Reset, Clock Supervisor						
	MB95F716M						60K	2K					75	74										Clock Supervisor						
	MB95F718J						20K	512			8		59	58			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2	-	1	3	-	32x 4 or 28x 8	-	Low-voltage Detection Reset, Clock Supervisor	On-chip Debug				
	MB95F718M						36K	1K					59	58										Clock Supervisor						
	MB95F774J						60K	2K					59	58										Clock Supervisor						
	MB95F774M						20K	512					59	58										Low-voltage Detection Reset, Clock Supervisor						
MB95770M	MB95F776J	16	LQFP-64	1.8 to 5.5	✓	Dual Op. Flash	20K	512			8		59	58			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2	-	1	3	-	32x 4 or 28x 8	-	Low-voltage Detection Reset, Clock Supervisor	On-chip Debug				
	MB95F776M						36K	1K					59	58										Clock Supervisor						
	MB95F778J						60K	2K					59	58										Low-voltage Detection Reset, Clock Supervisor						
	MB95F778M						20K	512					59	58										Clock Supervisor						
INVERTER																														
MB95630H	MB95F632H	16	LQFP-32 SH-DIP-32 QFN-32	2.4 to 5.5	✓	Dual Op. Flash	8K	256			10		28	29			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	3	-	1	1	-	-	-	Low-voltage Detection Reset, Multi-pulse Generator (Reload/PPG/Waveform Sequencer), Clock Supervisor	On-chip Debug				
	MB95F632K						12K	512					28	29																
	MB95F633H						20K	1024					29	28																
	MB95F633K						36K						28	29																
	MB95F634H						20K						28	29																
	MB95F634K						36K						28	29																
MB95690K	MB95F694K	16	LQFP-44 LQFP-48 LQFP-52 QFN-48	2.88 to 5.5	✓	Dual Op. Flash	20K	512			8		45	LQFP44 only: 8(1)		12(1) LQFP44 only: 8(1)	+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	3	-	1	1	-	1	-	Low-voltage Detection Reset, Multi-pulse Generator (Reload/PPG/Waveform Sequencer), Clock Supervisor	On-chip Debug				
	MB95F696K						36K	1K					45	LQFP44 only: 8(1)																
	MB95F698K						60K	2K					45	LQFP44 only: 8(1)																
STANDARD																														
MB95560H	MB95F562H	16	SOP-20 TSSOP-20 QFN-32	2.4 to 5.5	✓	Dual Op. Flash	8K	240			6		16	17			8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	-	-	-	-	1	-	-	On-chip Debug					
	MB95F562K						12K	496					16	17																
	MB95F563H						20K						16	17																
	MB95F563K						20K						17	17																
	MB95F564H						20K						17	17																
	MB95F564K						20K						17	17																
MB95570H	MB95F572H	16	SOP-8	2.4 to 5.5	✓	Dual Op. Flash	8K	240			2		4	5			8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	-	-	-	-	-	-	-	On-chip Debug					
	MB95F572K						12K	496					4	5																
	MB95F573H						20K						4	5																
	MB95F573K						20K						4	5																
	MB95F574H						20K						4	5																
	MB95F574K						20K						4	5																
MB95580H	MB95F582H	16	SOP-16 TSSOP-16 QFN-32	2.4 to 5.5	✓	Dual Op. Flash	8K	240			6		12	13			8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)													

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C][ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3120	S6J3128HA	112	TEQFP -144	4.5 to 5.25	-	Main Flash +Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	16	16	A24/D16	112	50(2)	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 10ch (LIN/UART/SIO Selectable)	CAN - FD x 3ch	-	-	32 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 4ch, Sound generator x 3ch	On-Chip Debug						
	S6J3129HA																																			
	S6J312AHA																																			
S6J3200	S6J32AAKS	160	TEQFP -208	1.1 to 1.3 3.0 to 3.6 4.5 to 5.5	✓	Main Flash +Work Flash	1088K + 112K	TC-RAM: 64KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 1024KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	30 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 1ch	On-Chip Debug							
	S6J32AAKU																																			
	S6J32AALS																																			
	S6J32AALU																																			
	S6J32BAKS																																			
	S6J32BAKU																																			
	S6J32BALS																																			
	S6J32BALU																																			
	S6J32CAKS																																			
	S6J32CAKU																																			
	S6J32CALS																																			
	S6J32CALU																																			
	S6J32DAKS																																			
	S6J32DAKU																																			
	S6J32DALS																																			
	S6J32DALU																																			

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	FreeRun Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device	
CAN/AUTOMOTIVE																																
MB91520	MB91F522B	80	LQFP-64	2.7 to 5.5	Main Flash +Work Flash	* See "part number suffix" in Note	320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K	56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K	- 16 16 - 76 96	44 56 16(1) 21(1) 16(1) 26(1)	- 8bit x 1 8bit x 2 16bit x 6 32bit x 4 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6	13(1) 13(1) 16(1) 16(1) 16(1) 26(1)	16bit x 3 32bit x 1 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6 16bit x 3 32bit x 6	+ PPG Timer x 21ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 27ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2 2 3	Multi Function Serial x 8ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 9ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	- - - - ✓	LIN, FPU, MPU, Tuning RAM MB91520B/D/F/J/K/L series: CAN: 64Msg-buffer x 2ch, 128Msg-buffer x 1ch, MB91520R/U/M/Y series: CAN: 128Msg-buffer x 6ch, FlexRay: 1 unit	Details of part number suffix <table border="1"><tr><td></td><td>Dual clock system</td></tr><tr><td>xWC</td><td>xJC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table> <table border="1"><tr><td></td><td>Single clock system</td></tr><tr><td>xSC</td><td>xHC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table>		Dual clock system	xWC	xJC	CSV initial value	ON OFF		Single clock system	xSC	xHC	CSV initial value	ON OFF	On-chip Debug
	Dual clock system																															
xWC	xJC																															
CSV initial value	ON OFF																															
	Single clock system																															
xSC	xHC																															
CSV initial value	ON OFF																															
MB91F523B																																
MB91F524B	LQFP-80																															
MB91F524D																																
MB91F525D	LQFP-100																															
MB91F526D																																
MB91F522F	LQFP-120																															
MB91F523F																																
MB91F524F	LQFP-144																															
MB91F525F																																
MB91F526F	LQFP-144 TEQFP-144																															
MB91F527R																																
MB91F527R	LQFP-176																															
MB91F527R																																
MB91F527R	LQFP-176 TEQFP-176																															
MB91F527R																																
MB91F527M	128	LQFP-208 TEQFP-208																														
MB91F528M																																
MB91F527Y		BGA-416																														
MB91F528Y																																
MB91550	MB91F552	80	LQFP-64	4.5 to 5.5	- Main Flash +Work Flash	192K+64K	24K	-	8 4 -	-	30 -	-	8(1) 4ch S/H(1)	-	-	-	1 1	Reload Timer x 5ch + PWM Timer x 6ch (2ch x 3 pairs) + PWC Timer x 2ch + Base Timer x 4ch (Reload/PPG/PWM/PWC Selectable)	-	-	Multi Function Serial x 3ch (UART/SIO/LIN Selectable)	-	1 - - - -	Comparator: 3ch, Slope Compensation: 1ch	On-Chip Debug							

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device			
AUTOMOTIVE																																	
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F673A						64.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug	
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	128.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F675A						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F675R						384.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F683A						64.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug	
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
	MB96F685A						256.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	1 1 1 1			
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug	
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1			
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	1 1 1 1 1			
	MB96F695R						384.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-</											

