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What is "[Embedded - Microcontrollers](#)"?

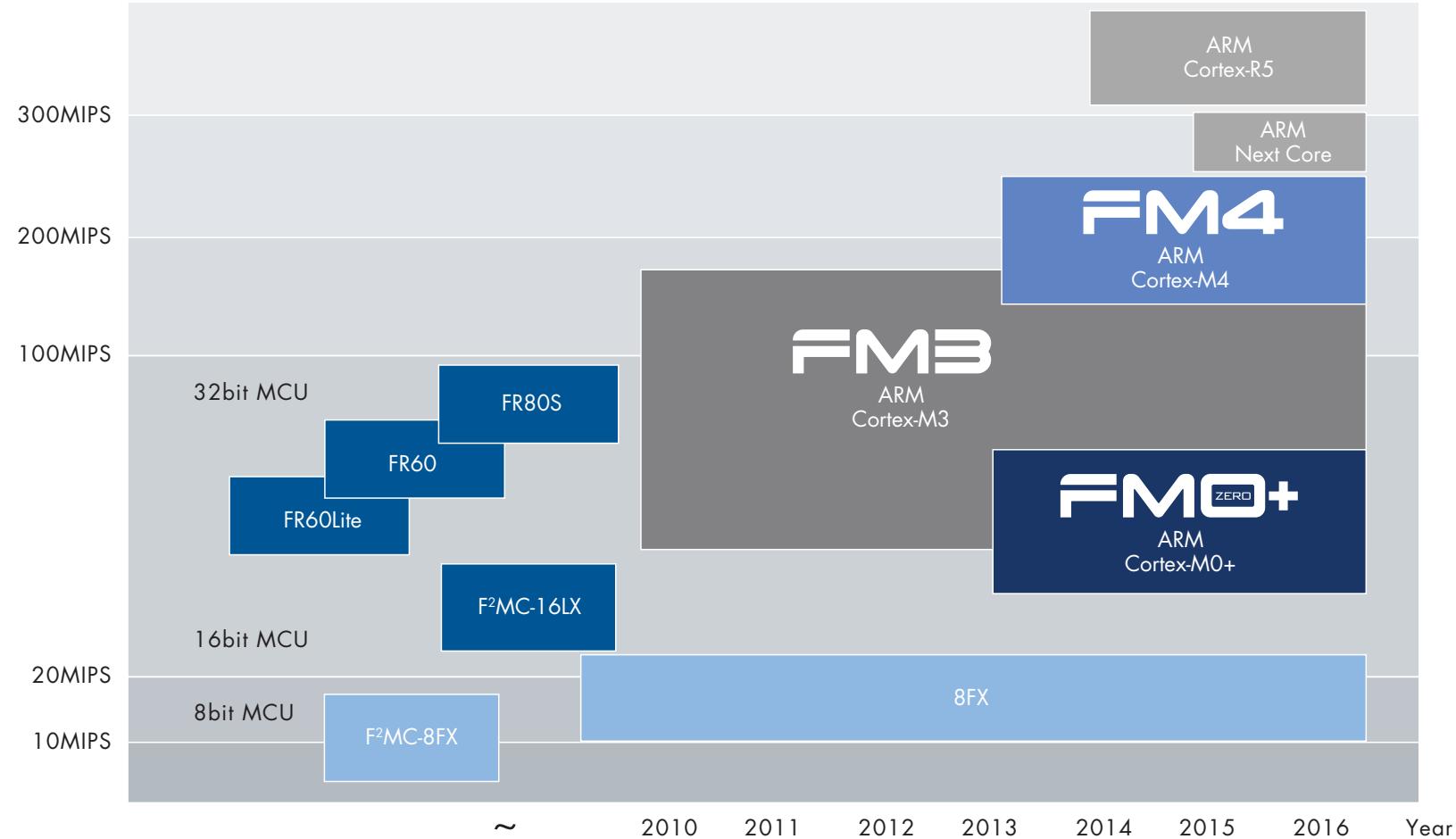
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

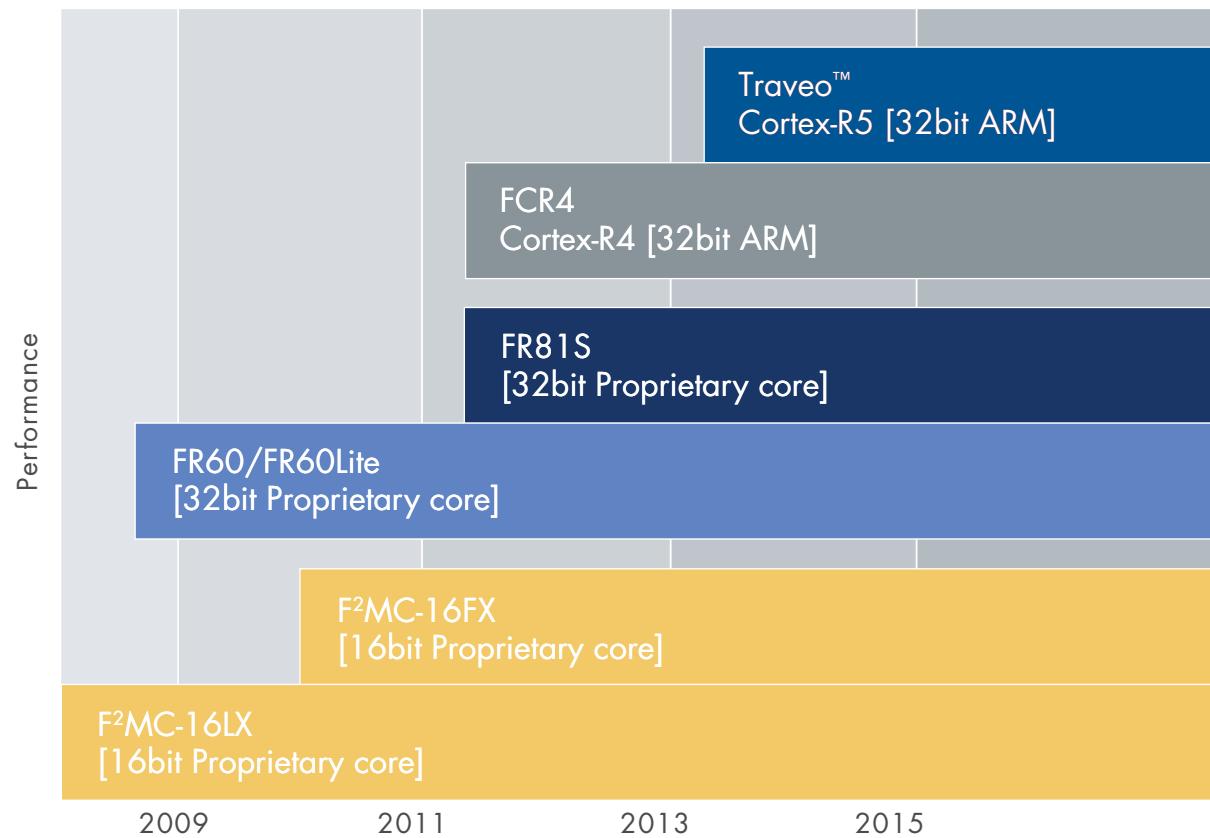
Details

Product Status	Obsolete
Core Processor	F ² MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I ² C, LINbus, SIO, UART/USART
Peripherals	LVD, POR, PWM, WDT
Number of I/O	45
Program Memory Size	60KB (60K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	2K x 8
Voltage - Supply (Vcc/Vdd)	2.88V ~ 5.5V
Data Converters	A/D 12x8/10b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	48-LQFP
Supplier Device Package	48-LQFP (7x7)
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb95f698knpmc-g109snere2

Consumer and Industrial MCU Core Roadmap



Automotive MCU Core Roadmap



Automotive MCU Family

TRAVEO FAMILY

The Cypress Traveo™ family expands the company's automotive application coverage, scalability and high performance into one line-up and at the same time adds new features to fulfill the latest requirements of the automotive industry. Based on the powerful ARM® Cortex®-R5 and R5F core in single and dual core operations, it offers state-of-the-art real-time performance, safety and security features.

The family supports the latest in-car networks and offers high performance graphics engines optimized for a minimum memory footprint and embeds dedicated features to increase data security in the car.

High-level Traveo Features

- ARM Cortex-R5 core
- Dual core
- Embedded twin-motor control with internal R/D converter
- Embedded 2D and 3D graphics engines
- High performance embedded flash memory
- Cypress HyperBus™: High speed serial interfaces to connect external memory
- Qualified for automotive use (AEC-Q100)
- Software support for Autosar, graphics drivers and more

FR FAMILY

Cypress 32bit MCU families have been designed in close co-operation with major automotive customers worldwide and inherit the high-performance core of Cypress's proprietary FR MCU architecture. They support communication interfaces such as CAN, FlexRay and LIN as well as up to 2MB on-chip memory capacity. The latest members (MB915xx) also include a single precision Floating Point Unit (FPU) providing additional computing power required by complex control algorithms.

This high computing performance combined with powerful peripheral functions such as on-chip graphics controller and motor control macros, offers a higher grade of flexibility and lower cost for automotive as well as industrial applications.

Many devices offer an external bus interface which can be connected to Cypress's stand alone FlexRay controller or to the latest generation of graphics controllers in order to build full-featured dashboards, driver information systems or advanced body systems.

FCR4 FAMILY

The FCR4 family has been specifically designed to offer an innovative, scalable solution for hybrid instrument clusters, which combine traditional meters and graphical displays. The devices offer a powerful architecture based on the ARM Cortex-R4 core and Cypress's 2D graphics engine.

High-level FCR4 Features

- ARM Cortex-R4 core
- Embedded 2D graphics engine
- 2MB flash, 64KB E2Flash
- Up to 208KB RAM
- Features including real-time clock/auto calibration, sound generator and I2S

F2MC-16FX FAMILY

Cypress 16-bit flexible microcontroller series offers a scalable family concept approach to a variety of automotive and industrial applications. The scalable flash/ROM/RAM sizes with different mixtures of peripherals saves development time and costs. CAN and LIN support, on-chip LCD controller, SMC (stepper motor controller), I²C bus interface, analog input channels, external bus interface, selectable port levels for CMOS, TTL and Automotive Levels are some of the enhanced features. A security feature is incorporated, preventing unauthorized reading of the contents of the flash memory.



FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbytes]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device		
S6E2HG	S6E2HG6E0A	160	LQFP-80										63	16 (3)																								
	S6E2HG6F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																				Dual Timer, Real Timer Clock, Unique ID, DSTC x 256ch, SDC	On-Chip Debug (SWJ-DP/ETM)				
	S6E2HG6G0A		LQFP-120	BGA-121									100																									
	S6E2HG4E0A	160	LQFP-80										63	16 (3)																								
	S6E2HG4F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																									
	S6E2HG4G0A		LQFP-120	BGA-121									100																									
S6E2HE	S6E2HE6E0A	160	LQFP-80										63	16 (3)																								
	S6E2HE6F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																									
	S6E2HE6G0A		LQFP-120	BGA-121									100																									
	S6E2HE4E0A	160	LQFP-80										63	16 (3)																								
	S6E2HE4F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																									
	S6E2HE4G0A		LQFP-120	BGA-121									100																									
S6E2H4	S6E2H46E0A	160	LQFP-80										63	16 (3)																								
	S6E2H46F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																									
	S6E2H46G0A		LQFP-120	BGA-121									100																									
	S6E2H44E0A	160	LQFP-80										63	16 (3)																								
	S6E2H44F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																									
	S6E2H44G0A		LQFP-120	BGA-121									100																									
S6E2H1	S6E2H16E0A	160	LQFP-80										63	16 (3)																								
	S6E2H16F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	512K + 32K	64K	-	8	16	✓	80																									
	S6E2H16G0A		LQFP-120	BGA-121									100																									
	S6E2H14E0A	160	LQFP-80										63	16 (3)																								
	S6E2H14F0A		LQFP-100	2.7 to 5.5	✓	Main Flash +Work Flash	256K + 32K	32K	-	8	16	✓	80																									
	S6E2H14G0A		LQFP-120	BGA-121									100																									
S6E2GM w/Security	S6E2GM8HHA	180	LQFP-144										121	24 (3)																								
	S6E2GM8JHA		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																							
	S6E2GM6HHA		LQFP-144										121		24 (3)																							
	S6E2GM6JHA		LQFP-176										153		32 (3)																							
S6E2GM	S6E2GM8H0A	180	LQFP-144										121	24 (3)																								
	S6E2GM8J0A		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																							
	S6E2GM6H0A		LQFP-144										121		24 (3)																							
	S6E2GM6J0A		LQFP-176										153		32 (3)																							
S6E2GK w/Security	S6E2GK8HHA	180	LQFP-144										121	24 (3)																								
	S6E2GK8JHA		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																							
	S6E2GK6HHA		LQFP-144										121		24 (3)																							
	S6E2GK6JHA		LQFP-176										153		32 (3)																							
S6E2GK	S6E2GK8H0A	180	LQFP-144										121	24 (3)																								
	S6E2GK8J0A		LQFP-176	2.7 to 5.5	✓	Main Flash	1024K	192K		8	32	✓	153		32 (3)																							
	S6E2GK6H0A		LQFP-144										121		24 (3)																							
	S6E2GK6J0A		LQFP-176										153		32 (3)																							

* In development; **Planning

FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CQ} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																		On-chip Debug (SWJ-DP)				
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						

FM3 Family – 32bit Microcontrollers

FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _C [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DIMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Convertor [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1C3	S6E1C32D0A	40	LQFP-64	1.65 to 3.6	✓	Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C31D0A						64K	12K			9		38																		
	S6E1C32C0A						128K	16K			5		20																		
	S6E1C31C0A		LQFP-32	WLCSP-26			128K	16K			7	-	24																		
	S6E1C32B0A						64K	12K			-																				
	S6E1C31B0A						128K	16K			12		54																		
	S6E1C32D0A		QFN-64	QFN-48			64K	12K			-																				
	S6E1C31D0A						128K	16K			9		38																		
	S6E1C32C0A						64K	12K			128K		16K																		
	S6E1C31C0A						64K	12K			9		38																		
	S6E1C32B0A						128K	16K			7		24																		
	S6E1C31B0A						64K	12K			-																				
S6E1C1	S6E1C12D0A	40	LQFP-64	1.65 to 3.6		Main Flash	128K	16K			12		54													Multi Function Serial x 6ch (UART/CSIO/I2C Selectable)	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, Wakeup I2C, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SWJ-DP)		
	S6E1C11D0A						64K	12K			9		38																		
	S6E1C12C0A						128K	16K			64K		12K																		
	S6E1C11C0A		LQFP-32	WLCSP-26			128K	16K			7		24																		
	S6E1C12B0A						64K	12K			-																				
	S6E1C11B0A						128K	16K			12		54																		
	S6E1C12D0A		QFN-64	QFN-48			64K	12K			9		38																		
	S6E1C11D0A						128K	16K			64K		12K																		
	S6E1C12C0A						64K	12K			9		38																		
	S6E1C11C0A						128K	16K			7		24																		
	S6E1C12B0A						64K	12K			-																				
	S6E1C11B0A						128K	16K			64K		12K																		

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C][ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3120	S6J3128HA	112	TEQFP -144	4.5 to 5.25	-	Main Flash +Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	16	16	A24/D16	112	50(2)	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 10ch (LIN/UART/SIO Selectable)	CAN - FD x 3ch	-	-	32 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 4ch, Sound generator x 3ch	On-Chip Debug						
	S6J3129HA																																			
	S6J312AHA																																			
S6J3200	S6J32AAKS	160	TEQFP -208	1.1 to 1.3 3.0 to 3.6 4.5 to 5.5	✓	Main Flash +Work Flash	1088K + 112K	TC-RAM: 64KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 1024KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	30 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 1ch	On-Chip Debug							
	S6J32AAKU																																			
	S6J32AALS																																			
	S6J32AALU																																			
	S6J32BAKS																																			
	S6J32BAKU																																			
	S6J32BALS																																			
	S6J32BALU																																			
	S6J32CAKS																																			
	S6J32CAKU																																			
	S6J32CALS																																			
	S6J32CALU																																			
	S6J32DAKS																																			
	S6J32DAKU																																			
	S6J32DALS																																			
	S6J32DALU																																			

Traveo Family – 32bit Microcontrollers

FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	FreeRun Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device	
CAN/AUTOMOTIVE																																
MB91520	MB91F522B	80	LQFP-64	2.7 to 5.5	Main Flash +Work Flash	* See "part number suffix" in Note	320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K 320K+64K 448K+64K 576K+64K 832K+64K 1088K+64K	56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K 56K 72K 104K 136K	- 16 16 - 76 96	44 56 16(1) 21(1) 16(1) 26(1)	- 8bit x 1 8bit x 2 16bit x 6 32bit x 4 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6	13(1) 13(1) 16(1) 16(1) 16(1) 26(1)	16bit x 3 32bit x 1 16bit x 4 32bit x 5 16bit x 3 32bit x 2 16bit x 3 32bit x 6 16bit x 3 32bit x 6	+ PPG Timer x 21ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 27ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 1ch (Reload/PPG/PWM/PWC Selectable) + PPG Timer x 8ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2 2 3	Multi Function Serial x 8ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 9ch (LIN/UART/SIO/I2C Selectable) Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	- - - - ✓	LIN, FPU, MPU, Tuning RAM MB91520B/D/F/J/K/L series: CAN: 64Msg-buffer x 2ch, 128Msg-buffer x 1ch, MB91520R/U/M/Y series: CAN: 128Msg-buffer x 6ch, FlexRay: 1 unit	Details of part number suffix <table border="1"><tr><td></td><td>Dual clock system</td></tr><tr><td>xWC</td><td>xJC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table> <table border="1"><tr><td></td><td>Single clock system</td></tr><tr><td>xSC</td><td>xHC</td></tr><tr><td>CSV initial value</td><td>ON OFF</td></tr></table>		Dual clock system	xWC	xJC	CSV initial value	ON OFF		Single clock system	xSC	xHC	CSV initial value	ON OFF	On-chip Debug
	Dual clock system																															
xWC	xJC																															
CSV initial value	ON OFF																															
	Single clock system																															
xSC	xHC																															
CSV initial value	ON OFF																															
MB91F523B																																
MB91F524B	LQFP-80																															
MB91F524D																																
MB91F525D	LQFP-100																															
MB91F526D																																
MB91F522F	LQFP-120																															
MB91F523F																																
MB91F524F	LQFP-144																															
MB91F525F																																
MB91F526F	LQFP-144 TEQFP-144																															
MB91F527R																																
MB91F527R	LQFP-176																															
MB91F527R																																
MB91F527R	LQFP-176 TEQFP-176																															
MB91F527R																																
MB91F527M	128	LQFP-208 TEQFP-208																														
MB91F528M																																
MB91F527Y		BGA-416																														
MB91F528Y																																
MB91550	MB91F552	80	LQFP-64	4.5 to 5.5	- Main Flash +Work Flash	192K+64K	24K	-	8 4 -	-	30 -	-	8(1) 4ch S/H(1)	-	-	-	1 1	Reload Timer x 5ch + PWM Timer x 6ch (2ch x 3 pairs) + PWC Timer x 2ch + Base Timer x 4ch (Reload/PPG/PWM/PWC Selectable)	-	-	Multi Function Serial x 3ch (UART/SIO/LIN Selectable)	-	1 - - - -	Comparator: 3ch, Slope Compensation: 1ch	On-Chip Debug							

FR Family – 32bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device					
AUTOMOTIVE																																			
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F673A						64.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675A						128.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675R						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683A						64.5K +32K	4K	16K	-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F685A						256.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695R						384.																												



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