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#### What is "[Embedded - Microcontrollers](#)"?

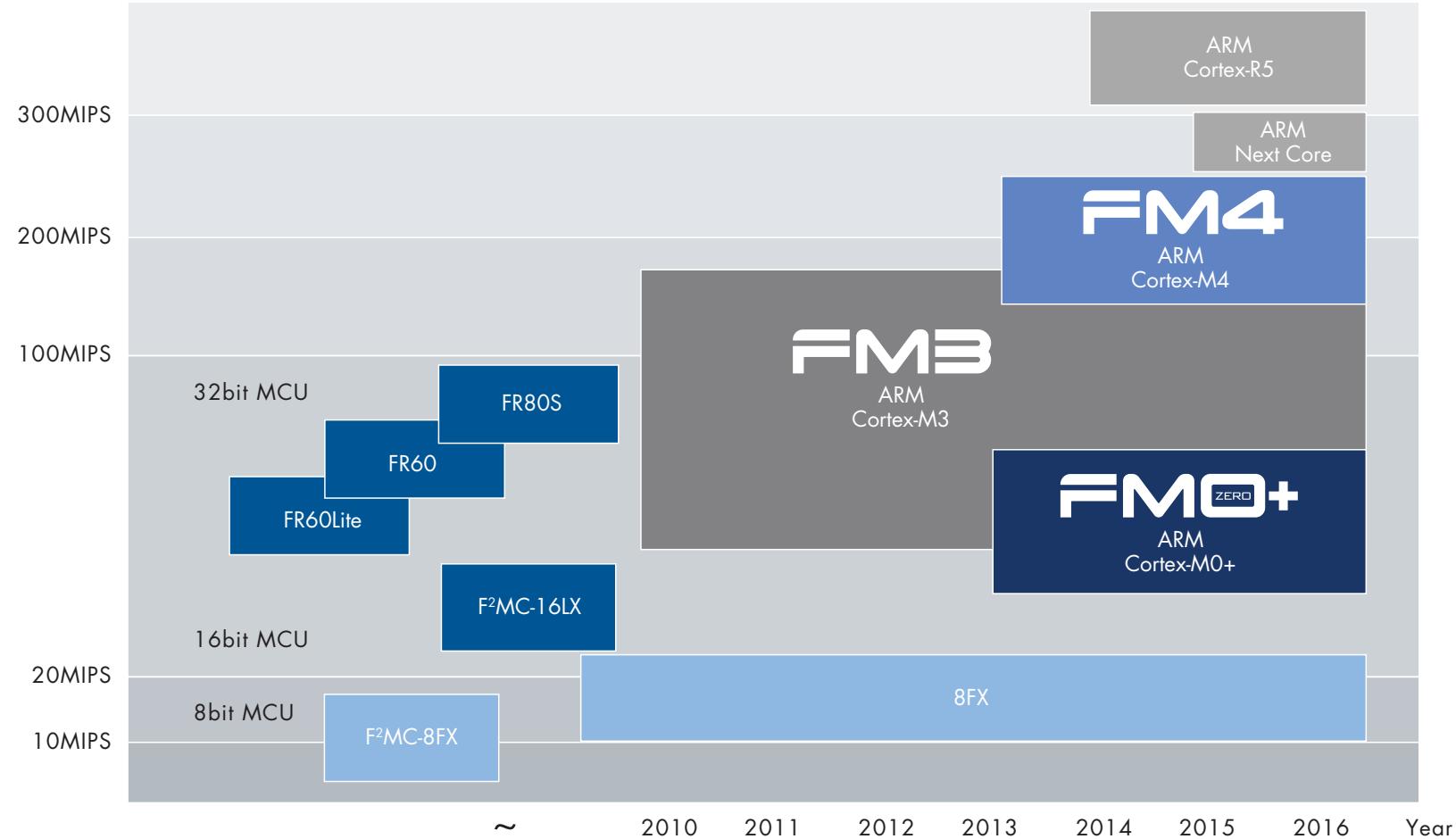
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

#### Applications of "[Embedded - Microcontrollers](#)"

##### Details

Product Status	Obsolete
Core Processor	F <sup>2</sup> MC-8FX
Core Size	8-Bit
Speed	16MHz
Connectivity	I <sup>2</sup> C, SIO, UART/USART
Peripherals	LCD, POR, PWM, WDT
Number of I/O	58
Program Memory Size	60KB (60K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	2K x 8
Voltage - Supply (Vcc/Vdd)	1.8V ~ 5.5V
Data Converters	A/D 8x8/12b
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	64-LQFP
Supplier Device Package	64-LQFP (10x10)
Purchase URL	<a href="https://www.e-xfl.com/product-detail/infineon-technologies/mb95f778mnpmc1-g-sne2">https://www.e-xfl.com/product-detail/infineon-technologies/mb95f778mnpmc1-g-sne2</a>

## Consumer and Industrial MCU Core Roadmap



## Consumer and Industrial MCU Family



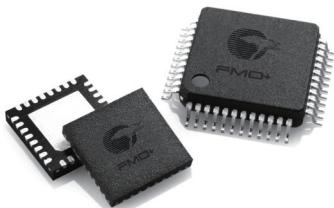
### FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



### FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



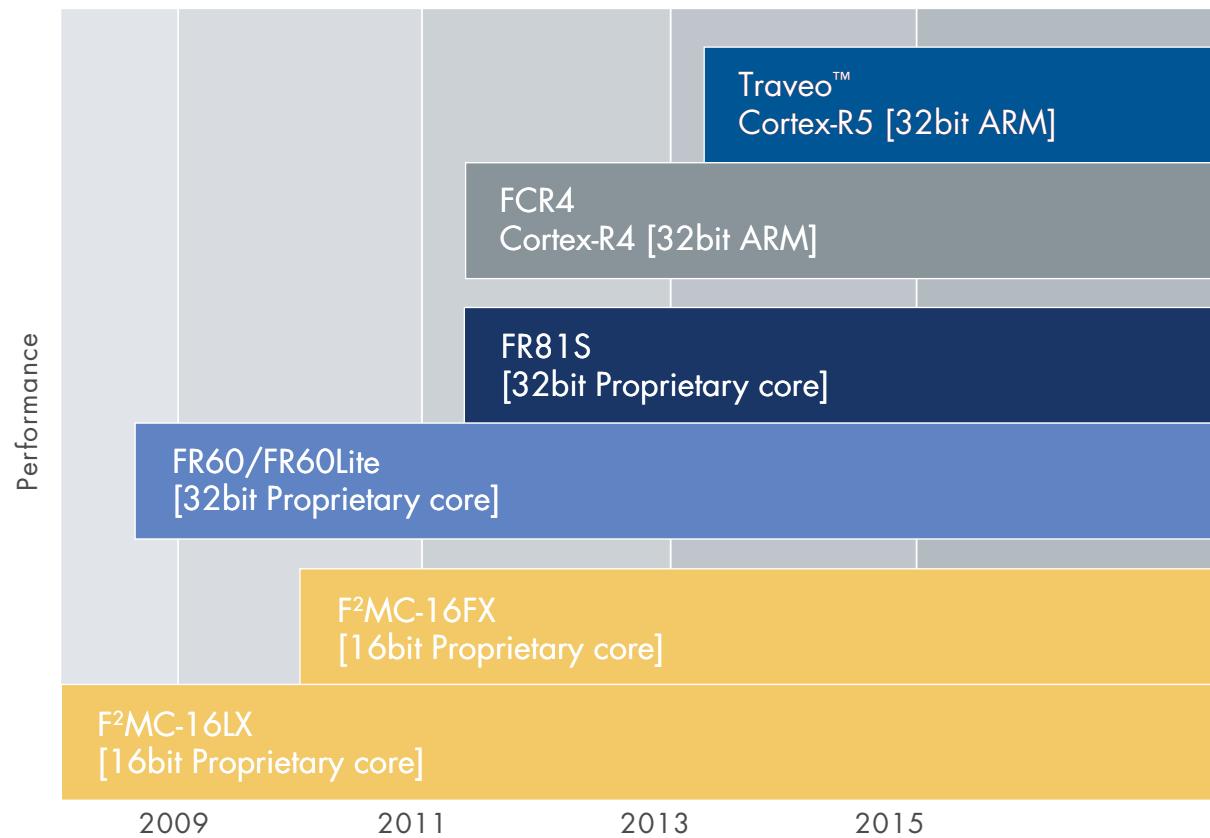
### FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

### 8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

## Automotive MCU Core Roadmap







## FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B460L	MB9BF464K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	15(2)	8(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I2C/LIN Selectable)	1	–	–	–	–	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF464L		LQFP-64 QFN-64																																	
	MB9BF465K		LQFP-48 QFN-48																																	
	MB9BF465L		LQFP-64 QFN-64																																	
	MB9BF466K		LQFP-48 QFN-48																																	
	MB9BF466L		LQFP-64 QFN-64																																	

## FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase inverter	Note	Evaluation Device
MB9BF160L	MB9BF164K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	15(2)	8(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I2C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF164L		LQFP-64 QFN-64																																	
	MB9BF165K		LQFP-48 QFN-48																																	
	MB9BF165L		LQFP-64 QFN-64																																	
	MB9BF166K		LQFP-48 QFN-48																																	
	MB9BF166L		LQFP-64 QFN-64																																	



## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/(unit)]	12bit AD Converter [bit/(unit)]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320T	MB9BF328S*	60	LQFP-144	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	<input checked="" type="checkbox"/>	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- 1ch (USB-Host/ USB-Function Selectable)	<input checked="" type="checkbox"/>	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF328T*																																		
	MB9BF329S*		LQFP-144	1.5M +64K	192K									122	154	-																			
	MB9BF329T*		LQFP-176 BGA-192											154	-	-																			
MB9B120T	MB9BF128S*	60	LQFP-144	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	<input checked="" type="checkbox"/>	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- 1ch (USB-Host/ USB-Function Selectable)	<input checked="" type="checkbox"/>	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF128T*													122	154	-																			
	MB9BF129S*		LQFP-144	1.5M +64K	192K									122	154	-																			
	MB9BF129T*		LQFP-176 BGA-192											154	-	-																			
MB9A420L	MB9AF421K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	<input checked="" type="checkbox"/>	FLASH	64K	4K	-	-	14	-	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	1 - - - -	<input checked="" type="checkbox"/>	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)										
	MB9AF421L		LQFP-64 QFN-64											19	51	-																			
MB9A120L	MB9AF121K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	<input checked="" type="checkbox"/>	FLASH	64K	4K	-	-	14	-	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- - - -	<input checked="" type="checkbox"/>	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)										
	MB9AF121L		LQFP-64 QFN-64											19	51	-																			

\* In development; \*\*Planning

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>CC</sub> [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer			Serial			Communication			Note	Evaluation Device				
BASIC GROUP																															
MB9A110A	MB9AF111LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	8	9(2)	12(3)	9(2)	16(3)	12(3)	16(3)	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)			Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)			Dual Timer	On-chip Debug (SWJ-DP)				
	MB9AF111MA		LQFP-80																												
	MB9AF111NA		LQFP-100 QFP-100 BGA-112																												
	MB9AF112LA		LQFP-64 QFN-64																												
	MB9AF112MA		LQFP-80																												
	MB9AF112NA		LQFP-100 QFP-100 BGA-112																												
	MB9AF114LA	40	LQFP-64 QFN-64																												
	MB9AF114MA		LQFP-80																												
	MB9AF114NA		LQFP-100 QFP-100 BGA-112																												
	MB9AF115MA		LQFP-80																												
	MB9AF115NA		LQFP-100 QFP-100 BGA-112**																												
	MB9AF116MA		LQFP-80																												
	MB9AF116NA		LQFP-100 QFP-100 BGA-112**																												
MB9A310K	MB9AF311K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)			Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)			1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock		On-chip Debug (SWJ-DP)
	MB9AF312K																														
MB9A110K	MB9AF111K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	64K+32K	16K	-	4	6	-	36	-	8(2)	-	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	✓	Dual Timer, Real Time Clock	On-chip Debug (SWJ-DP)				

\* In development; \*\*Planning



## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)																		On-chip Debug (SWJ-DP)				
	MB9AF341MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF341NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF342LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF342MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF342NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF344LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF344MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF344NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	8	-	51		12(2)															On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF141NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF142LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF142MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF142NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						
	MB9AF144LB		LQFP-64 QFN-64							8	-	51		12(2)																						
	MB9AF144MB		LQFP-80 BGA-96							11	✓	66		17(2)																						
	MB9AF144NB		LQFP-100 QFP-100 BGA-112							16	✓	83		24(2)																						

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>DD</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
ULTRA LOW LEAK GROUP																																					
MB9AAA0N	MB9AFAA1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AFAA1M		LQFP-80																																		
	MB9AFAA1N		LQFP-100 QFP-100																																		
	MB9AFAA2L		LQFP-64 QFN-64																																		
	MB9AFAA2M		LQFP-80																																		
	MB9AFAA2N		LQFP-100 QFP-100																																		
MB9A1A0N	MB9AF1A1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AF1A1M		LQFP-80																																		
	MB9AF1A1N		LQFP-100 QFP-100																																		
	MB9AF1A2L		LQFP-64 QFN-64																																		
	MB9AF1A2M		LQFP-80																																		
	MB9AF1A2N		LQFP-100 QFP-100																																		
MB9A130LA	MB9AF131LB	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	8K					8	52	8(1)	8(1)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)								
	MB9AF131KB		LQFP-48 QFN-48																																		
	MB9AF132LB		LQFP-64 QFN-64																																		
	MB9AF132KB		LQFP-48 QFN-48																																		



# Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C] [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC/Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device				
CAN/AUTOMOTIVE																																								
S6J3200	S6J323CKS	240	TEQFP -208	1.1 to 1.3	✓	Main Flash + Work Flash	2112K + 112K	TC-RAM: 128KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 2048KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	128	50(1)	120	46(1)	128	50(1)	120	46(1)	128	50(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LINUART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	-	30 x 4	32 x 4	ARM Cortex-R5, Ethernet AVB, Media-LB, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 2ch,	On-Chip Debug
	S6J323CKU	TEQFP -216 (0.4mm pitch)	3.0 to 3.6	4.5 to 5.5																																				
	S6J323CLS	TEQFP -208																																						
	S6J323CLU	TEQFP -216 (0.4mm pitch)																																						
	S6J324CKS	TEQFP -208																																						
	S6J324CKU	TEQFP -216 (0.4mm pitch)																																						
	S6J324CLS	TEQFP -216 (0.4mm pitch)																																						
	S6J324CLU	TEQFP -216 (0.4mm pitch)																																						
	S6J325CKS	TEQFP -208																																						
	S6J325CKU	TEQFP -216 (0.4mm pitch)																																						
	S6J325CLS	TEQFP -216 (0.4mm pitch)																																						
	S6J325CLU	TEQFP -208																																						
	S6J326CKS	TEQFP -208																																						
	S6J326CKU	TEQFP -216 (0.4mm pitch)																																						
	S6J326CLS	TEQFP -216 (0.4mm pitch)																																						
	S6J326CLU	TEQFP -216 (0.4mm pitch)																																						
	S6J327CKS	TEQFP -208																																						
	S6J327CKU	TEQFP -216 (0.4mm pitch)																																						
	S6J327CLS	TEQFP -208																																						
	S6J327CLU	TEQFP -216 (0.4mm pitch)																																						
	S6J328CKS	TEQFP -208																																						
	S6J328CKU	TEQFP -216 (0.4mm pitch)																																						
	S6J328CLS	TEQFP -216 (0.4mm pitch)																																						
	S6J328CLU	TEQFP -216 (0.4mm pitch)																																						

## FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic, NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	









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Doc# 002-06949 Rev.\*A