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What is "[Embedded - Microcontrollers](#)"?

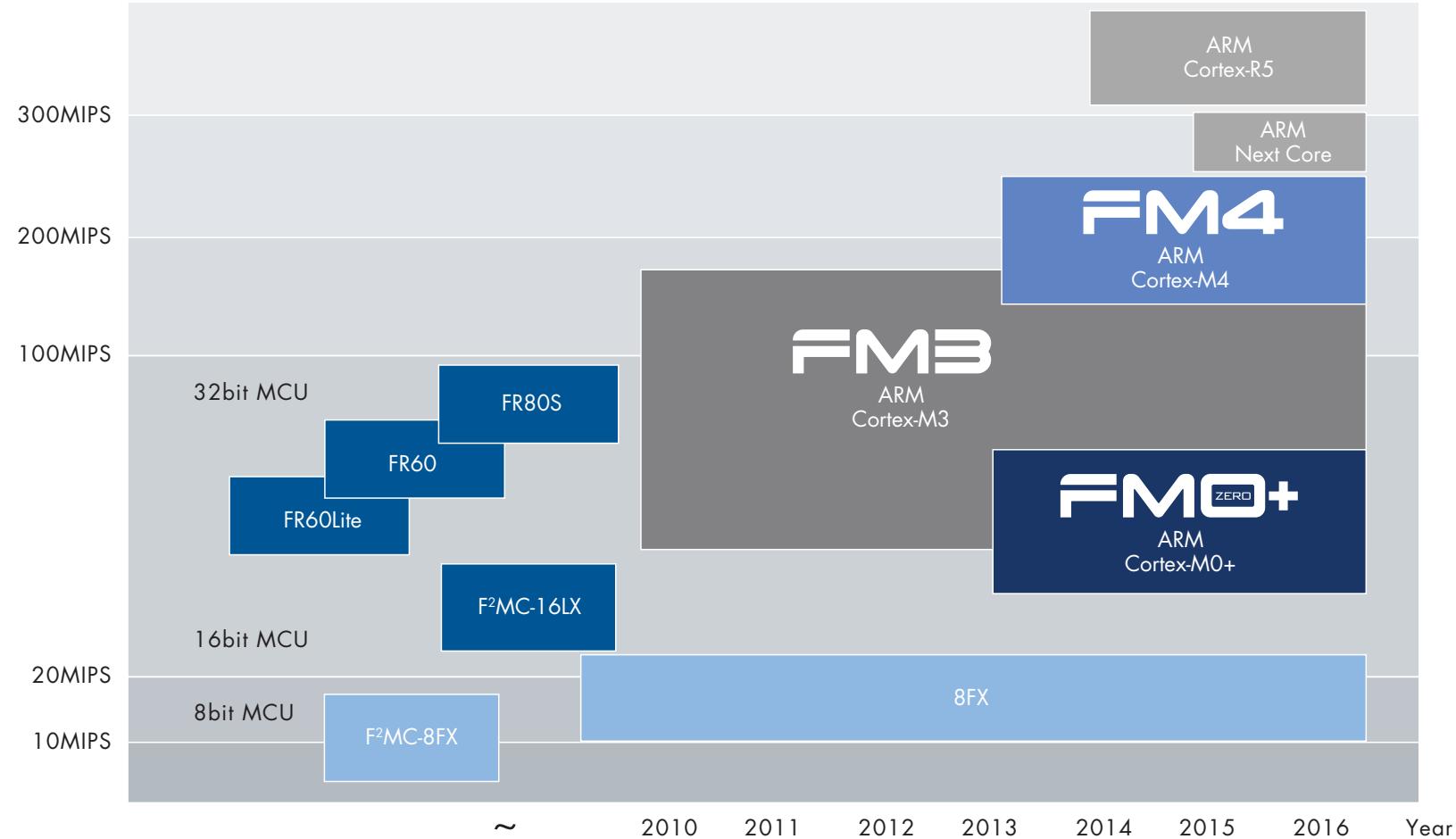
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	F ² MC-16FX
Core Size	16-Bit
Speed	56MHz
Connectivity	CANbus, EBI/EMI, I ² C, LINbus, SCI, UART/USART
Peripherals	DMA, LCD, LVD, LVR, POR, PWM, WDT
Number of I/O	96
Program Memory Size	288KB (288K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	16K x 8
Voltage - Supply (Vcc/Vdd)	3V ~ 5.5V
Data Converters	A/D 16x10b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	Surface Mount
Package / Case	120-LQFP
Supplier Device Package	120-LQFP (16x16)
Purchase URL	https://www.e-xfl.com/product-detail/infineon-technologies/mb96f386rscpmc-gs-194e2

Consumer and Industrial MCU Core Roadmap



Consumer and Industrial MCU Family



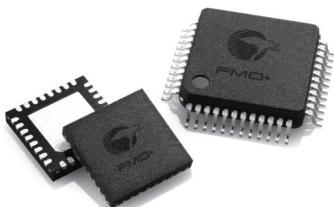
FM4 FAMILY

Cypress ARM® Cortex®-M4F microcontroller family is a high range line providing maximum CPU frequency of 200MHz, a high speed flash memory with DSP and FPU hardware instructions. Customers can select the best fitting device from a range of products, coming in packages from 48 pin to 216 pin and flash memory densities between 256KB and 2MB. The wide operation supply voltage range up to 5.5V which improves the signal to noise ratio, results in a robust design and is unique among Cortex-M4F microcontroller families. The MCUs are designed for applications that require advanced, high-speed computing performance such as general-purpose inverters, servomotors, PLCs and other industrial equipment, as well as inverter-based home appliances such as washing machines and air conditioners.



FM3 FAMILY

Cypress ARM Cortex-M3 microcontroller family is a scalable platform for many industrial applications. Customers can select the best fitting device from a range of products, coming in packages from 32 pin to 176 pin and flash memory densities between 32KB and 1MB. With a maximum CPU frequency of 144MHz and high speed flash memory, FM3 supports the fastest ARM Cortex-M3 devices on the market. The wide operation supply voltage range up to 5.5V, which improves the signal to noise ratio, results in a robust design and is quite unique among Cortex-M3 microcontroller families. The FM3 MCU family is split into four groups: high performance, basic, low power and ultra low leakage groups. The main differences between the groups are CPU operation frequency and supply voltage. All products are based on the same architecture (software compatible), use the same peripherals and are pin compatible in most cases. The ultra low leakage line products are based on an optimized low leakage process technology. Development tools and evaluation boards are offered from different vendors and Cypress.



FMO+ FAMILY

The Cypress FMO+ family, which is based on the ARM Cortex-M0+ core, is designed for low power and cost-sensitive applications such as white goods, sensors, meters, HMI systems, power tools and Internet of Things (IoT) battery powered or energy harvesting wearable devices. These microcontrollers can be easily embedded into systems adopting 8-, 16- or 32-bit MCUs, accelerating product development and reducing development costs. The FMO+ family includes two groups for ultra-low-power and cost-effective applications. The devices in the ultra-low-power group have an operating voltage range of 1.65V to 3.6V, and a maximum CPU clock frequency of 40MHz, a RUN mode current of 70 µA/MHz, an RTC mode current of 0.7 µA and wake-up time of approximately 40 µs.

8FX FAMILY

Cypress 8FX MCU family is a high-performance 8-bit microcontroller utilizing a different embedded flash memory size. This series uses the F2MC-8FX CISC CPU, which offers industry leading class performance of an 8-bit microcontroller unit enabling more instructions to be executed per cycle. On top of delivering industry class performance MCUs, the 8FX family also delivers low power efficient MCU products for the customer's usage. This series also features a variety of on-chip timers, A/D converters, analog and digital peripheral and communication interfaces such as LIN-UART (Local Interconnect Network Universal Asynchronous Receiver-Transmitter), CAN (controller area network) and I2C (Inter-Integrated Circuit) interface for various application usages. For easy development, the 8FX family also employs a 1-line on-chip debug that uses only one pin on the microcontroller, thereby minimizing the number of pins used for debugging in product development. Cypress also provides easy to use and cost competitive development starter kits and development environments for this MCU series.

Automotive MCU Family

TRAVEO FAMILY

The Cypress Traveo™ family expands the company's automotive application coverage, scalability and high performance into one line-up and at the same time adds new features to fulfill the latest requirements of the automotive industry. Based on the powerful ARM® Cortex®-R5 and R5F core in single and dual core operations, it offers state-of-the-art real-time performance, safety and security features.

The family supports the latest in-car networks and offers high performance graphics engines optimized for a minimum memory footprint and embeds dedicated features to increase data security in the car.

High-level Traveo Features

- ARM Cortex-R5 core
- Dual core
- Embedded twin-motor control with internal R/D converter
- Embedded 2D and 3D graphics engines
- High performance embedded flash memory
- Cypress HyperBus™: High speed serial interfaces to connect external memory
- Qualified for automotive use (AEC-Q100)
- Software support for Autosar, graphics drivers and more

FR FAMILY

Cypress 32bit MCU families have been designed in close co-operation with major automotive customers worldwide and inherit the high-performance core of Cypress's proprietary FR MCU architecture. They support communication interfaces such as CAN, FlexRay and LIN as well as up to 2MB on-chip memory capacity. The latest members (MB915xx) also include a single precision Floating Point Unit (FPU) providing additional computing power required by complex control algorithms.

This high computing performance combined with powerful peripheral functions such as on-chip graphics controller and motor control macros, offers a higher grade of flexibility and lower cost for automotive as well as industrial applications.

Many devices offer an external bus interface which can be connected to Cypress's stand alone FlexRay controller or to the latest generation of graphics controllers in order to build full-featured dashboards, driver information systems or advanced body systems.

FCR4 FAMILY

The FCR4 family has been specifically designed to offer an innovative, scalable solution for hybrid instrument clusters, which combine traditional meters and graphical displays. The devices offer a powerful architecture based on the ARM Cortex-R4 core and Cypress's 2D graphics engine.

High-level FCR4 Features

- ARM Cortex-R4 core
- Embedded 2D graphics engine
- 2MB flash, 64KB E2Flash
- Up to 208KB RAM
- Features including real-time clock/auto calibration, sound generator and I2S

F2MC-16FX FAMILY

Cypress 16-bit flexible microcontroller series offers a scalable family concept approach to a variety of automotive and industrial applications. The scalable flash/ROM/RAM sizes with different mixtures of peripherals saves development time and costs. CAN and LIN support, on-chip LCD controller, SMC (stepper motor controller), I²C bus interface, analog input channels, external bus interface, selectable port levels for CMOS, TTL and Automotive Levels are some of the enhanced features. A security feature is incorporated, preventing unauthorized reading of the contents of the flash memory.



FM4 Family – 32bit Microcontrollers

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
S6E2C1-E	S6E2C10H2A	200	LQFP-144	2.7 to 5.5	✓	-	0M	256K	-	8	32	✓	120	-	24 (3)	12 x 2	Multi-Function Timer x 3units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable), PPG 9ch	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 4	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	✓	Dual Timer, Real Timer Clock, Unique ID, DSTC x 256ch, SDC, I2S 1ch(without 144pin), HDMI-CEC 2ch, High Speed Quad SPI x1(without 144pin), Programmable CRC, Chipher	On-Chip Debug (SWJ-DP/ETM/ HTM)								
	S6E2C10J2A		LQFP-176 BGA-192																																	
	S6E2C10L2A		LQFP-216																																	
MB9B560R	MB9BF566M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	24(3)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF566N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF566R		LQFP-120 BGA-144																																	
	MB9BF567M		LQFP-80																																	
	MB9BF567N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF567R		LQFP-120 BGA-144																																	
	MB9BF568M		LQFP-80																																	
	MB9BF568N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF568R		LQFP-120 BGA-144																																	
MB9B460R	MB9BF466M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	24(3)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF466N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF466R		LQFP-120 BGA-144																																	
	MB9BF467M		LQFP-80																																	
	MB9BF467N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF467R		LQFP-120 BGA-144																																	
	MB9BF468M	160	LQFP-80	160	✓	Main Flash +Work Flash	1024K +32K	128K	-	8	16	✓	63	-	16(3)	24(3)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF468N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF468R		LQFP-120 BGA-144																																	
MB9B360R	MB9BF366M	160	LQFP-80	2.7 to 5.5	✓	Main Flash +Work Flash	512K +32K	64K	-	8	16	✓	63	-	16(3)	24(3)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)									
	MB9BF366N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF366R		LQFP-120 BGA-144																																	
	MB9BF367M		LQFP-80																																	
	MB9BF367N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF367R		LQFP-120 BGA-144																																	
	MB9BF368M		LQFP-80																																	
	MB9BF368N		LQFP-100 QFP-100 BGA-112																																	
	MB9BF368R		LQFP-120 BGA-144																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)								
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

FM4 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	i _C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x col]	Three-phase inverter	Note	Evaluation Device
HIGH-PERFORMANCE GROUP																																				
MB9B500B	MB9BF504NB	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF504RB		LQFP-120																																	
	MB9BF505NB		LQFP-100 BGA-112																																	
	MB9BF505RB		LQFP-120																																	
	MB9BF506NB		LQFP-100 BGA-112																																	
	MB9BF506RB		LQFP-120																																	
MB9B400A	MB9BF404NA	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	2	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF404RA		LQFP-120																																	
	MB9BF405NA		LQFP-100 BGA-112																																	
	MB9BF405RA		LQFP-120																																	
	MB9BF406NA		LQFP-100 BGA-112																																	
	MB9BF406RA		LQFP-120																																	
MB9B300B	MB9BF304NB	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	256K 384K 512K	32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF304RB		LQFP-120																																	
	MB9BF305NB		LQFP-100 BGA-112																																	
	MB9BF305RB		LQFP-120																																	
	MB9BF306NB		LQFP-100 BGA-112																																	
	MB9BF306RB		LQFP-120																																	
MB9B100A	MB9BF102NA	80	LQFP-100 BGA-112	2.7 to 5.5	✓	FLASH	128K 256K 384K 512K	16K 32K 48K 64K	8 - 16	✓	80 100 80 100 80 100	- - - - - -	16(3)	- - - - - -	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)											
	MB9BF102RA		LQFP-120																																	
	MB9BF104NA		LQFP-100 BGA-112																																	
	MB9BF104RA		LQFP-120																																	
	MB9BF105NA		LQFP-100 BGA-112																																	
	MB9BF105RA		LQFP-120																																	
	MB9BF106NA		LQFP-100 BGA-112																																	
	MB9BF106RA		LQFP-120																																	
MB9B520M	MB9BF521K	72	LQFP-48 QFN-48	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)													
	MB9BF521L		LQFP-64 QFN-64																																	
	MB9BF521M		LQFP-80 BGA-96																																	
	MB9BF522K		LQFP-48 QFN-48																																	
	MB9BF522L		LQFP-64 QFN-64																																	
	MB9BF522M</																																			

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VDD [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit ADC Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320M	MB9BF321K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF321L																																		
	MB9BF321M																																		
	MB9BF322K																																		
	MB9BF322L																																		
	MB9BF322M																																		
	MB9BF324K																																		
	MB9BF324L																																		
	MB9BF324M																																		
MB9B120M	MB9BF121K	72	LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96 LQFP-48 QFN-48 LQFP-64 QFN-64 LQFP-80 BGA-96	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	64K +32K 128K +32K 256K +32K	16K -	8	14 19 23 14 19 23 14 19 23	35 50 65 35 50 65 35 50 65	14(2) 23(2) 26(2) 14(2) 23(2) 26(2) 14(2) 23(2) 26(2)	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	1	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable) Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Unique ID, Real Time Clock	On-chip Debug (SWJ-DP)											
	MB9BF121L																																		
	MB9BF121M																																		
	MB9BF122K																																		
	MB9BF122L																																		
	MB9BF122M																																		
	MB9BF124K																																		
	MB9BF124L																																		
	MB9BF124M																																		
MB9B120J	MB9BF121J	72	LQFP-32 QFN-32	2.7 to 5.5	✓	FLASH	64K	8K	-	4	7	-	23	-	8(1)	-	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 4ch (UART/CSIO/I2C/LIN Selectable)	-	-	-	-	-	✓	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SW-DP)						
MB9B520T	MB9BF528S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF528T*																																		
	MB9BF529S*																																		
	MB9BF529T*																																		
MB9B420T	MB9BF428S*	60	LQFP-144 LQFP-176 BGA-192 LQFP-144 LQFP-176 BGA-192	2.7 to 5.5	✓	Dual Op. Flash (Main area + Work area)	1M +64K 1.5M +64K	160K 192K	-	8	32	✓	122 154 122 154	-	24(2) <td data-kind="parent" data-rs="4">10bit x 2</td> <td data-kind="parent" data-rs="4">Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)</td> <td data-kind="parent" data-rs="4">Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)</td> <td data-kind="parent" data-rs="4">QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2</td> <td data-kind="parent" data-rs="4">1</td> <td data-kind="parent" data-rs="4">Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)</td> <td data-kind="parent" data-rs="4">1ch (USB-Host/ USB-Function Selectable)</td> <td data-kind="parent" data-rs="4">-</td> <td data-kind="parent" data-rs="4">✓</td> <td data-kind="parent" data-rs="4">CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID</td> <td data-kind="parent" data-rs="4">On-chip Debug (SWJ-DP/ETM)</td>	10bit x 2	Multi-Function Timer x 1 unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1 QPRC x 2 QPRC x 1 QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/ USB-Function Selectable)	-	✓	CAN: 32Msg-buffer, Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)									
	MB9BF428T*																																		
	MB9BF429S*																																		
	MB9BF429T*																																		

FM3 Family – 32bit Microcontrollers

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	-	8	8	-	51		12(2)																On-chip Debug (SWJ-DP)					
	MB9AF341MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF341NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF342LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF342MB		LQFP-80 BGA-96								11	✓	66		17(2)		-	-	-	-																
	MB9AF342NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF344LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF344MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF344NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	-	8	8	-	51		12(2)														On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF141NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF142LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF142MB		LQFP-80 BGA-96								11	✓	66		17(2)		-	-	-	-																
	MB9AF142NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF144LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF144MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF144NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					

FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{DD} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Timer	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I ² C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
ULTRA LOW LEAK GROUP																																					
MB9AAA0N	MB9AFAA1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)							
	MB9AFAA1M		LQFP-80																																		
	MB9AFAA1N		LQFP-100 QFP-100																																		
	MB9AFAA2L		LQFP-64 QFN-64																																		
	MB9AFAA2M		LQFP-80																																		
	MB9AFAA2N		LQFP-100 QFP-100																																		
MB9A1A0N	MB9AF1A1L	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	12K					8	52	9(1)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	24 x 4 or 20 x 8	HDMI-CEC/Remote Control Reception x 2, Real Time Clock	On-chip Debug (SWJ-DP)							
	MB9AF1A1M		LQFP-80																																		
	MB9AF1A1N		LQFP-100 QFP-100																																		
	MB9AF1A2L		LQFP-64 QFN-64																																		
	MB9AF1A2M		LQFP-80																																		
	MB9AF1A2N		LQFP-100 QFP-100																																		
MB9A130LA	MB9AF131LB	20	LQFP-64 QFN-64	1.8 to 5.5	✓	FLASH	64K	8K					8	52	8(1)	8(1)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	–	Multi Function Serial x 8ch (UART/CSIO/I ² C Selectable)	–	–	–	–	–	Real Time Clock	On-chip Debug (SWJ-DP)		
	MB9AF131KB		LQFP-48 QFN-48																																		
	MB9AF132LB		LQFP-64 QFN-64																																		
	MB9AF132KB		LQFP-48 QFN-48																																		

8FX – 8bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/umt]	12bit AD Converter [bit/umt]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device	
LCD CONTROLLERS																																					
MB95610H	MB95F613H	16	LQFP-80	2.4 to 5.5	✓	Dual Op. Flash	12K	512					40	4(1)				+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2															Clock Supervisor	On-chip Debug		
	MB95F613K						20K	1024	-	-	8	-	41						Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																	
	MB95F614H						36K	1024					40								Clock Supervisor	On-chip Debug															
	MB95F614K												41								Low-voltage Detection Reset, Clock Supervisor	On-chip Debug															
	MB95F616H												40									Clock Supervisor	On-chip Debug														
MB95710M	MB95F714J	16	LQFP-80	1.8 to 5.5	✓	Dual Op. Flash	20K	512					75	8(1)				+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2																Low-voltage Detection Reset, Clock Supervisor	On-chip Debug	
	MB95F714M						36K	1K	-	-	8	-	74						Clock Supervisor	On-chip Debug																	
	MB95F716J												75								Low-voltage Detection Reset, Clock Supervisor	On-chip Debug															
	MB95F716M												74								Clock Supervisor	On-chip Debug															
	MB95F718J						60K	2K					75								Low-voltage Detection Reset, Clock Supervisor	On-chip Debug															
MB95770M	MB95F774J	16	LQFP-64	1.8 to 5.5	✓	Dual Op. Flash	20K	512					59	8(1)				+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	2																Low-voltage Detection Reset, Clock Supervisor	On-chip Debug	
	MB95F774M						36K	1K	-	-	8	-	58						Clock Supervisor	On-chip Debug																	
	MB95F776J												59								Low-voltage Detection Reset, Clock Supervisor	On-chip Debug															
	MB95F776M												58								Clock Supervisor	On-chip Debug															
	MB95F778J						60K	2K					59								Low-voltage Detection Reset, Clock Supervisor	On-chip Debug															
INVERTER																																					
MB95630H	MB95F632H	16	LQFP-32 SH-DIP-32 QFN-32	2.4 to 5.5	✓	Dual Op. Flash	8K	256					28	8(1)				+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	3			1	1	1	-	1	-	-	-	-	-	-	-	Low-voltage Detection Reset, Multi-pulse Generator (Reload/PPG/Waveform Sequencer), Clock Supervisor	On-chip Debug		
	MB95F632K						12K	512					29						Clock Supervisor	On-chip Debug																	
	MB95F633H						20K		-	-	10	-	28						Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																	
	MB95F633K												29							Clock Supervisor	On-chip Debug																
	MB95F634H												28							Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																
MB95690K	MB95F694K	16	LQFP-44 LQFP-48 LQFP-52 QFN-48	2.88 to 5.5	✓	Dual Op. Flash	20K	512					29	45 LQFP44 only: 8(1)			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	3			1	1	1	-	1	-	-	-	-	-	-	-	Low-voltage Detection Reset, Multi-pulse Generator (Reload/PPG/Waveform Sequencer), Clock Supervisor	On-chip Debug			
	MB95F696K						36K	1K	-	-	8	-	29					Clock Supervisor	On-chip Debug																		
	MB95F698K						60K	2K					29					Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																		
STANDARD																																					
MB95560H	MB95F562H	16	SOP-20 TSSOP-20 QFN-32	2.4 to 5.5	✓	Dual Op. Flash	8K	240					16	6(1)			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	-														Clock Supervisor	On-chip Debug				
	MB95F562K						12K		-	-	6	-	17					Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																		
	MB95F563H						20K		-	-			16					Clock Supervisor	On-chip Debug																		
	MB95F564H												17					Low-voltage Detection Reset, Clock Supervisor	On-chip Debug																		
	MB95F564K												17					Clock Supervisor	On-chip Debug																		
MB95570H	MB95F572H	16	SOP-8	2.4 to 5.5	✓	Dual Op. Flash	8K	240					4	2(1)			+ 8/16bit Compound Timer x 2ch (Interval Timer/PWM/PWC/ Input Capture Selectable)	-														Clock Supervisor	On-chip Debug				
	MB95F572K																																				

Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C [ch]]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC/Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
MB9D560	MB9DF564MA	200	TEQFP -208	1.1 to 1.3	Main Flash +Work Flash	(640K + 64K) x2	64K x2			16	8	-	125	149	32(1)	8(2)	10bit x 2	32bitFree-Run Timer x 5ch 32bitInput Capture x 3unit 6ch 16bitFree-Run Timer x 20ch 16bit Input Capture x 8unit 15ch 16bitOutput Compare x 12unit 24ch Waveform Generator x 4unit 24ch	16bit Base Timer x 12ch (PWM/PPG/Reload/PWC Selectable)	4	-	Multi Function Serial x 5ch (LIN/UART/SIO Selectable)	3	-	-	-	-	✓	ARM Cortex-R5 CAN: 64msb, RDC x 2unit, Motor vector accelerato r x 2unit Models with A suffix on part number have no built-in FlexRay. Models with G suffix on part number have built-in FlexRay.	On-Chip Debug						
	MB9DF564MG					(896K + 64K) x2	96K x2																													
	MB9DF565MA					(1152K + 64K) x2	128K x2																													
	MB9DF565MG					(640K + 64K) x 2	64K x 2																													
	MB9DF566MA					(896K + 64K) x 2	96K x 2																													
	MB9DF566MG					(1152K + 64K) x 2	128K x 2																													
	MB9DF564ML					(640K + 64K) x 2	64K x 2																													
	MB9DF564MQ					(896K + 64K) x 2	96K x 2																													
	MB9DF565ML					(1152K + 64K) x 2	128K x 2																													
MB9D560	MB9DF565MQ					(640K + 64K) x 2	64K x 2																													
	MB9DF566ML					(896K + 64K) x 2	96K x 2																													
	MB9DF566MLQ					(1152K + 64K) x 2	128K x 2																													
	MB9DF566LG					(640K + 64K) x 2	64K x 2																													
	MB9DF564LL					(896K + 64K) x 2	96K x 2																													
	MB9DF564LQ					(1152K + 64K) x 2	128K x 2																													
	MB9DF565LL					(640K + 64K) x 2	64K x 2																													
	MB9DF565LQ					(896K + 64K) x 2	96K x 2																													
	MB9DF566LL					(1152K + 64K) x 2	128K x 2																													
	MB9DF566LQ																																			

Traveo Family – 32bit Microcontrollers

Traveo Family – 32bit Microcontrollers

FCR4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA/Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seq x com]	Three-phase Inverter	Note	Evaluation Device
HYBRID AUTOMOTIVE INSTRUMENTS CLUSTER																																				
MB9DF125	MB9DF125PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	1088	128	8	8	32	-	123	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9DF125EPMC					FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC); 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 1 ch, NMI (intern/extern): 32/1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9DF126	MB9DF126BPMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	208	16	8	32	-	110	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	3	-	-	ARM Cortex R4, APIX Remote handler with 2 ch AIC (APIX Inter Connect), APIX1 Phy: 1 ch, Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 64KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, 2 ch, Quad SPI Flash Interface: 1 ch (MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
MB9EF226	MB9EF226PMC	128	LQFP-176	1.1 to 1.3 3.0 to 5.5	✓	FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 4 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface: 2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	
	MB9EF226EPMC					FLASH	2176	128	16	8	32	-	117	50(1)	-	-	8	8	8	8	10	-	-	24	2	-	1	2	3	2	2	-	-	ARM Cortex R4, 2D Graphics Engine: scaler, color palette, gamma correction, blending, raster operation, various alpha blending modes, run-length decoding, hor/ver flip and rotation, affine transformations, Embedded Video-RAM: 1MB, TFT Output Interface: RGB888/RSDS Output, SHE (Secure Hardware Extension), Real Time Clock, Stepper Motor Controller (SMC): 6 ch, Data Flash: 48KB, Windows Watchdog, Clock supervisor, Sound Generator, I2S: 2 ch, Quad SPI Flash Interface2 ch/1 ch for MCU and 1 ch for Graphic), NMI (intern/extern): 32/1 ch, MediaLB (3 wire): 1 ch, CRC Hardware Module: 1 ch, Protection Unit: MPU, PPU, TPU, various Power Down modes	On-chip Debug	

F²MC-16FX – 16bit Microcontrollers

F²MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V _{CC} [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device					
AUTOMOTIVE																																			
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1				
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1				
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1				
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F647R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
MB96650	MB96F653A	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug		
	MB96F653R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F655A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F657R						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1				
	MB96F673A						64.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96670	MB96F673R	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675A						128.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F675R						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	24 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683A						64.5K +32K	4K	16K	-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
	MB96F685A						256.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	-	8bit x 8 16bit x 4	-	-	1	-	2	-	-	32 x 4	-	SMC x 2ch Sound generator Option without CAN	On-chip Debug
MB96690	MB96F693A	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	8K	16K	-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F693R						128.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695A						256.5K +32K	8K		-	4	16	-	(Single clock) 79 77 (Dual clock)	27(1)	-	-	4	2	6	5	-	-	8bit x 14 16bit x 10	-	-	1	-	5	-	-	36 x 4	-	SMC x 4ch Sound generator x 2ch Option without CAN	On-chip Debug
	MB96F695R						384.																												