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#### What is "[Embedded - Microcontrollers](#)"?

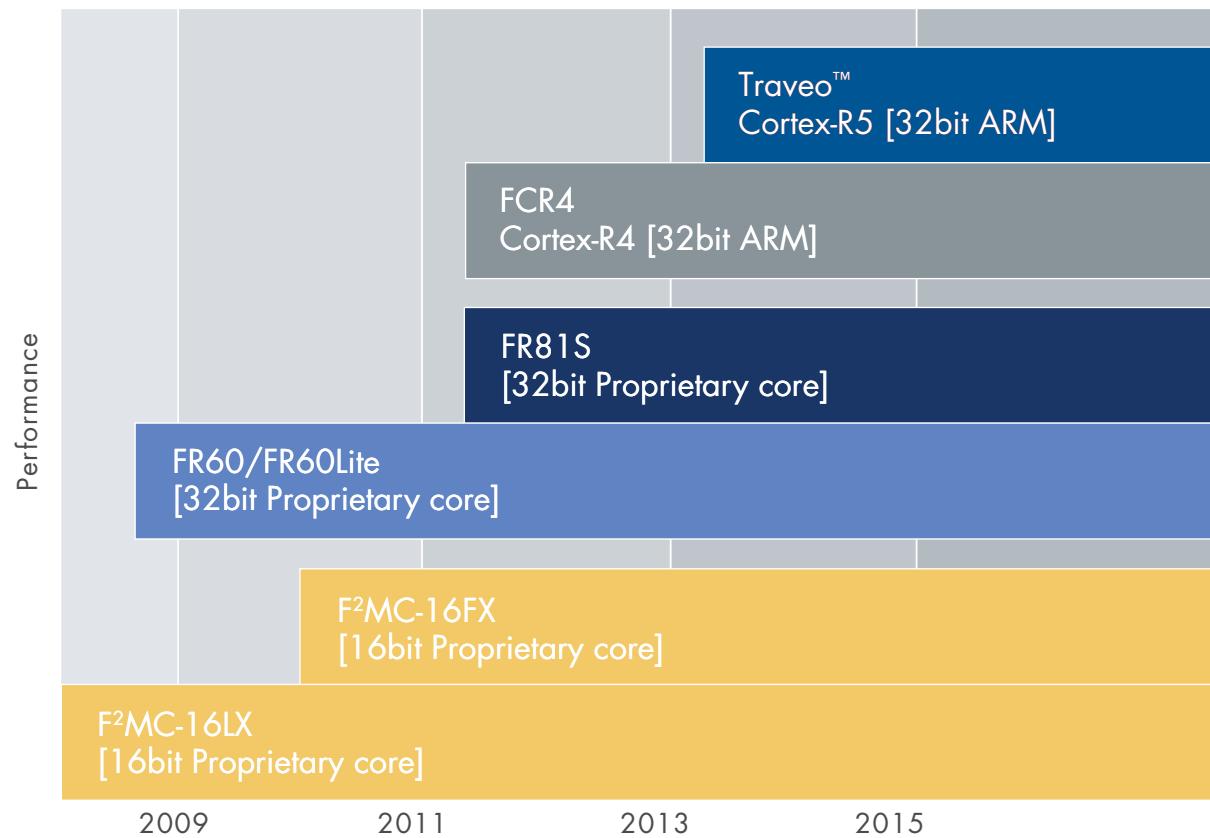
"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

#### Applications of "[Embedded - Microcontrollers](#)"

##### Details

Product Status	Obsolete
Core Processor	F <sup>2</sup> MC-16FX
Core Size	16-Bit
Speed	56MHz
Connectivity	CANbus, EBI/EMI, I <sup>2</sup> C, LINbus, SCI, UART/USART
Peripherals	DMA, LCD, LVD, LVR, POR, PWM, WDT
Number of I/O	96
Program Memory Size	288KB (288K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	16K x 8
Voltage - Supply (Vcc/Vdd)	3V ~ 5.5V
Data Converters	A/D 16x10b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	Surface Mount
Package / Case	120-LQFP
Supplier Device Package	120-LQFP (16x16)
Purchase URL	<a href="https://www.e-xfl.com/product-detail/infineon-technologies/mb96f386rscpmc-gs-199e2">https://www.e-xfl.com/product-detail/infineon-technologies/mb96f386rscpmc-gs-199e2</a>

## Automotive MCU Core Roadmap



## **FM4 Family – 32bit Microcontrollers**

## **FM4 Family – 32bit Microcontrollers**

# FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg. com]	Three-phase inverter	Note	Evaluation Device
MB9B160R	MB9BF166M	160	LQFP-80	2.7 to 5.5	✓	Main Flash + Work Flash	512K +32K	64K					63	16(3)	24(3)	12 x 2	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I²C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, DSTC x 128ch, Unique ID, SD Card I/F	On-chip Debug (SWJ-DP/ETM)								
	MB9BF166N		LQFP-100																																	
	MB9BF166R		QFP-100																																	
	MB9BF167M		BGA-112																																	
	MB9BF167N		LQFP-80																																	
	MB9BF167R		QFP-100																																	
	MB9BF168M		BGA-144																																	
	MB9BF168N		LQFP-80																																	
	MB9BF168R		QFP-100																																	
	MB9BF168R		BGA-112																																	
MB9B560L	MB9BF564K	160	LQFP-48	2.7 to 5.5	✓	Main Flash + Work Flash	256K +32K	32K					15	33	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I²C/LIN Selectable)	1	1ch (USB-Host/ USB-Function Selectable)	-	-	-	-	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF564L		LQFP-64																																	
	MB9BF565K		LQFP-48																																	
	MB9BF565L		LQFP-64																																	
	MB9BF566K		LQFP-48																																	
	MB9BF566L		LQFP-64																																	

## FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chn/unit]	12bit AD Converter [chn/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B460L	MB9BF464K	160	LQFP-48 QFN-48	2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	15(2)	8(2)	12 x 2	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSI0/I2C/LIN Selectable)	1	–	–	–	–	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF464L		LQFP-64 QFN-64																																	
	MB9BF465K		LQFP-48 QFN-48																																	
	MB9BF465L		LQFP-64 QFN-64																																	
	MB9BF466K		LQFP-48 QFN-48																																	
	MB9BF466L		LQFP-64 QFN-64																																	

## FM4 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC[V]	Sub-Clock	Memory Type	ROM [bytes]	RAM [bytes]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DAConverter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg 7 com]	Three-phase inverter	Note	Evaluation Device
MB9B360L	MB9BF364K	160	LQFP-48 QFN-48	2.7 to 5.5 2.7 to 5.5	✓	Main Flash +Work Flash	256K +32K	32K	8	-	15	16	33	48	8(2)	12 x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 2units (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 6ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 6ch (UART/CSIO/I2C/LIN Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	-	✓	Dual Timer, Real Time Clock, Unique ID, DSTC x 128ch	On-chip Debug (SWJ-DP)							
	MB9BF364L		LQFP-64 QFN-64																																	
	MB9BF365K		LQFP-48 QFN-48																																	
	MB9BF365L		LQFP-64 QFN-64																																	
	MB9BF366K		LQFP-48 QFN-48																																	
	MB9BF366L		LQFP-64 QFN-64																																	

## **FM3 Family – 32bit Microcontrollers**

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [bit/(unit)]	12bit AD Converter [bit/(unit)]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg com]	Three-phase Inverter	Note	Evaluation Device
BASIC GROUP																																			
MB9B320T	MB9BF328S*	60	LQFP-144	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	<input checked="" type="checkbox"/>	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- 1ch (USB-Host/ USB-Function Selectable)	<input checked="" type="checkbox"/>	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF328T*																																		
	MB9BF329S*		LQFP-144	1.5M +64K	192K									122	122	-																			
	MB9BF329T*		LQFP-176 BGA-192											154	154	-																			
MB9B120T	MB9BF128S*	60	LQFP-144	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash (Main area + Work area)	1M +64K	160K	-	8	32	<input checked="" type="checkbox"/>	122	154	-	24(2)	10bit x 2	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 16ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	QPRC x 2	1	Multi Function Serial x 16ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- 1ch (USB-Host/ USB-Function Selectable)	<input checked="" type="checkbox"/>	Dual Timer, HDMI-CEC/Remote Control Reception x 2, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP/ETM)								
	MB9BF128T*													122	122	-																			
	MB9BF129S*		LQFP-144	1.5M +64K	192K									154	154	-																			
	MB9BF129T*		LQFP-176 BGA-192											154	154	-																			
MB9A420L	MB9AF421K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	<input checked="" type="checkbox"/>	FLASH	64K	4K	-	-	14	-	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	1 - - - -	<input checked="" type="checkbox"/>	CAN: 32Msg-buffer, Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)										
	MB9AF421L		LQFP-64 QFN-64											19	19	-																			
MB9A120L	MB9AF121K	40	LQFP-48 LQFP-52 QFN-48	2.7 to 5.5	<input checked="" type="checkbox"/>	FLASH	64K	4K	-	-	14	-	36	-	8(1)	10bit x 1	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 3ch/PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 8ch (Reload/PPG/PWM/ PWC Selectable)	-	1	Multi Function Serial x 4ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	- - - -	<input checked="" type="checkbox"/>	Dual Timer, Real Time Clock, Unique ID	On-chip Debug (SWJ-DP)										
	MB9AF121L		LQFP-64 QFN-64											19	19	-																			

\* In development; \*\*Planning

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>CQ</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [kByte]	DMAc [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]			Three-phase inverter		Note	Evaluation Device
BASIC GROUP																																	
MB9A310A	MB9AF311LA	40	LQFP-64 QFN-64	2.7 to 5.5	✓	FLASH	64K	16K	8	9(2)	12(3)	16(3)	9(2)	12(3)	16(3)	16(3)	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	QPRC x 2	1	Multi Function Serial x 8ch (UART/CSIO/I2C/LIN Selectable)	1ch (USB-Host/USB-Function Selectable)	-	✓	Dual Timer	On-chip Debug (SWJ-DP/ETM)								
	MB9AF311MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF311NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF312MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF312NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF314LA		LQFP-64 QFN-64																							On-chip Debug (SWJ-DP)							
	MB9AF314MA		LQFP-80																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF314NA		LQFP-100 QFP-100 BGA-112																							On-chip Debug (SWJ-DP)							
	MB9AF315MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF315NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							
	MB9AF316MA		LQFP-80																							On-chip Debug (SWJ-DP)							
	MB9AF316NA		LQFP-100 QFP-100 BGA-112**																							On-chip Debug (SWJ-DP/ETM)							

## FM3 Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub-Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I²C [ch]	UART/SI/O [ch]	SIO [ch]	LIN/MAR/T/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
LOW-POWER GROUP																																				
MB9A340NA	MB9AF341LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	-	8	8	-	51		12(2)																On-chip Debug (SWJ-DP)					
	MB9AF341MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF341NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF342LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF342MB		LQFP-80 BGA-96								11	✓	66		17(2)		-	-	-	-																
	MB9AF342NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF344LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF344MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF344NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
MB9A140NA	MB9AF141LB	40	LQFP-64 QFN-64	1.65 to 3.6	✓	Dual Op. Flash (Main area + Work area)	64K +32K	16K	-	8	8	-	51		12(2)														On-chip Debug (SWJ-DP)							
	MB9AF141MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF141NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF142LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF142MB		LQFP-80 BGA-96								11	✓	66		17(2)		-	-	-	-																
	MB9AF142NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					
	MB9AF144LB		LQFP-64 QFN-64								8	-	51		12(2)																					
	MB9AF144MB		LQFP-80 BGA-96								11	✓	66		17(2)																					
	MB9AF144NB		LQFP-100 QFP-100 BGA-112								16	✓	83		24(2)																					

## FMO + Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>C</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [chnut]	12bit AD Converter [chnut]	DA Convert [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWIC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	I <sup>2</sup> C [ch]	UART/SI [ch]	SIO [ch]	LINUART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
S6E1A	S6E1A11B0A	40	LQFP-32 QFN-32	2.7 to 5.5	✓	Flash	56K	6K	-	2	8	-	23	37	-	5(1)	-	Multi-Function Timer x 1unit (Free-Run 3ch/ Output Compare 6ch/ Input Capture 4ch/ PPG 3ch/ Waveform Generator 3ch/ AD Activation Compare 3ch Selectable)	Base Timer x 4ch (Reload/PPG/PWM/ PWC Selectable)	QPRC x 1	1	Multi Function Serial x 3ch (UART/CSIO/I <sup>2</sup> C/LIN Selectable)	-	-	-	-	-	Dual Timer, Real Time Clock, Unique ID, Fast I/O	On-chip Debug (SW-DP/MTB)							
	S6E1A11C0A		LOFP-48 LOFP-52 QFN-48																																	
	S6E1A12B0A		LOFP-32 QFN-32																																	
	S6E1A12C0A		LOFP-48 LOFP-52 QFN-48																																	
S6E1B8 w/security	S6E1B86GHA	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM / 32 SEG x 8 COM / 36 SEG x 4 COM / 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F, Cipher	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84GHA																																			
	S6E1B86FHA																																			
	S6E1B84FHA																																			
	S6E1B86EHA																																			
	S6E1B84EHA																																			
S6E1B8	S6E1B86G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM / 32 SEG x 8 COM / 36 SEG x 4 COM / 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B84G0A																																			
	S6E1B86F0A																																			
	S6E1B84F0A																																			
	S6E1B86E0A																																			
	S6E1B84E0A																																			
S6E1B3	S6E1B36G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM / 32 SEG x 8 COM / 36 SEG x 4 COM / 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B34G0A																																			
	S6E1B36F0A																																			
	S6E1B34F0A																																			
	S6E1B36E0A																																			
	S6E1B34E0A																																			
S6E1B1	S6E1B16G0A	40	LQFP-120	1.65 to 3.6	✓	Main Flash +Work Flash	512K + 40K	64K	-	-	24	-	102	-	24	-	Multi-Function Timer x 1units (Free-Run 3ch/Output Compare 6ch/Input Capture 4ch /PPG3ch/Waveform Generator 3ch /AD Activation Compare 6ch Selectable), PPG3ch	Base Timer x 8ch (Reload/PPG/PWM/PWC Selectable)	-	1	Multi Function Serial x 8ch (UART/CSIO/I <sup>2</sup> C Selectable)	-	1ch (USB-Host/ USB-Function Selectable)	40 SEG x 8 COM / 44 SEG x 4 COM / 32 SEG x 8 COM / 36 SEG x 4 COM / 20 SEG x 8 COM / 24 SEG x 4 COM	Dual Timer, Real Timer Clock, Unique ID, DSTC x 64ch, Tiny I2S, HDMI-CEC 2ch, CRC, Smart Card I/F	On-Chip Debug (SW-JDP/ Boundary SCAN)										
	S6E1B14G0A																																			
	S6E1B16F0A																																			
	S6E1B14F0A																																			
	S6E1B16E0A																																			
	S6E1B14E0A																																			

## 8FX – 8bit Microcontrollers

# Traveo Family – 32bit Microcontrollers

# Traveo Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA[C][ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	12bit AD Converter [ch/unit]	12bit AD Converter with 4ch sample & hold	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PWC Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other Timers [ch]	I2C [ch]	UART/SI [ch]	SIO [ch]	LIN/UART/SIO [ch]	CAN [ch]	USB-Host [ch]	USB-Function [ch]	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																				
S6J3120	S6J3128HA	112	TEQFP -144	4.5 to 5.25	-	Main Flash +Work Flash	576K + 48K	TC-RAM: 32KB System - RAM: 16KB Backup - RAM: 8KB	Instruction: 16 Data: 16	16	16	A24/D16	112	50(2)	-	-	12	6	12	16bit Base Timer x 30ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 10ch (LIN/UART/SIO Selectable)	CAN - FD x 3ch	-	-	32 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 4ch, Sound generator x 3ch	On-Chip Debug						
	S6J3129HA																																			
	S6J312AHA																																			
S6J3200	S6J32AAKS	160	TEQFP -208	1.1 to 1.3 3.0 to 3.6 4.5 to 5.5	✓	Main Flash +Work Flash	1088K + 112K	TC-RAM: 64KB System - RAM: 128KB Backup - RAM: 16KB VRAM: 1024KB	Instruction: 16 Data: 16	16	16	-	120	46(1)	-	24	12	24	Reload Timer x 14ch 16bit Base Timer x 24ch (PWM/PPG/Reload/PWC Selectable)	2	RTC x 1ch	Multi Function Serial x 12ch (LIN/UART/SIO/I2C Selectable)	CAN-FD x 4ch	-	-	30 x 4	-	ARM Cortex-R5, SHE(Secure Hardware Extension), SMC x 6ch, Sound generator x 4ch, 2D Graphic Engine, Display Output x 1ch	On-Chip Debug							
	S6J32AAKU																																			
	S6J32AALS																																			
	S6J32AALU																																			
	S6J32BAKS																																			
	S6J32BAKU																																			
	S6J32BALS																																			
	S6J32BALU																																			
	S6J32CAKS																																			
	S6J32CAKU																																			
	S6J32CALS																																			
	S6J32CALU																																			
	S6J32DAKS																																			
	S6J32DAKU																																			
	S6J32DALS																																			
	S6J32DALU																																			

# Traveo Family – 32bit Microcontrollers

## FR Family – 32bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: VCC [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMA/C [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Output Compare [ch]	Free-Run Timer [ch]	Timer	Input Capture [ch]	Reload Timer [ch]	PWM Timer [ch]	PW/C Timer [ch]	PPG Timer [ch]	Up/Down Counter [ch]	Other timers [ch]	Serial	Communication	LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device
CAN/AUTOMOTIVE																																
MB91570	MB91F575B	80	LQFP-144	4.5 to 5.5	-	-	-	-	576K +64K	48K	-	-	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	CAN: 32/64Msg-buffer, LIN, Sound generator, SMC x 6ch From among the RAM, 8KB can be used as BackUpRAM  For models with H suffix on part number, the initial status of the clock supervisor is off	On-chip Debug		
	MB91F575BS				-	-	-	-	1088K +64K	72K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F575BH				-	-	-	-	1600K +64K	112K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F577B				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F577BS				-	-	-	-	1600K +64K	112K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F577BH				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F577BHS		LQFP-208		-	-	-	-	1600K +64K	112K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F578C				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F578CH				-	-	-	-	1600K +64K	112K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F579C				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F579CHS				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F578CM				-	-	-	-	1600K +64K	112K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F578CSM				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F578CHSM				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F579CM				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F579CSM				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
	MB91F579CHSM				-	-	-	-	2112K +64K	144K	-	-	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111	109	111			
MB91580	MB91F583ASG	128	LQFP-64	4.5 to 5.5	-	Main Flash +Work Flash	-	-	320K +64K	32K	-	-	8(1) 7(1) 2(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 4ch (LIN/UART/SIO/I2C Selectable. However, I2C only supports ch0/ch1)	6	3	-	-	-	32 x 4	-	On-chip Debug			
	MB91F583ASH				-		-	-	448K +64K	48K	-	-	8(1) 7(1) 2(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F583ASJ				-		-	-	576K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F583ASK				-		-	-	320K +64K	32K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F584ASG				-		-	-	448K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F584ASH				-		-	-	576K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F584ASJ				-		-	-	320K +64K	32K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F584ASK				-		-	-	448K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F585ASG				-		-	-	576K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
	MB91F585ASH				-		-	-	832K +64K	64K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		1	-	-	-	-	-	✓				
MB91580	MB91F585AMK	128	LQFP-100	4.5 to 5.5	-	Main Flash +Work Flash	-	-	320K +64K	32K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-	Multi Function Serial x 2ch (LIN/UART/SIO/I2C Selectable)	2	-	-	-	-	-	✓	On-chip Debug			
	MB91F585AMH				-		-	-	448K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F585AMJ				-		-	-	576K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F584AMH				-		-	-	320K +64K	32K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F584AMK				-		-	-	448K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F585AMJ				-		-	-	576K +64K	48K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F585AMJ				-		-	-	832K +64K	64K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F586LA				-		-	-	1088K +64K	128K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F586LB				-		-	-	1088K +64K	128K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F586LC				-		-	-	1088K +64K	128K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F586LD				-		-	-	1088K +64K	128K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6	4	Reload Timer x 4ch + PPG Timer x 6ch + Base Timer x 2ch (Reload/PPG/PWM/PWC Selectable)	2	-		2	-	-	-	-	-	✓				
	MB91F587LA				-		-	-	1088K +64K	128K	-	-	8(1) 7(1) 8(1)	10bit x 1	7	6																

# F<sup>2</sup>MC-16FX – 16bit Microcontrollers

## F<sup>2</sup>MC-16FX – 16bit Microcontrollers

Series Name	Product Name	Maximum Internal Clock Frequency [MHz]	Package [pin]	Operating Voltage: V <sub>CC</sub> [V]	Sub Clock	Memory Type	ROM [byte]	RAM [byte]	Cache [Kbyte]	DMAC [ch]	Ext. Interrupt [ch]	External Bus	Maximum I/O port [ch]	10bit AD Converter [ch/unit]	12bit AD Converter [ch/unit]	DA Converter [bit x ch]	Timer				Serial			Communication			LCD Controller [seg x com]	Three-phase Inverter	Note	Evaluation Device			
AUTOMOTIVE																																	
MB96610	MB96F612A	32	LQFP-48	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F612R						64.5K +32K	10K		-	2	11	-	(Single clock) 35 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F613R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615A						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
	MB96F615R						128.5K +32K	10K		-	2	11	-	(Single clock) 37 35 (Dual clock)	16(1)	-	-	5	4	4 +3 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	-	-	-	-	-	1 1 1 1 1 1		
MB96620	MB96F622A	32	LQFP-64	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	32.5K +32K	4K	10K	-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F622R						64.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F623R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625A						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
	MB96F625R						128.5K +32K	10K		-	2	13	-	(Single clock) 52 50 (Dual clock)	21(1)	-	-	6	4	6 +2 (Dedicated for LIN)	2 +1 (Dedicated for PPG)	-	-	8bit x 16 16bit x 8	QPRC x 2	1	-	-	-	-	1 1 1 1 1 1		
MB96630	MB96F633A	32	LQFP-80	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F633R						128.5K +32K	10K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635A						256.5K +32K	24K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F635R						384.5K +32K	28K		-	4	15	-	(Single clock) 64 66 (Dual clock)	21(1)	-	-	7	3	6 +1 (Dedicated for LIN)	3	-	-	8bit x 20 16bit x 15	QPRC x 2	2	-	-	-	5	1 1 1 1 1 1		
	MB96F636R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F643A						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96640	MB96F643R	32	LQFP-100	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F645A						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F645R						256.5K +32K	24K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F646R						384.5K +32K	28K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F647R						128.5K +32K	10K		-	4	16	-	(Single clock) 81 79 (Dual clock)	24(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 24 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F653A						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
MB96650	MB96F653R	32	LQFP-120	2.7 to 5.5	<input checked="" type="checkbox"/>	Dual Op. Flash	64.5K +32K	10K	16K	-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1	Option without CAN	On-chip Debug
	MB96F655A						128.5K +32K	10K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F655R						256.5K +32K	24K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F656R						384.5K +32K	28K		-	4	16	-	(Single clock) 101 99 (Dual clock)	29(1)	-	-	7	3	6 +1 (Dedicated for LIN)	5	-	-	8bit x 32 16bit x 16	QPRC x 2	2	-	-	-	6	1 1 1 1 1 1		
	MB96F673A						64.5K +32K	4K	16K	-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1	SMC x 2ch Sound generator Option without CAN	On-chip Debug		
	MB96F673R						128.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F675A						256.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F675R						384.5K +32K	4K		-	2	7	-	(Single clock) 50 48 (Dual clock)	12(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F683A						64.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4	3	-	-	8bit x 8 16bit x 4	-	1	-	-	2	1 1 1 1 1 1				
	MB96F683R						128.5K +32K	4K		-	2	7	-	(Single clock) 65 63 (Dual clock)	14(1)	-	-	2	4														





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