Welcome to E-XFL.COM

Understanding Embedded - Microcontroller, Microprocessor, FPGA Modules

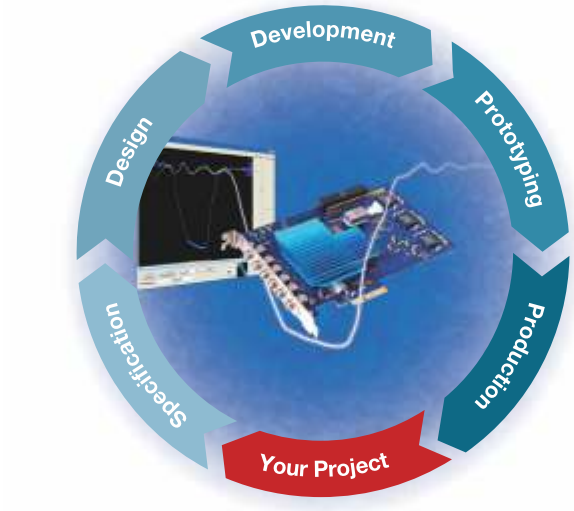
Embedded - Microcontroller, Microprocessor, and FPGA Modules are fundamental components in modern electronic systems, offering a wide range of functionalities and capabilities. Microcontrollers are compact integrated circuits designed to execute specific control tasks within an embedded system. They typically include a processor, memory, and input/output peripherals on a single chip. Microprocessors, on the other hand, are more powerful processing units used in complex computing tasks, often requiring external memory and peripherals. FPGAs (Field Programmable Gate Arrays) are highly flexible devices that can be configured by the user to perform specific logic functions, making them invaluable in applications requiring customization and adaptability.

Applications of Embedded - Microcontroller,

Details	
Product Status	Obsolete
Module/Board Type	MCU, FPGA
Core Processor	ARM Cortex-A9
Co-Processor	Zynq-7000 (Z-7030)
Speed	125MHz
Flash Size	32MB
RAM Size	1GB
Connector Type	Samtec LSHM
Size / Dimension	1.97" x 1.57" (50mm x 40mm)
Operating Temperature	-40°C ~ 85°C
Purchase URL	https://www.e-xfl.com/product-detail/trenz-electronic/te0715-04-30-1i

Company Profile

Since 1992, Trenz Electronic GmbH successfully operates as a development service enterprise in the electronics branch. Our services include design-in support as well as turnkey design which typically cover all steps from product specification, hardware and software design up to prototyping and production.



We are particularly specialized in the design of high-speed data acquisition, high-accuracy measurement and embedded digital signal processing systems based on FPGA and ARM architectures.

We maintain long-term customer relationships, characterized by flexibility and technical competence.

Hardware Design

- System Architecture and Design
- Hardware Integration (Design-In)
- Ultrafast Digital Logic
- Analog and Mixed Signal
- Digital Signal Processing
- Schematic Capture and PCB Layout

HDL Design

- FPGA and System-On-Chip Design
- System Design and Synthesis
- HDL Design (VHDL,Verilog)
- Integration of Soft-Cores (Xilinx MicroBlaze, ARM Cortex …)
- USB, PCI-Express, Gigabit Ethernet
- Ultrafast ADC/DAC Interfaces

Software Development

- Device Driver and Application Software development
- Software and Firmware development

ISO 9001 REGISTERED FIRM

ISO 9001:2008 (quality management) certified

ISO 14001 REGISTERED FIRM

ISO 14001:2004 (environmental management) certified

Trenz Electronic GmbH

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TE0820 Series
Xilinx Zynq UltraScale+, DDR4, SPI Boot Flash, eMMC

Overview

The Trenz Electronic TE0820 are 4 x 5 standard footprint MPSoC modules integrating a Xilinx Zynq UltraScale+ with up to 4 GByte 32-Bit DDR4 SDRAM, and max. 512 MByte SPI Boot Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All modules in 4 x 5 cm form factor are fully mechanically and largely electrically compatible among them. All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

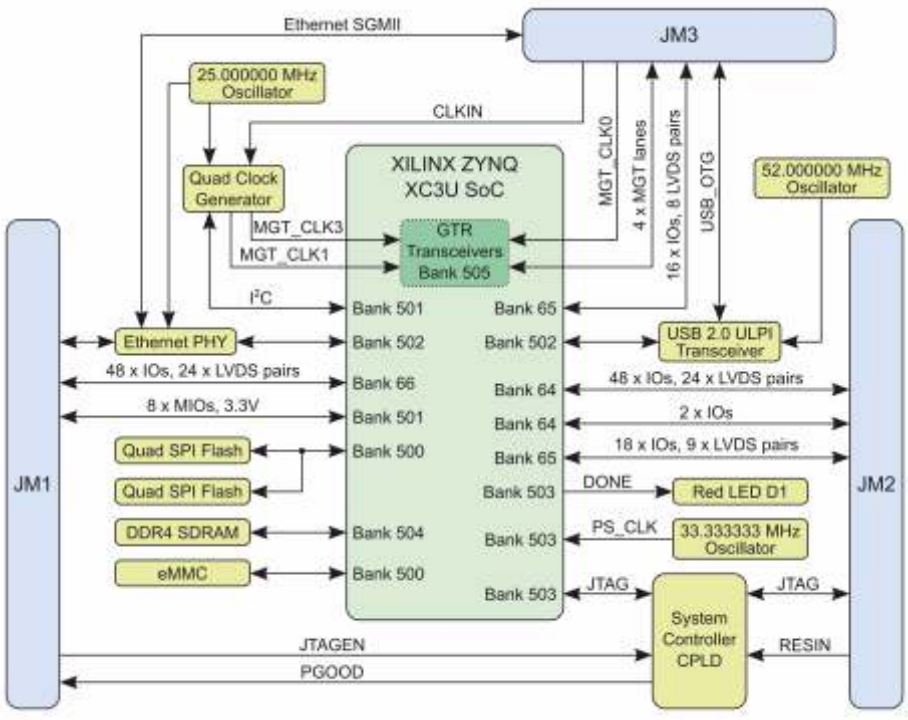
Extended device life cycle

Rugged for industrial applications

Key Features (preliminary)

- Xilinx Zynq UltraScale+ MPSoC 784 pin package (ZU3EG, option ZU5EV)
- Memory:
  - 32-Bit DDR4 - 4 GByte max
  - SPI Boot Flash dual parallel - 512 MByte max
  - 4 GByte eMMC (up to 64 GByte)
- B2B connectors:
  Plug-on module with 2 x 100-pin and 1 x 60-pin high-speed hermaphroditic strips
  - 14 x MIO, 132 I/O's x HP (3 banks)
  - Serial transceiver: PS GTR 4
  - GT Reference clock input
  - PLL for GT Clocks (optional external reference)
  - 1 GBit Ethernet PHY
  - USB 2.0 OTG PHY
  - Real Time Clock
- Size: 40 x 50 mm
- All power supplies on board.

Other assembly options for cost or performance optimization plus high volume prices available on request.



TEB0911 Series
Xilinx Zynq UltraScale+, 6 FMC Solts, Gigabit Ethernet

Key Features

- Zynq UltraScale+ MPSoC - 1156 Package ZU9 (ZU6, ZU9, ZU15 Possible as assembly option)
- 64-Bit DDR4 SODIMM (PS connected)
- PS-GTR
  - M2 PCIe SSD (internal, 1-Lane)
  - 2 x USB3 Host (from 4 port internal HUB)
  - 2 Lane DisplayPort output - Monitor
- RJ45 GbE Ethernet PS connected, 88E1512 PHY
- 4 x FMC-HPC connector front
  - 4 GTH
  - 1 GT Clock
  - 68+4 HP or HD I/O
- FMC-HPC connector Back
  - 4 GTH
  - 1 GT Clock
  - 12 I/O
- FMC-HPC connector Back
  - 1 GTH
  - 1 GT Clock
  - 12 I/O
- 2 x SFP+ connected to 2 PL GTH,
- 1 x SFP+ connected to PL GTH
- Power: 24V

Overview

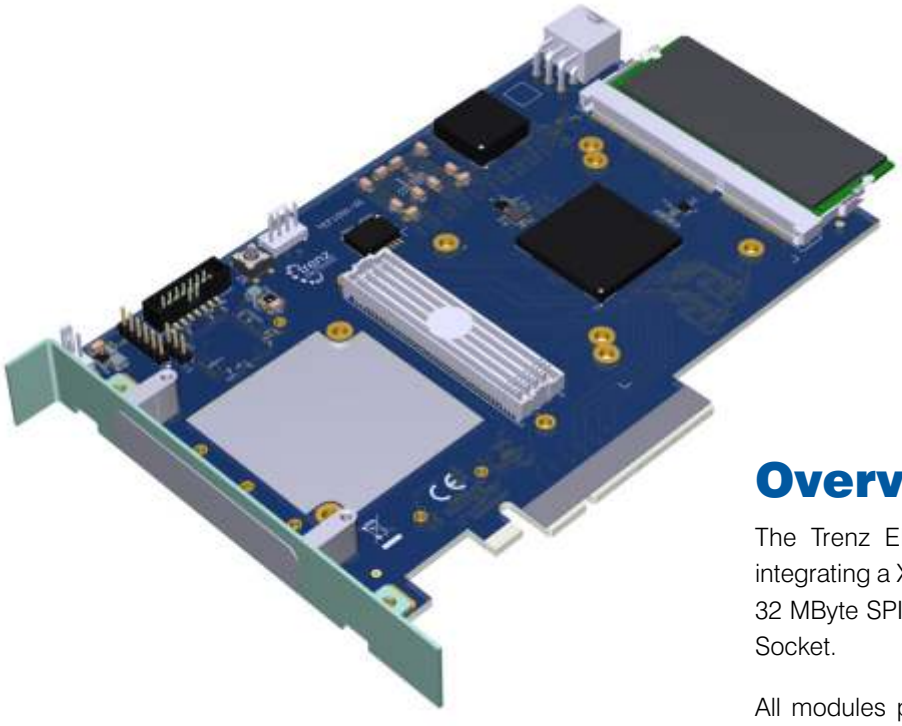
The Trenz Electronic TEB0911 "UltraRack+" is a high performance Zynq UltraScale+ MPSoC board with 6 FMC slots and Gigabit Ethernet.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

XILINX ALL PROGRAMMABLE™

ZYNQ® UltraSCALE+

TEF1001 Series
PCIe FMC Carrier, Xilinx Kintex-7, SPI Flash, 4 lane PCIe



Overview

The Trenz Electronic TEF1001 is a PCIe FMC Carrier integrating a Xilinx Kintex-7 FPGA (K160T, K325T or K410T), 32 MByte SPI Flash, an 4 lane PCIe and a DDR3 SODIMM Socket.

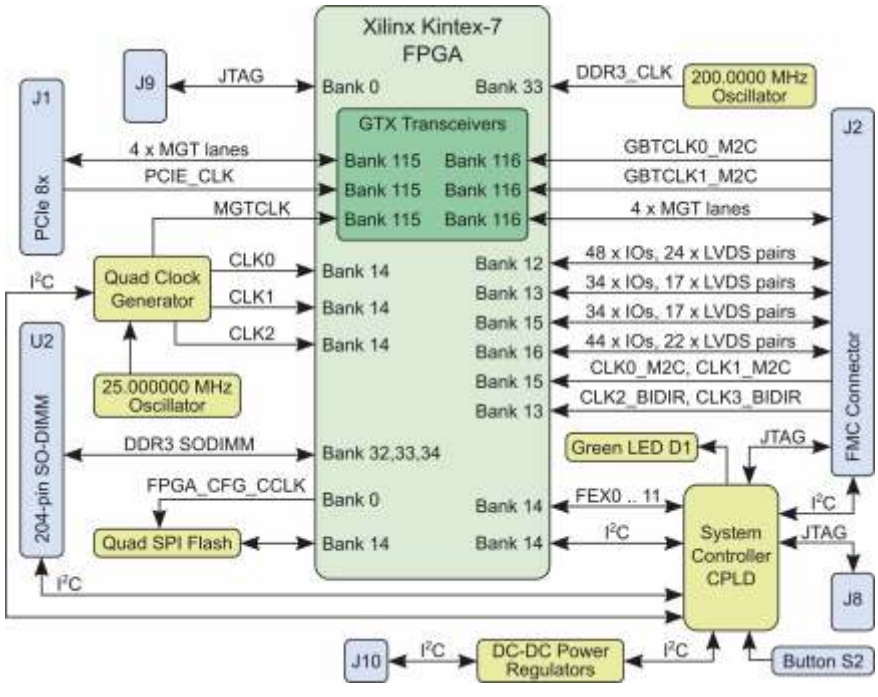
All modules produced by Trenz Electronic are developed and manufactured in Germany.

XILINX ALL PROGRAMMABLE™ KINTEX 7™

Key Features

- One Vita 57.1 FMC HPC Slot
- 4 lane PCIe Gen 2
- Xilinx Kintex-7 XC7K160T-2FBG676I
- DDR3 SODIMM Socket
- 32 MByte SPI Flash
- Programmable clock generator Si5338
- 200 MHz Low-Jitter LVDS oscillator
- High performance DC-DC converters

Other assembly options for cost or performance optimization plus high volume prices available on request.



TE0745 Series
Xilinx Zynq, 1 GByte DDR3, 32 MByte Flash, 1 GBit Ethernet

5.2 x 7.6 form factor

Overview

The Trenz Electronic TE0745 is an industrial-grade SoC module integrating a Xilinx Zynq-7 (Z-7030, Z-7035, Z-7045), 1 GByte 32-Bit wide DDR3/L, 32 MByte SPI Flash memory for configuration and operation and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle

Rugged for industrial applications

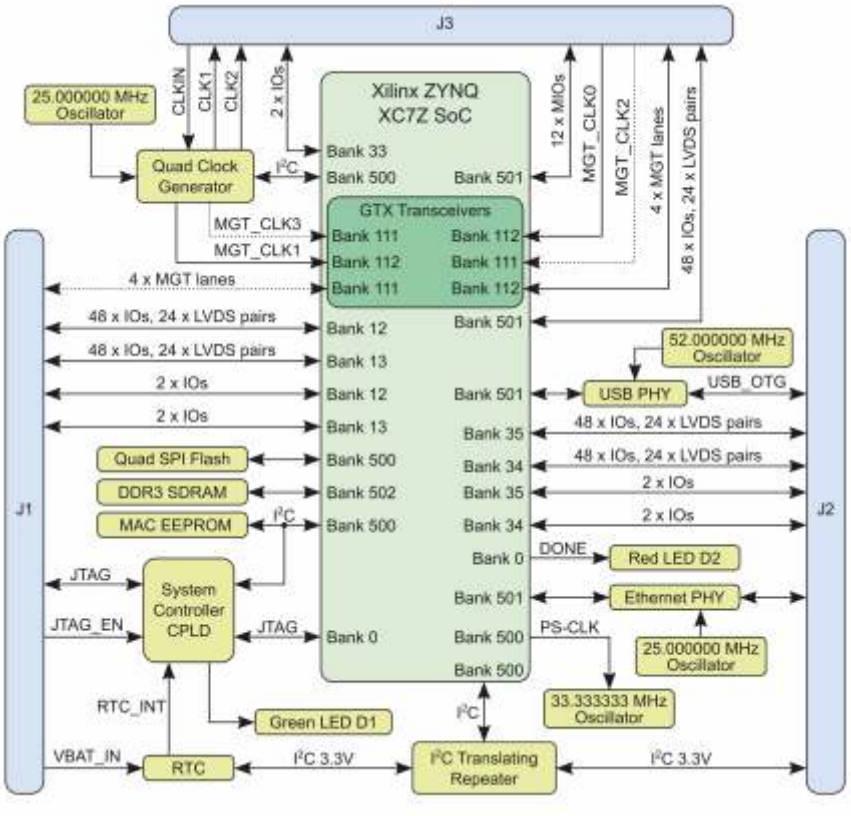
XILINX ALL PROGRAMMABLE™

ZYNQ™

Key Features

- Xilinx Zynq 7030/7035/7045
- Rugged for shock and high vibration
- 1 GByte 32-Bit wide DDR3/L
- 32 MByte SPI Flash
- Dimensions: 5.2 x 7.6 cm
- B2B Connectors with 3 x 160 pin
  - 250 I/O's, all HR and HP I/O
  - 1 GBit Ethernet PHY,
  - USB 2.0 OTG PHY
  - 8 x GTX (7030: 4 GT)
  - 2 GT Reference Clock inputs (7030: 1 REFC
  - Reference clock input for PLL (optional)
  - 2 x PLL outputs
  - I2C
  - 6 MIO
- Real Time Clock
- MAC Address EEPROM
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.



TE0722 Series

Xilinx Zynq, Dual Core ARM A9+, 16 MByte SPI Flash, DIP40 Form Factor

1.8 form X factor 5.1

Overview

The DIPFORTy1 "Soft Propeller" is based on the Xilinx Zynq-7000, a System on Chip which contains a FPGA and a Dual Core ARM A9+ processor with enough logic gates to become a Propeller. The board also has 16 MByte of Flash used for configuration and everything fits on a Propeller-compatible DIP 40 pinout.
DIPFORTy1 "Soft-Propeller" is the lowest cost Zynq based module ever made and the first Zynq module that can use existing bases and project boards (Parallax Propeller chip compatibility). All this in a compact 1.8 x 5.1 cm form factor, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle

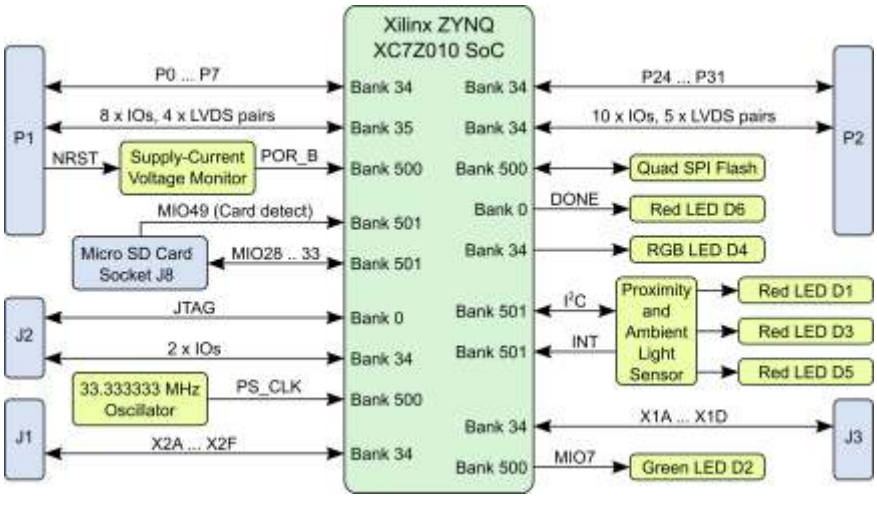
"Ideal for Maker" Make:

XILINX ALL PROGRAMMABLE™ ZYNQ™

Key Features

- Xilinx Zynq-7: XC7Z010-CLG225
  - 16 MByte SPI Flash (primary boot)
  - 33.333 MHz Clock (MEMS Oscillator)
- Dual Core ARM A9+
- DIP40 form factor
  - 2 x 20 holes for socket pins or pin-header
  - Size: 18 x 51 mm
- Total user accessible PL I/O: 46 (+3 Input only)
  - DIP40 header pins: 34 I/O
  - XMOD J1: 6 I/O
  - XMOD J2: JTAG + 2 I/O (or 3 input + 2 I/O)
  - XMOD J3: 4 I/O
- 3.3V single supply
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- User LED (ARM CPU MIO GPIO)
- MicroSD Card socket (MIO, ZYNQ secondary boot media)
- Sil1143 Proximity and ambient light sensor

Other assembly options for cost or performance optimization available or high volume prices on request.



TE0841 Series
Xilinx Kintex UltraScale, DDR4, Flash, 8 x Transceiver

Overview

The Trenz Electronic TE0841 is an industrial-grade FPGA module integrating a Xilinx Kintex UltraScale KU35 or KU40, max. 4 GByte 16-Bit width DDR4, max. 256 MByte QSPI Flash for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.
All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle

Rugged for industrial applications

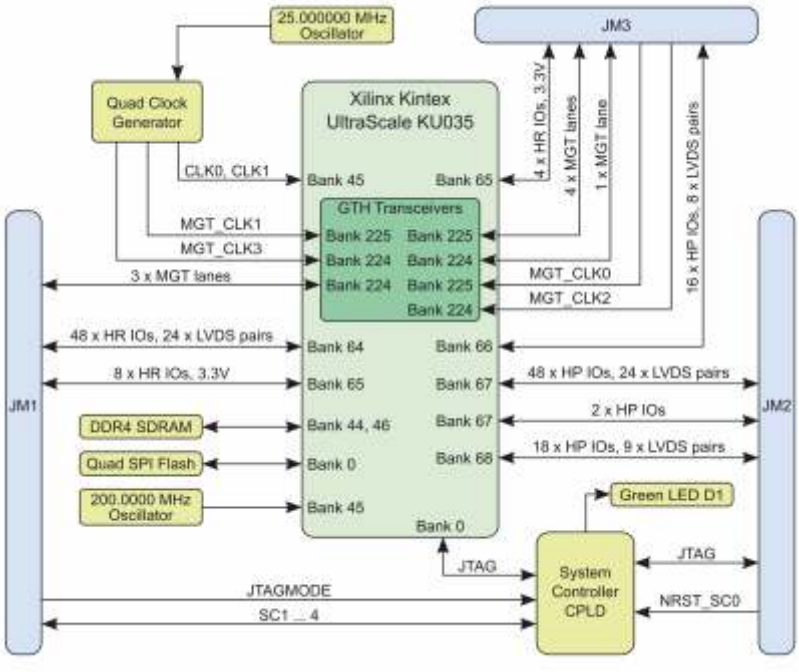
XILINX ALL PROGRAMMABLE™

KINTEX UltraSCALE

Key Features

- Xilinx Kintex UltraScale KU35 or KU40 FPGA
- Rugged for industrial application
- max. 4 GByte 16-bit wide DDR4
- max. 256 MBit (32 MByte) SPI Boot Flash
- Size: 40 x 50 mm
- 3 mm mounting holes for skyline heat spreader
- B2B Connectors: 3 x Razor Beam, total 260 terminals
  - User I/O: HR 12, HP 132
  - Serial transceiver: GTH 8 lanes (all)
  - GT clock inputs: 2
- Clocking
  - Si5338 - 4 output PLL, GT and PL clocks
  - 200 MHz LVDS oscillator
- All power supplies on board, single supply operation supported.
- Evenly spread supply pins for good signal integ

Other assembly options for cost or performance optimization available plus high volume prices on request.



Overview

Trenz Electronic TE0741 are industrial-grade FPGA modules integrating a Xilinx Kintex-7 T FPGA, 32 MByte Flash memory for configuration and operation, 8 transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.
All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle

Rugged for industrial applications

XILINX ALL PROGRAMMABLE™ KINTEX 7™

Key Features

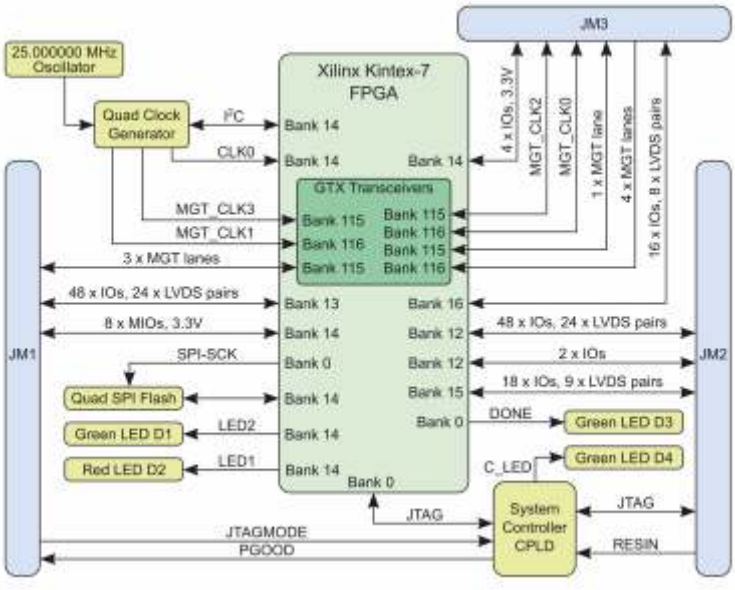
- Industrial-grade Xilinx Kintex-7 (70T, 160T, 325T and 410T) SoM
- Rugged for shock and high vibration
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
  - 2 x Transceiver clock (default 125 MHz)
  - Fabric clock (default 200 MHz)
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 144 FPGA I/O's (65 LVDS pairs possible) available on board-to-board connectors
- 8 GTX (high-performance transceiver) lanes
  - GTX (high-performance transceiver) clock input
- On-board high-efficiency DC-DC converters
  - 20.0 A x 1.0 V power rail
  - 1.5 A x 1.8 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User LEDs
- Evenly spread supply pins for good signal integrity

Recommended Software:

- Kintex-7 XC7K**70T**-2CF, Kintex-7 XC7K**160T**-2CF: Xilinx Vivado Webpack (free license)

- Kintex-7 XC7K**325T**-2CF, Kintex-7 XC7K**410T**-2CF: Xilinx Vivado Design Suite

Other assembly options for cost or performance optimization plus high volume prices available on request.



TE0710 Series

Xilinx Artix-7 T, DDR3, Flash, 2 × 100 MBit Ethernet

Overview

Trenz Electronic TE0710 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, two MBit Ethernet transceivers (physical layer), 512 Mbyte DDR3 SDRAM with 8-Bit width, 32 MByte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stackingstrips.

All modules in 4 x 5 cm form factor are mechanically compatible.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

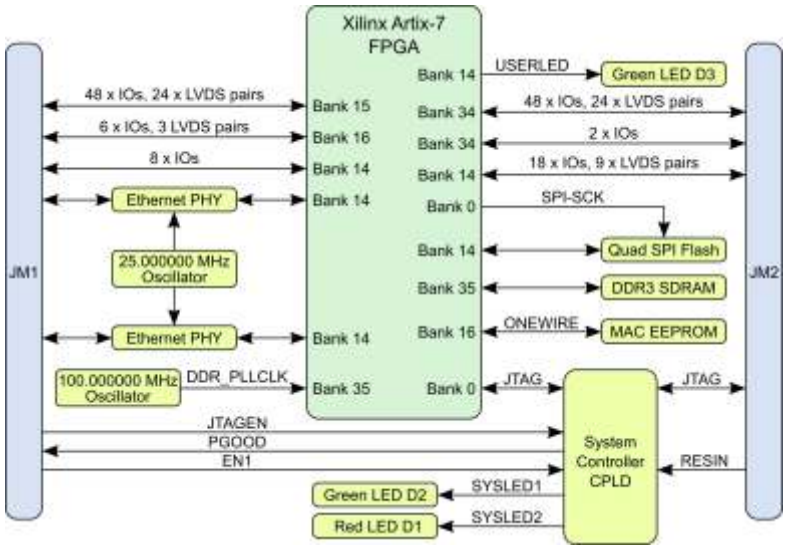
Key Features

- Industrial-grade Xilinx Artix-7 (15T to 100T) SoM, supported by the free Xilinx Vivado WebPACK tool
- Rugged for shock and high vibration
- 512 MByte DDR3 SDRAM
- Dual 100 MBit Ethernet PHY
- MAC Address EEPROM
- 32 MByte QSPI Flash memory (with XiP support)
- 100 MHz programmable MEMS oscillator
- Plug-on module with 2 × 100-pin high-speed hermaphroditic strips
- 112 FPGA I/O's (51 differential pairs) available on board-to-board connectors
- On-board high-efficiency DC-DC converters
  - 4.0 A x 1.0 V power rail
  - 1.0 A x 1.8 V power rail
  - 1.0 A x 1.5 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User LED
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle

Rugged for industrial applications



TE0713 Series

Xilinx Artix-7 T, DDR3L, USB 3.0, 4 GTP lanes

Overview

The Trenz Electronic TE0713 is an industrial-grade FPGA module integrating a Xilinx Artix-7 FPGA, USB 3.0 to FIFO bridge, 1 GByte of DDR3L SDRAM, 32 MByte Flash memory for configuration and operation, and powerful switching-mode power supplies for all on-board voltages. Numerous configurable I/O's are provided via rugged high-speed strips. Modules in 4 x 5 cm form factor are fully mechanically and largely electrically compatible among them. All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

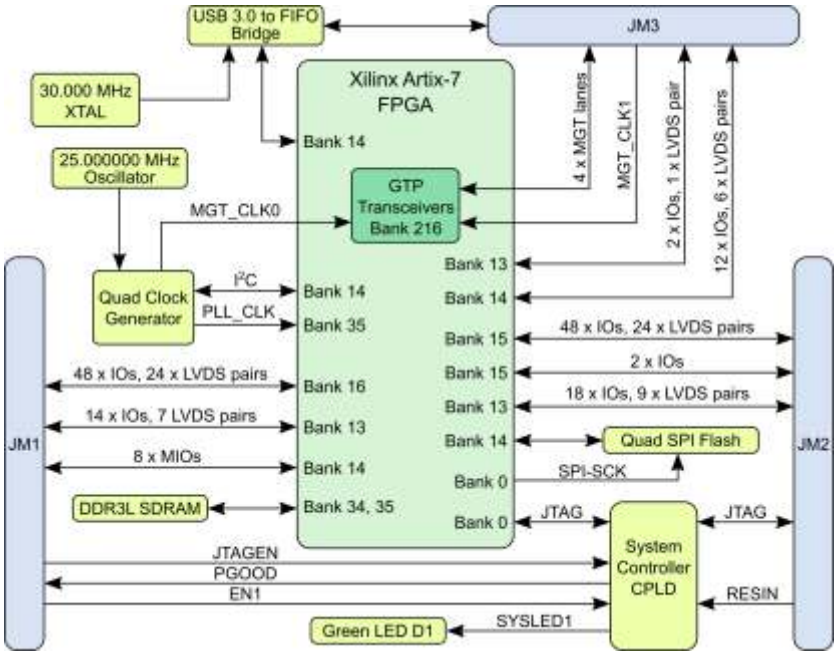
Extended device life cycle

Rugged for industrial applications

Key Features

- Xilinx Artix-7 (15T to 200T) SoM
- Both industrial and commercial temperature ranges available
- Rugged for high shock and high vibration resistance
- 1 GByte DDR3L 32-bit SDRAM
- 32 MByte QSPI Flash memory (with XiP support)
- USB 3.0 to FIFO interface bridge
- Programmable clock quad generator
  - GTP transceiver clock (default 125 MHz)
  - Fabric clock (default 200 MHz)
- Plug-on module with 2 x 100-pin and 1 x 60-pin high-speed hermaphroditic strips
- 152 FPGA I/O's (75 differential pairs) available via B2B connectors
- 4 GTP (multi Gigabit transceiver) lanes
- External clock input for GTP transceivers via B2B connector
- On-board high-efficiency DC-DC converters
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User configurable LED
- Evenly spread supply pins for good signal integrity.

Other assembly options for cost or performance optimization plus high volume prices available on request.



www.trenz-electronic.de/te0713

Overview

The Trenz Electronic TE0714 is an industrial-grade FPGA module integrating a Xilinx Artix-7 (A15T, A35T, A50T), 16 MByte Flash memory for configuration and operation and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle

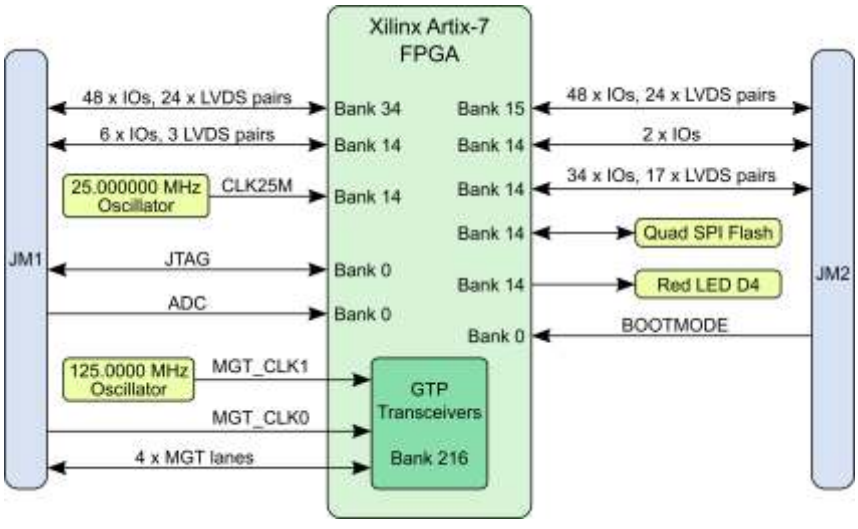
Rugged for industrial applications

XILINX ARTIX 7

Key Features

- Xilinx Artix-7 (A15T, A35T, A50T)
- Rugged for shock and high vibration
- 16 MByte QSPI Flash memory
- Dimensions: 4 x 3 cm
- Differential MEMS Oscillator for GT Clocking
- MEMS Oscillator for PL Clocks (option)
- Plug-on module with 2 × 100-pin high-speed hermaphroditic strips
  - 144 FPGA I/O's (max 68 differential)
  - XADC Analog Input
  - 4 GTP (high-performance transceiver) lanes
  - GT Reference Clock input
  - Optimized I/O and power pins for good signal integrity
- On-board high-efficiency DC-DC converters
- eFUSE bit-stream encryption (AES)
- One user LED

Other assembly options for cost or performance optimization plus high volume prices available on request.



Overview

Xmod-USB-X is a universal USB adapter with 2 channels based on FTDI FT2232H USB2 HS Interface chip.

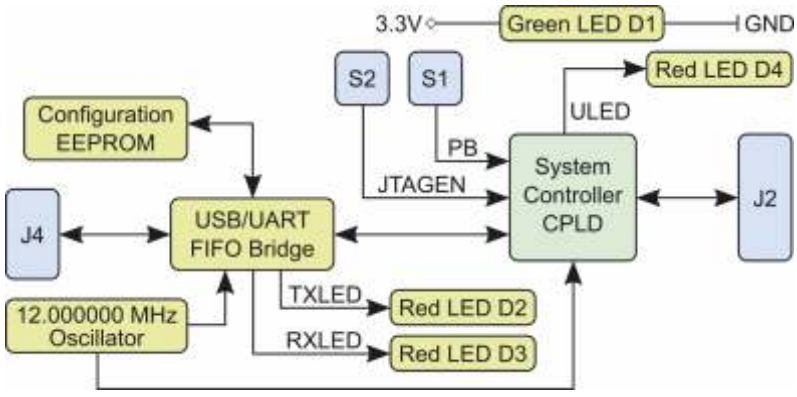
In the consigned default configu-ration Port A is JTAG and Port B is a serial interface. FT2232H port A and B are connected to small on-board programmable CPLD to allow flexible application specific remappings of FT2232H functions into 8 user I/O pins of single Xmod 12 x 8 Module.

Minimum PCB area on base board to support JTAG function 5 x 10 mm (does not include mounting hole space).
TE0790 is compatible with Xilinx Tools in contrast to the TE0790-01L, that can be used flexibly.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

- Xmod form-factor
  - Supported base slots: 6 x 2, 8 x 4, 10 x 6, 12 x 8, 5 x 2, 5 x 3
  - Size: 20 x 25 mm
  - M3 mounting hole
- FT2232H
  - Channel B RX/TX LED's (on top, not visible from front)
  - Mini-USB connector (more rugged then micro-USB)
  - 93C56 EEPROM
- Lattice XO2-256 CPLD
  - On board programmable using Lattice tools
  - 8 universal I/O pins
  - VCCIO either 3.3 V or user supplied (1.8 to 3.3V)
  - Red user LED (front visible)
  - 12 MHz clock from on-board Oscillator
- LDO for optional USB power
- Green Power-on LED (front visible)
- User button (front accesible)
- 4 position DIP switch
  - Choose CPLD program mode
  - FTDI EEPROM disable (not implemented in PCB REV 1)
  - Use VIO same as VCC
  - Use VCC from USB



TE Carrier Boards
built for Trenz Electronic micromodules with 4 x 5 cm form factor

The Trenz Electronic Carrier Boards are base-boards for 4 x 5 SoMs, which exposes the modules B2B-connector-pins to accessible connectors and provides a whole range of on-board components to test and evaluate Trenz Electronic 4 x 5 SoMs.

TE0701

- Overvoltage-, undervoltage- and reversed- supply-voltage-protection
- Barrel jack for 12 V power supply
- Carrier Board System-Controller CPLD
- Mini CameraLink connector
- RJ45 Gigabit Ethernet MagJack
- FPGA Mezzanine Card (FMC) connector
- USB JTAG- and UART interface with Mini-USB connector
- HDMI transmitter with HDMI connector
- 8 x user LED's, 2 x user push buttons, 2 x DIP switch
- PMOD connectors, Micro SD card socket and Micro-USB interface

TE0703

- 2 x VG96 backplane connectors (mounting holes and solder pads)
- SDIO port expander with voltage-level translation
- Micro SD card socket
- 4 x user LED's, 1 x user-push button, 2 x user configurable DIP switches
- Mini USB connector (USB JTAG and UART interface)
- RJ45 Gigabit Ethernet socket with 4 integrated LED's.
- USB host connector
- Barrel jack for 5 V power supply input
- DC-DC step-down converter for 3.3 V power supply
- USB JTAG and UART interface

TE0705

TE0705 is a "downgraded" version of TE0701. As little as possible has been changed in functionality except the functionality that was removed.

Changes from TE0701

- PMOD connectors changed to IDC headers
- HDMI removed
- CL connector removed
- USB connector position changed
- 5 pin header support added on both USB interfaces
- 12 V DC power input connector changed to different type
- FMC connector removed and replaced by two dual row 100 mil pin headers

TE0706

- VG96 backplane connector and 50-pin IDC male connector socket
- SDIO port expander with voltage-level translation
- Micro SD card socket and a USB type A connector
- 1 x user push button, user configurable DIP switch
- 1 x RJ45 Gigabit Ethernet MagJack
- 1 x Ethernet PHY
- Barrel jack for 5 V power supply input
- DC-DC step- down converter for 3.3 V power supply
- JTAG pins on 12-pin header
- 3 x VCCIO selection jumper

TEBA0841

Mainly for the use with TE0841 and TE0741 modules.

- XMOD (TE0790) pin header
- SFP connector
- Micro USB
- 1 x pin header 16 pol. (JTAG, MGT-CLK, boot mode, RST, IOs)
- 1 x pin header 10 pol. (SD IOs)
- 2 x pin headers 50 pol. (FPGA bank IOs and power)
- 1 x pin header for FPGA bank power VCCIOA and 1 x for VCCIOD
- LDO voltage regulator 3.3 V to 2.5 V
- 2 x user LED's (Red/Green)

www.trenz-electronic.de/te-baseboards

Module series comparison table

	TE0710	TE0711	TE0712	TE713	TE0714	TE0715	GigaZee TE0720
Device family	ARTIX-7	ARTIX-7	ARTIX-7	ARTIX-7	ARTIX-7	ZYNQ	ZYNQ
Device list	35T 50T 75T 100T	35T 50T 75T 100T	35T 50T 75T 100T 200T	15T - 200T	15T 35T 50T	Z-7015 Z-7030	Z-7020
Form factor/size	4 X 5	4 X 5	4 X 5	4 X 5	4 X 3	4 X 5	4 X 5
Connectors	2 × Samtec LSHM	3 × Samtec LSHM	3 × Samtec LSHM	3 x Samtec LSHM	2 x Samtec LSHM	3 × Samtec LSHM	3 x Samtec LSHM
Programmable logic family	Artix-7	Artix-7	Artix-7	Artix-7	Artix -7	Z-7015: Artix-7 Z-7030: Kintex-7	Artix-7
Processing system	MicroBlaze	MicroBlaze	MicroBlaze	MicroBlaze	MicroBlaze	2 x Cortex A9	2 x Cortex A9
SDRAM capacity [MByte] max	512 DDR3	-	1024 DDR3	1024 DDR3L	-	1024 DDR3	1024 DDR3
Flash [MByte]	32	32	32	32	16	32	32
EEPROM	-	FTDI User EEPROM	MAC	-	-	MAC	MAC
eMMC	-	-	-	-	-	-	4 - 64 GByte
Ethernet PHY	2 x 100 MBit	-	100 MBit	-	-	1 GBit	1 GBit
USB PHY	-	USB 2 UART/FIFO		USB 3.0	-	USB 2.0 OTG	USB 2.0 OTG
Total I/O	112	178	158	152	144	132 + 14 MIO	152 + 14 MIO
GBit transceivers	-	-	4 x GTP	4 x GTP	4 x GTP	Z-7015: 4 x GTP Z-7030: 4 x GTX	-
Other features	-	-	Programmable Clock Generator	Programmable Clock Generator	-	Programmable Clock Generator, RTC	RTC

oi710 - Quad 1.2GHz DAC

www.sundance.technology/oi710/

The oi710 a combination of a DAC module (SMT-FMC211) and the EMC²-DP. As such this system gives you four 1.25GHz DAC channels on a OneBank® PC/104 FPGA carrier card.

- Quad channel DAC (TI DAC3484)
- DAC control by Artix-7 FPGA (XC7A15T)
- 16-bit DDR3 local memory for DAC data
- I²C bus for control
- External clock and triggers

EMC²-DP stackable box

Coming soon!

Here's a preview of our stackable ruggedised case for the EMC²-DP. Currently in the R&D phase so please get in touch with any customisation requests!

oi816 - Octal 16BIT ADC

www.sundance.technology/oi816/

A combination of an eight channel ADC module (FMC168) and the EMC²-DP. This system gives you eight 16-bit ADC channels at 250MSPS on a OneBank® PC/104 FPGA carrier card.

- Eight-channel 16-bit 250MSPS A/D conversion
- Available as air cooled and conduction cooled
- VITA 57.1-2010 compliant
- Based on TI ADS42LB69
- Coaxial front panel inputs on SSMC connectors
- Single ended AC or DC coupled analogue input

- Flexible clock tree enables:
  - internal clock
  - internal clock locked to an external reference
  - external clock
  - external sync / 1PPS

Sundance Multiprocessor Technology Ltd.
Unit 20 Chiltern House, Waterside,
Chesham, HP5 1PS.
United Kingdom.

Phone: +44 (0) 1494 793 167

Email: enquiries@sundance.com

Xilinx development boards and kits provide an out-of-the box design solution to accelerate development time and time-to-market. Xilinx offers kits complete with evaluation boards, the Vivado Design Suite tools, IP cores, reference designs and FPGA Mezzanine Card (FMC) support – so application development begins immediately out of the box.

The Vivado Design Suite delivers a SoC-strength, IP-centric and system-centric, next generation development environment that has been built from the ground up to address the productivity bottlenecks in system-level integration and implementation.

Xilinx Zynq UltraScale+ MPSoC ZCU102 Evaluation Kit

The ZCU102 Evaluation Kit enables designers to jumpstart designs for Automotive, Industrial, Video and Communications applications. This kit features a Zynq UltraScale+™ MPSoC device with a quad-core ARM® Cortex-A53, dual-core Cortex-R5 real-time processors, and a Mali-400 MP2 graphics processing unit based on Xilinx's 16nm FinFET+ programmable logic fabric. The ZCU102 supports all major peripherals and interfaces enabling development for a wide range of applications.

Key Features & Benefits

- Optimized for quick application prototyping with Zynq Ultrascale+ MPSoC
- DDR4 SODIMM – 4GB 64-bit w/ ECC attached to Processor Subsystem (PS)
- DDR4 Component – 512MB 16-bit attached to Programmable Logic (PL)
- PCIe Root Port Gen2x4, USB3, Display Port & SATA
- 4x SFP+ cages for Ethernet
- 2x FPGA Mezzanine Card (FMC) interfaces for I/O expansion including 16 x 16.3 Gb/s GTH transceivers and 64 user defined differential I/O signals

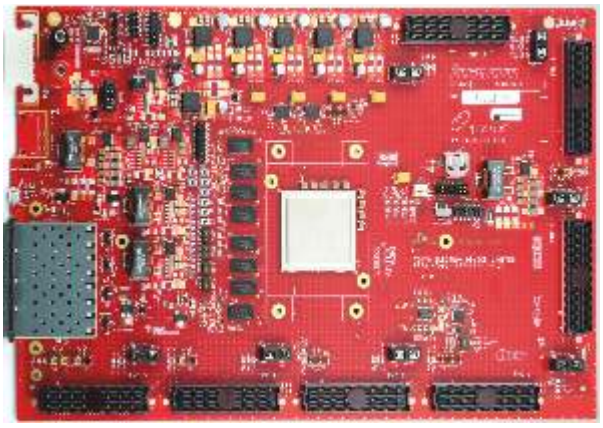
This is just one example of our wide variety of Boards and Kits from Xilinx. Please have a look in our online shop for a wider selection or ask for a quote at sales@trenz.biz.

Since 1965, Tokyo Electron Device Limited (TED) has been focused on the semiconductor distribution business. Leveraging 40 years of industry experience, in 2004, TED began offering FPGA solutions under the “inrevium” brand name.
Today, inrevium offers FPGA platform solutions, market specific IP, technical support, and design services to customers worldwide. Inrevium's domain-specific expertise, market knowledge, and pre-qualified solutions, resulted in inrevium being adorned with the prestigious Xilinx Alliance Program Member designation.

With design and development centers in Japan, China, and Canada, and a global network of sales offices, inrevium remains uniquely positioned to provide high-value design services. In addition to services, the development centers also create market-specific multi-million gate LSI devices, FPGA evaluation boards, FMC option cards, ASIC prototyping boards, drivers, firmware, and IP, to support a wide range of worldwide applications.

FPGA Evaluation Platforms

The inrevium Xilinx FPGA Evaluation Kits are special-purpose FPGA kits intended for use by design professionals, innovating and delivering stunning 3D, Organic Light Emitting Diode (OLED), Quad HD (4K2K resolution) and many other digital display technologies, as well as 3D TV broadcasting.



KINTEX UltraSCALE : 8K4K Image Evaluation Platform

FPGA Mezzanine Card (FMC) Standard

Developed by a consortium of companies ranging from FPGA vendors to end users, the FPGA Mezzanine Card is an ANSI standard that provides a standard Mezzanine Card form factor, connectors and modular interface to an FPGA located on a base board.

FMC is VITA 57 standard, provides a specification describing an I/O mezzanine module with connection to an FPGA or other device with reconfigurable I/O capability.

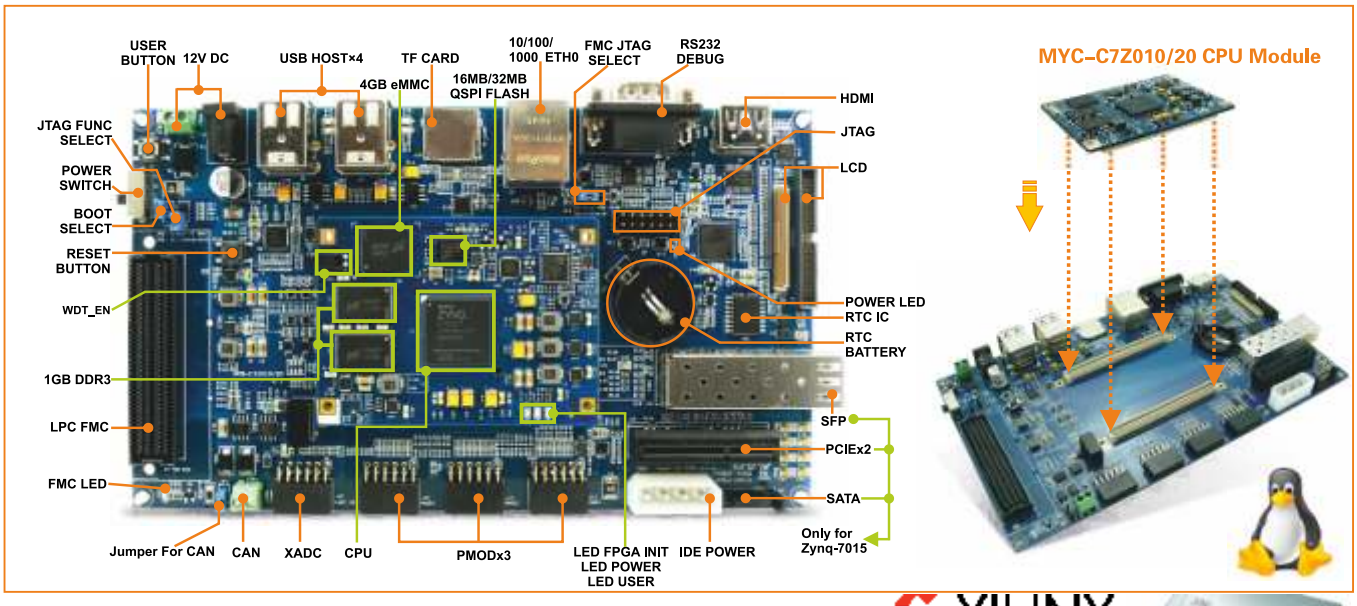
USB3.0 FMC Connectivity mezzanine card

These are just selected examples of a wide variety of FPGA boards and FMC cards from inrevium.
Please have a look in our online shop or contact us at sales@trenz.biz
to get a quote for any available inrevium product.

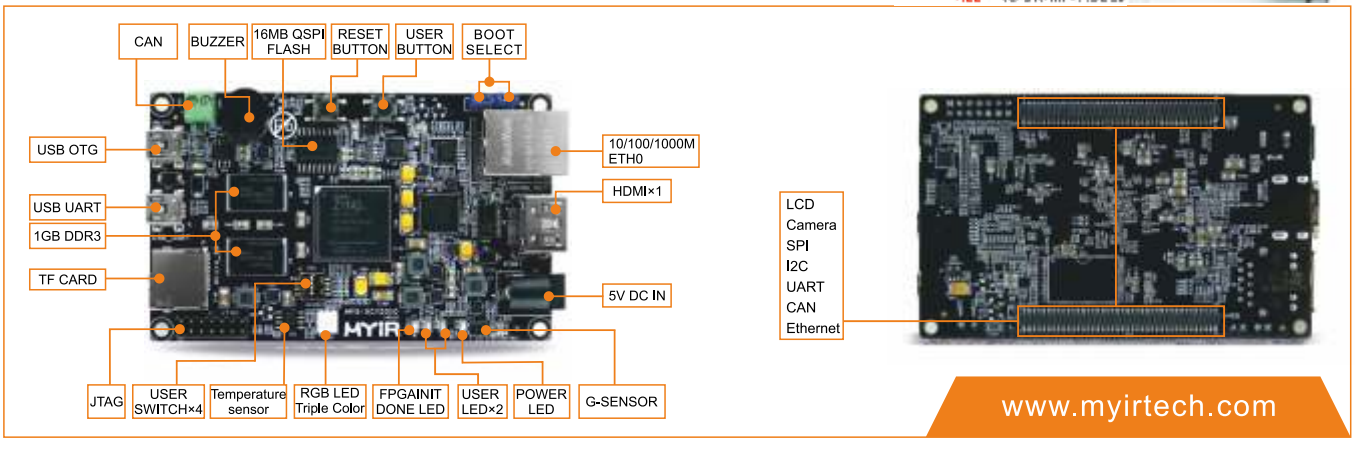
MYIR Tech Limited is a global provider of ARM hardware and software tools, design solutions for embedded applications.

MYIR is an ARM Connected Community Member and work closely with ARM and many semiconductor vendors. They sell products ranging from board level products such as development boards, single board computers and CPU modules to help with your evaluation, prototype, and system integration or creating your own applications. Their products are used widely in industrial control, medical devices, consumer electronic, telecommunication systems, Human Machine Interface (HMI) and more other embedded applications. MYIR has an experienced team and provides custom services based on many processors (especially ARM processors) to help customers make your idea a reality.

MYD–C7Z010/20 Development Board



Z-turn Board



These are just selected examples of a variety of FPGA boards from MYIR. Please have a look in our online shop or contact us at sales@trenz.biz to get a quote for any available MYIR product.

cronologic GmbH & Co. KG

cronologic offers a family of high-resolution high-throughput PCIe analog-to-digital converters (ADCs)

All models share these characteristics:

- 4 analog input channels
- Additional digital trigger and/or gating inputs
- PCIe x4 or x8 half-size boards
- Gross DMA-bandwidth of 1 Gbyte/s or 2 Gbyte/s
- Arbitrary board combinations can be synchronized
- LEMO 00 series input connectors (adapter cabels to SMA connector available)
- The DC-offset can be shiftet to make optimal use of the ADC range for either positiv or negative pulses

	Ndigo5G-10			Ndigo5G-8			Ndigo250M-14	Violet250M-14	Violet125M-14	unit
PCIe lanes	4			4			8	8	8	-
PCIe Bandwidth	800			800			1400	1400	1400	MB/s
Analog channels	4	2	1	4	2	1	4	4	4	-
Max. Sampling Rate	1250	2500	5000	1250	2500	5000	250	250	125	Msps
Max. Bandwidth	1000			1000			120 or 700	120 or 700	60 or 450	MHz
Max. individual sample length	26	26	26	26			32	hours	hours	µs
THD	58	58	58	58			73	73	86	dBc
SNR	51	50	50	45			64	64	74	dBc
SFDR incl.	61	60	60	58			74	74	86	dBc
SFDR excl.	74	64	63	57			76	76	TBD	dBc
SINAD	50	48	48	45			64	64	72	dBc
ENOB	8.0	7.7	7.7	7.2			10.3	10.3	11.0	-
Input type and coupling	AC single ended			AC single ended			DC single ended	DC single ended	DC single ended	-

Ndigo Series

It has been designed to aquire trains of pulses at high repetition rates. Employing an onboard zero supression, the pulse data is recorded with pre- and post-cursors, omitting the data inbetween to reduce the requirements on bandwidth and pulse processing or averaging. There is no deadtime between samples as long as the sustained rate is lower than the available PCIe bandwidth. The first available instances of this series provide 5 Gsps at 10-bit resolutuion and 250 Msps at 14-bit resolution.

These boards are ideally suited for applications like

- Mass Spectrometry
- Photon Counting
- Lidar
- NMR