



Welcome to E-XFL.COM

Understanding [Embedded - Microcontroller, Microprocessor, FPGA Modules](#)

Embedded - Microcontroller, Microprocessor, and FPGA Modules are fundamental components in modern electronic systems, offering a wide range of functionalities and capabilities. Microcontrollers are compact integrated circuits designed to execute specific control tasks within an embedded system. They typically include a processor, memory, and input/output peripherals on a single chip. Microprocessors, on the other hand, are more powerful processing units used in complex computing tasks, often requiring external memory and peripherals. FPGAs (Field Programmable Gate Arrays) are highly flexible devices that can be configured by the user to perform specific logic functions, making them invaluable in applications requiring customization and adaptability.

Applications of [Embedded - Microcontroller,](#)

Details

Product Status	Discontinued at Digi-Key
Module/Board Type	FPGA Core
Core Processor	Kintex-7 70T
Co-Processor	-
Speed	200MHz
Flash Size	32MB
RAM Size	-
Connector Type	Samtec LSHM
Size / Dimension	1.97" x 1.57" (50mm x 40mm)
Operating Temperature	-40°C ~ 85°C
Purchase URL	https://www.e-xfl.com/product-detail/trenz-electronic/te0741-02-070-2if



Overview

The Trenz Electronic TE0820 are 4 x 5 standard footprint MPSoC modules integrating a Xilinx Zynq UltraScale+ with up to 4 GByte 32-Bit DDR4 SDRAM, and max. 512 MByte SPI Boot Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All modules in 4 x 5 cm form factor are fully mechanically and largely electrically compatible among them. All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

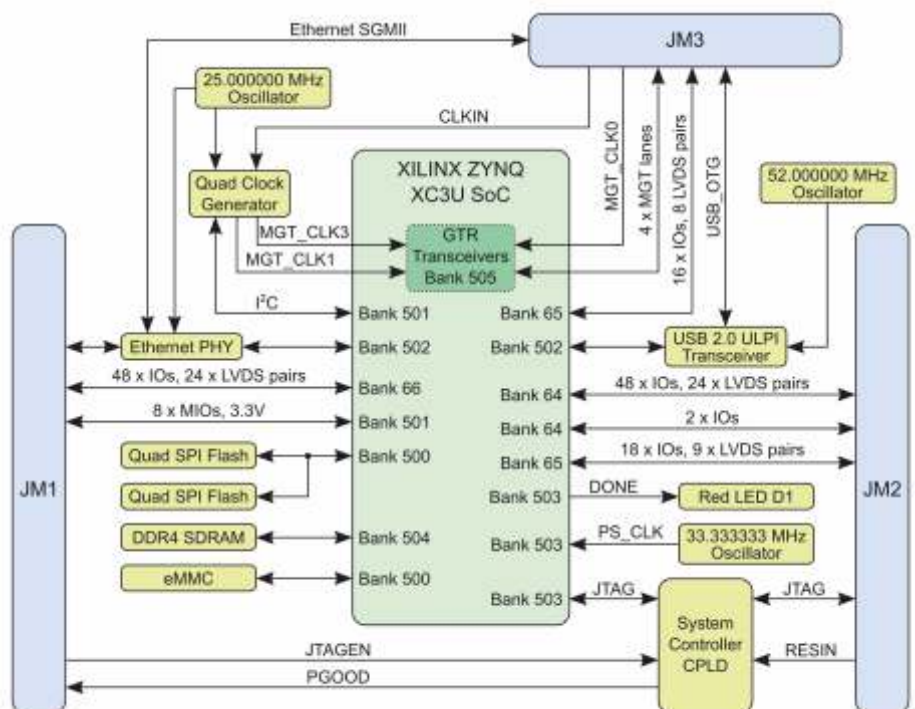
Key Features (preliminary)

- Xilinx Zynq UltraScale+ MPSoC 784 pin package (ZU3EG, option ZU5EV)
- Memory:
 - 32-Bit DDR4 - 4 GByte max
 - SPI Boot Flash dual parallel - 512 MByte max
 - 4 GByte eMMC (up to 64 GByte)
- B2B connectors:
 - Plug-on module with 2 x 100-pin and 1 x 60-pin high-speed hermaphroditic strips
 - 14 x MIO, 132 I/O's x HP (3 banks)
 - Serial transceiver: PS GTR 4
 - GT Reference clock input
 - PLL for GT Clocks (optional external reference)
 - 1 GBit Ethernet PHY
 - USB 2.0 OTG PHY
 - Real Time Clock
- Size: 40 x 50 mm
- All power supplies on board.

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle

Rugged for industrial applications





Overview

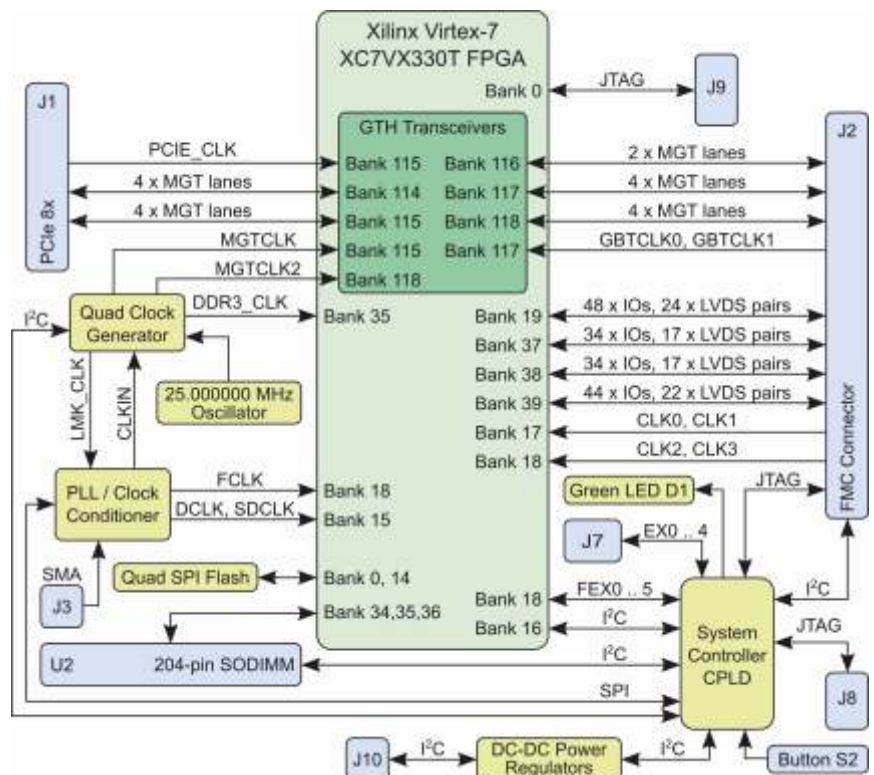
Trenz Electronic TEC0330 is an 8 lanes PCIe GEN2 Card integrating a high performance Xilinx Virtex-7 330T FPGA with 32 MByte Flash memory for configuration and operation, DDR3 SODIMM Socket and full FMC HPC connector for FPGA Mezzanine Cards.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

- FMC HPC
- 8 lane PCIe Gen 2 capable
- Xilinx Virtex-7 XC7VX330T-2FFG1157C
- DDR3 SODIMM Socket
- 32 MByte SPI Flash
- LMK04828B Clock Synthesizer
- External Clock Input

Other assembly options for cost or performance optimization plus high volume prices available on request.





Overview

Trenz Electronic TE0715 are industrial-grade SoC modules integrating a Xilinx Zynq-7000 SoC, a gigabit Ethernet transceiver (physical layer), 1 gigabyte DDR3 SDRAM with 32-Bit width, 32 megabyte Flash memory for configuration and operation, 4 transceivers, a USB ULPI transceiver, and powerful switch-mode power supplies for all onboard voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

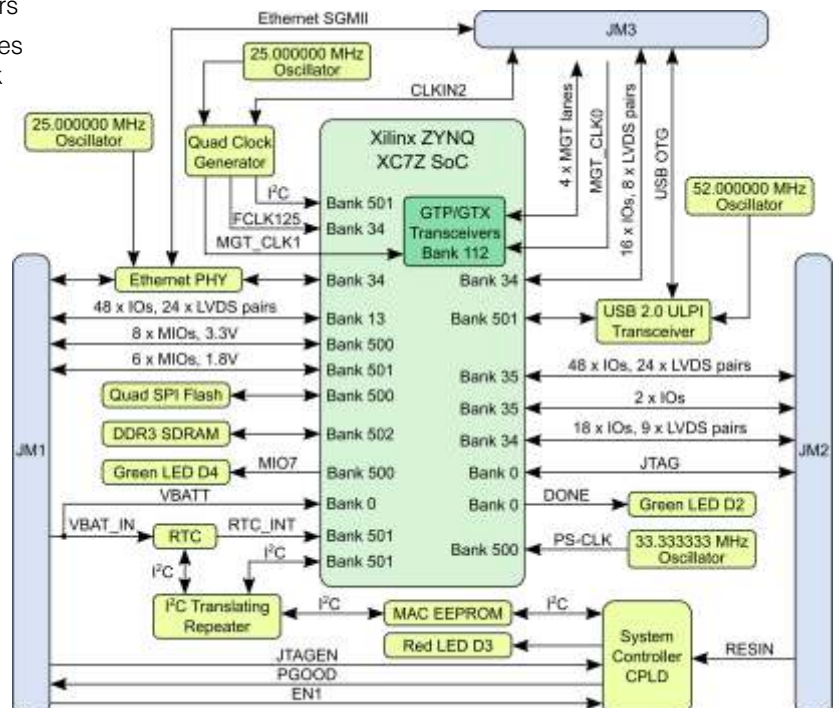
Key Features

- Industrial-grade Xilinx Zynq-7000 (Z-7015, Z-7030) SoM , supported by the free Xilinx Vivado WebPACK tool
- Rugged for shock and high vibration
- ARM dual-core Cortex-A9
- 10/100/1000 tri-speed gigabit Ethernet transceiver (PHY) with SGMII
- MAC Address EEPROM
- 32-Bit-wide 1 GByte DDR3 SDRAM
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
- Transceiver clock (default 125 MHz)
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 132 FPGA I/O's (65 LVDS pairs possible) and 14 PS-MIO available on board-to-board connectors
- 4 GTP/GTX (high-performance transceiver) lanes
GTP/GTX (high-performance transceiver) clock input
- USB 2.0 high-speed ULPI transceiver
- On-board high-efficiency DC-DC converters
- 4.0 A x 1.0 V power rail
- 1.5 A x 1.5 V power rail
- 1.5 A x 1.8 V power rail
- System management
- eFUSE bit-stream encryption
- AES bit-stream encryption
- Temperature compensated RTC (real-time clock)
- User LED
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle

Rugged for industrial applications





Overview

The Trenz Electronic TE0728-04-1Q is a SoC module integrating a Xilinx Automotive Zynq-7020, 512 MByte DDR3 SDRAM with 16-Bit width, 16 MByte Flash Memory for configuration and operation, two 100 Megabit Ethernet transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

Within the complete module only Automotive components are installed. All this in a compact 6 x 6 cm form factor, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

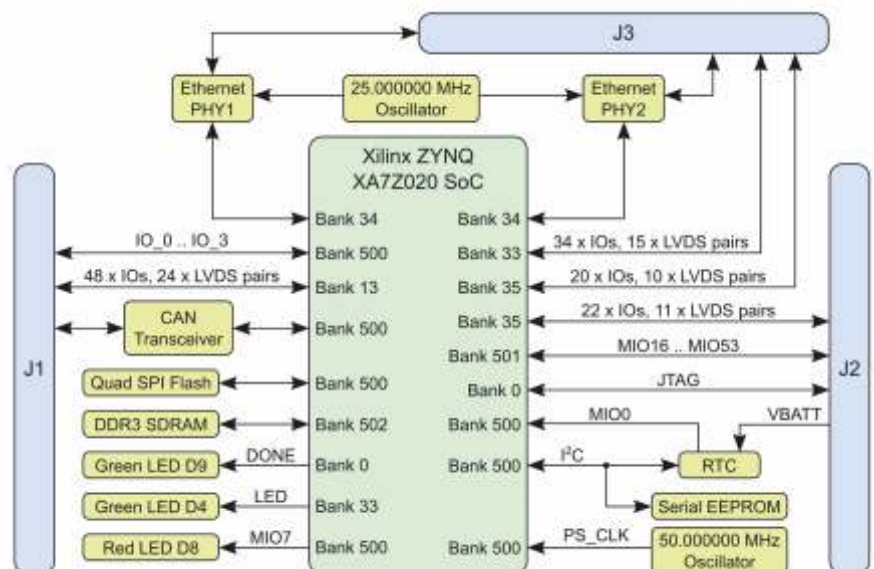
Key Features

- Xilinx Zynq XA7Z020-1CLG484Q (Automotive)
- Rugged for shock and high vibration
- 2 x ARM Cortex-A9 MPCore
- 2 x 100 MBit Ethernet transceiver (PHY)
- 16-Bit-wide 512 MByte DDR3 SDRAM
- 16 MByte QSPI Flash memory (with XiP support)
- Plug-on module with 3 x 80-pin high-reliability high-speed strips
- 76 single ended I/O, 24 LVDS pairs (48 I/O) and 42 MIO available on
- board-to-board connectors
- Board-to-board connectors
- CAN transceiver (PHY)
- Temperature compensated RTC (real-time clock)
- 12 V power supply with watchdog
- On-board high-efficiency DC-DC converters
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- 3 user LEDs
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle

Rugged for automotive applications





Overview

The Trenz Electronic TE0723 is a Arduino compatible FPGA module integrating a Xilinx Zynq-7010, 512 MByte DDR3L, and 16 MByte SPI Flash Memory for configuration and operation.

The "ArduZynq" is the lowest cost, Linux ready solution to use the latest FPGA: the Xilinx 7 series. Use it as a FPGA development platform, or run Linux on the Cortex A9 cores.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

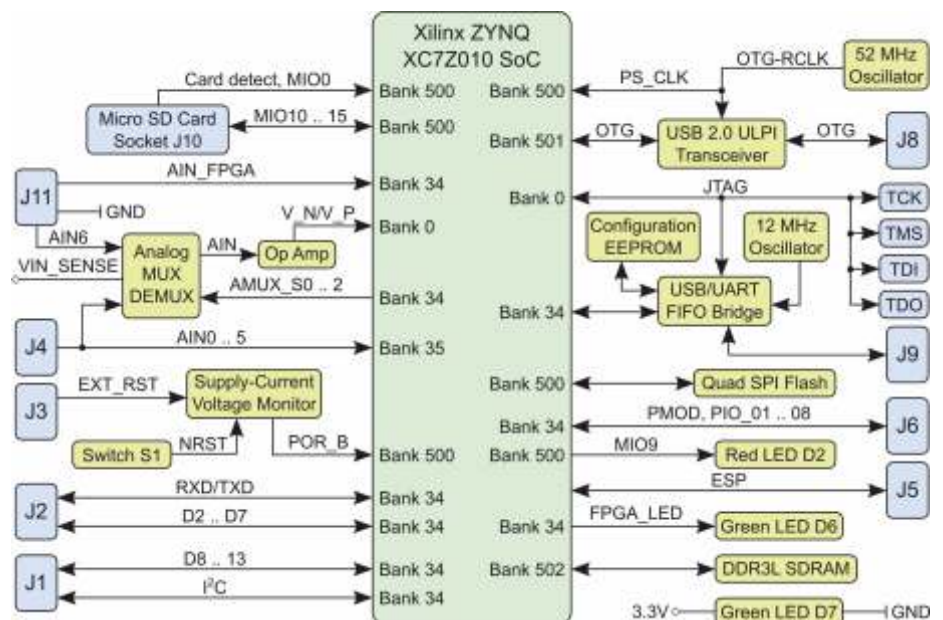
Key Features

- Xilinx XC7Z010-1CLG225C
- Form Factor: Arduino Shield
- Dual ARM Cortex-A9
- Up to 512 MByte DDR3L
- 16 MByte SPI Flash memory
- 12 MHz MEMS Oscillator low power consumption
- Hi-Speed USB2.0 ULPI Transceiver
- 23 FPGA I/O's available on board-to-board connectors
- MicroSD Card socket
- Micro USB OTG
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- On-board USB JTAG and UART
- CERN Open Hardware Licence 1.2

Other assembly options for cost or performance optimization available or high volume prices on request.

Extended device life cycle

"Ideal for Maker"
Make:





Overview

The Trenz Electronic TE0726 is a Raspberry Pi compatible FPGA module integrating a Xilinx Zynq-7010, 512 MByte DDR3L SDRAM, 4 USB ports, an Ethernet port and 16 MByte Flash memory for configuration und operation.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

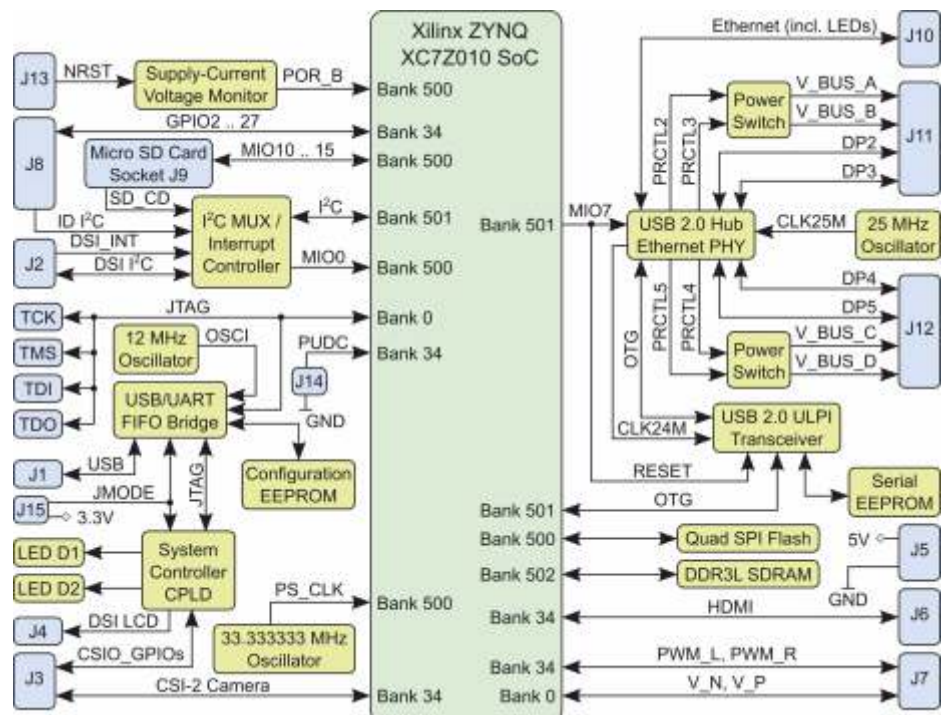
Extended device life cycle

"Ideal for Maker"
Make:

Key Features

- Xilinx Zynq XC7Z010-1CLG225C
 - 512 MByte DDR3L SDRAM
 - 16 MByte Flash
- LAN9514 USB Hub with Ethernet
 - 4 x USB with power switches
 - 100 MBit Ethernet RJ45
- Micro SD card slot
- HDMI Typ A
- DSI Connector (Display)
- CSI-2 Connector (Camera)
- Micro USB
 - power input
 - USB UART
 - JTAG ARM- und FPGA-Debug
- 3.5 mm audio plug (PWM Audio output only)
- Raspberry Pi Model 2 form faktor
- HAT header with 26 I/O's

Other assembly options for cost or performance optimization plus high volume prices available on request.





Overview

The DIPFORTy1 "Soft Propeller" is based on the Xilinx Zynq-7000, a System on Chip which contains a FPGA and a Dual Core ARM A9+ processor with enough logic gates to become a Propeller. The board also has 16 MByte of Flash used for configuration and everything fits on a Propeller-compatible DIP 40 pinout.

DIPFORTy1 "Soft-Propeller" is the lowest cost Zynq based module ever made and the first Zynq module that can use existing bases and project boards (Parallax Propeller chip compatibility). All this in a compact 1.8 x 5.1 cm form factor, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

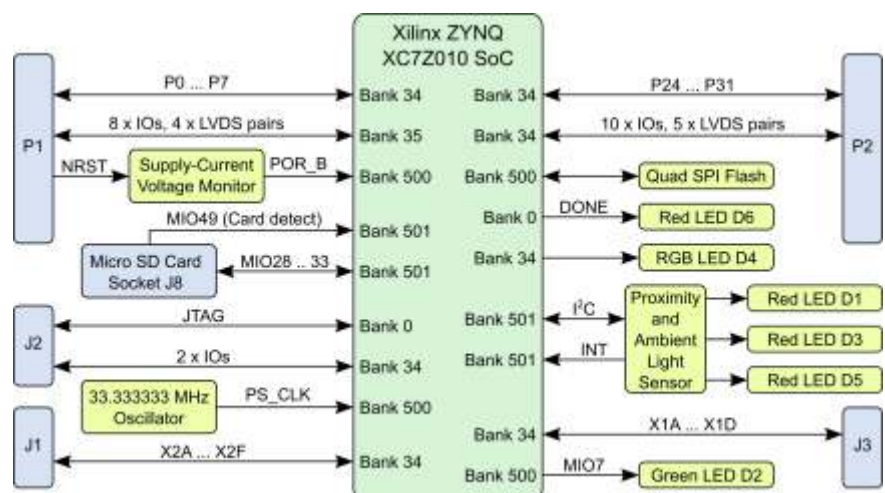
- Xilinx Zynq-7: XC7Z010-CLG225
 - 16 MByte SPI Flash (primary boot)
 - 33.333 MHz Clock (MEMS Oscillator)
- Dual Core ARM A9+
- DIP40 form factor
 - 2 x 20 holes for socket pins or pin-header
 - Size: 18 x 51 mm
- Total user accessible PL I/O: 46 (+3 Input only)
 - DIP40 header pins: 34 I/O
 - XMOD J1: 6 I/O
 - XMOD J2: JTAG + 2 I/O (or 3 input + 2 I/O)
 - XMOD J3: 4 I/O
- 3.3V single supply
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- User LED (ARM CPU MIO GPIO)
- MicroSD Card socket (MIO, ZYNQ secondary boot media)
- SiI1143 Proximity and ambient light sensor

Other assembly options for cost or performance optimization available or high volume prices on request.

Extended device life cycle

"Ideal for Maker"

Make:





Overview

The Trenz Electronic TE0841 is an industrial-grade FPGA module integrating a Xilinx Kintex UltraScale KU35 or KU40, max. 4 GByte 16-Bit width DDR4, max. 256 MByte QSPI Flash for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

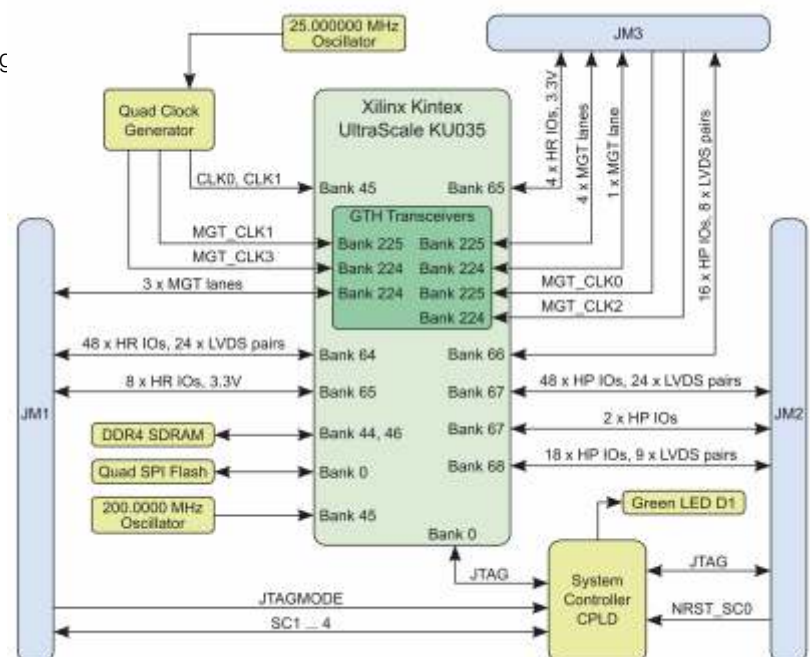
Key Features

- Xilinx Kintex UltraScale KU35 or KU40 FPGA
- Rugged for industrial application
- max. 4 GByte 16-bit wide DDR4
- max. 256 MBit (32 MByte) SPI Boot Flash
- Size: 40 x 50 mm
- 3 mm mounting holes for skyline heat spreader
- B2B Connectors: 3 x Razor Beam, total 260 terminals
 - User I/O: HR 12, HP 132
 - Serial transceiver: GTH 8 lanes (all)
 - GT clock inputs: 2
- Clocking
 - Si5338 - 4 output PLL, GT and PL clocks
 - 200 MHz LVDS oscillator
- All power supplies on board, single supply operation supported.
- Evenly spread supply pins for good signal integ

Other assembly options for cost or performance optimization available plus high volume prices on request.

Extended device life cycle

Rugged for industrial applications





Overview

Trenz Electronic TE0741 are industrial-grade FPGA modules integrating a Xilinx Kintex-7 T FPGA, 32 MByte Flash memory for configuration and operation, 8 transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

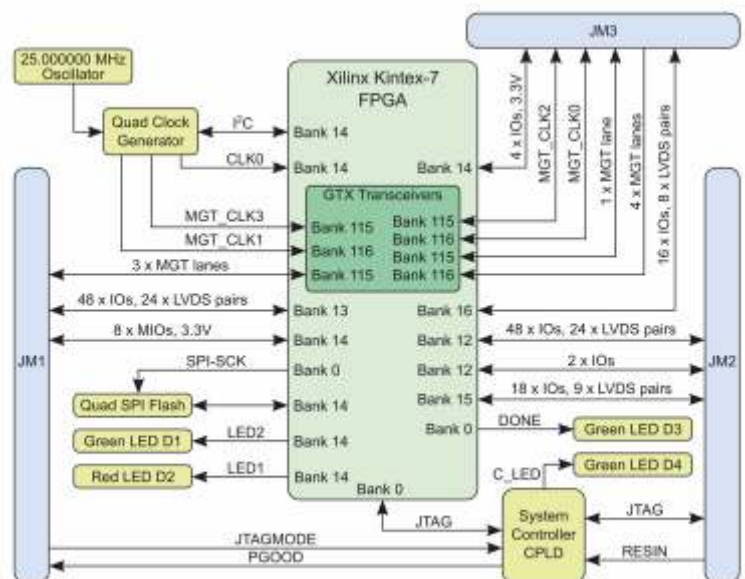
- Industrial-grade Xilinx Kintex-7 (70T, 160T, 325T and 410T) SoM
- Rugged for shock and high vibration
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
 - 2 x Transceiver clock (default 125 MHz)
 - Fabric clock (default 200 MHz)
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 144 FPGA I/O's (65 LVDS pairs possible) available on board-to-board connectors
- 8 GTX (high-performance transceiver) lanes
 - GTX (high-performance transceiver) clock input
- On-board high-efficiency DC-DC converters
 - 20.0 A x 1.0 V power rail
 - 1.5 A x 1.8 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User LEDs
- Evenly spread supply pins for good signal integrity

Recommended Software:

- Kintex-7 XC7K70T-2CF, Kintex-7 XC7K160T-2CF: Xilinx Vivado Webpack (free license)
- Kintex-7 XC7K325T-2CF, Kintex-7 XC7K410T-2CF: Xilinx Vivado Design Suite

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle
Rugged for industrial applications





Overview

Trenz Electronic TE0711 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, 32 MByte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-number of board voltages. A large configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

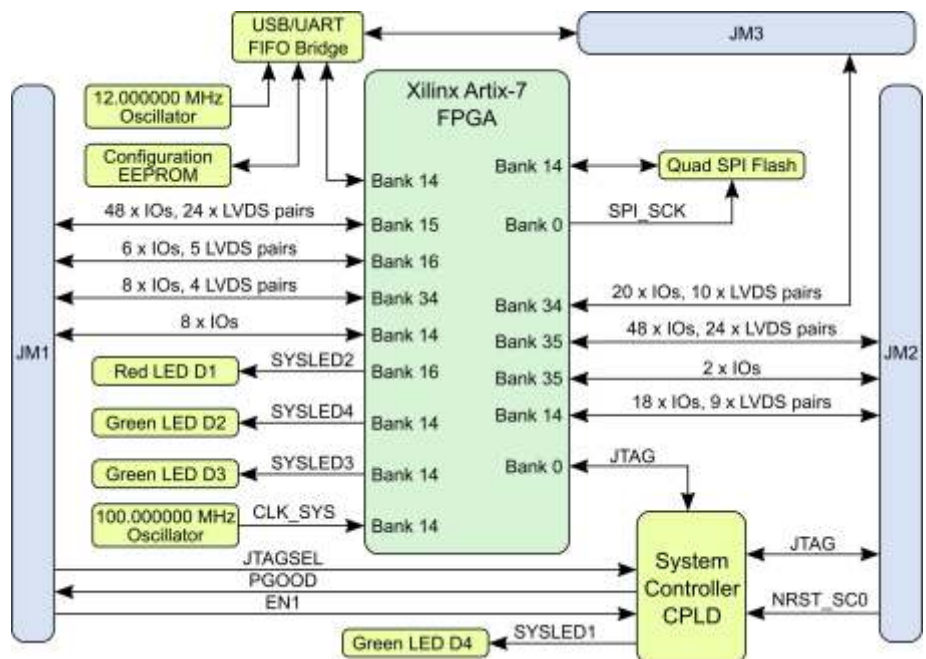
Key Features

- Industrial-grade Xilinx Artix-7 (15T to 100T) SoM, supported by the free Xilinx Vivado WebPACK tool
- Rugged for shock and high vibration
- 32 MByte QSPI Flash memory (with XiP support)
- 100 MHz programmable MEMS oscillator
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 178 FPGA I/O's (84 differential pairs) available on board-to-board connectors
- On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.0 A x 1.8 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- 3 user LEDs
- FTDI USB to UART/FIFO bridge
- Evenly-spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Extended device life cycle

Rugged for industrial applications





Overview

Trenz Electronic TE0712 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, a Megabit Ethernet transceiver (physical layer), 1 Gigabyte DDR3 SDRAM with 32-Bit width, 32 Megabyte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

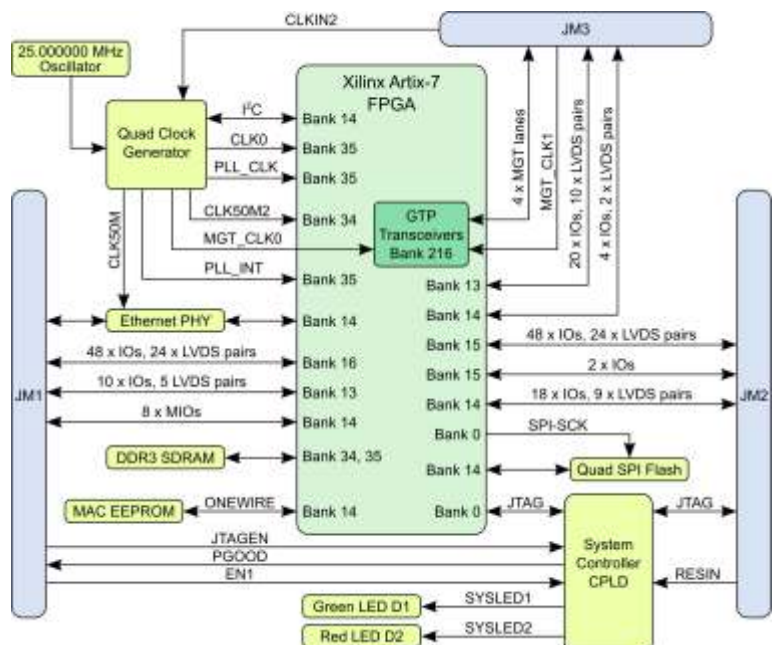
All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

- Industrial-grade Xilinx Artix-7 (15T to 200T) SoM, supported by the free Xilinx Vivado WebPACK tool
- Rugged for shock and high vibration
- 1 GByte DDR3 SDRAM
- 100 MBit Ethernet PHY
 - MAC Address EEPROM
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
 - Transceiver clock (default 125 MHz)
 - Fabric clock (default 200 MHz)
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 158 FPGA I/O's (78 differential pairs) available on board-to-board connectors
- 4 GTP (high-performance transceiver) lanes
 - GTP (high-performance transceiver) clock input
- On-board high-efficiency DC-DC converters
 - 12 A x 1.0 V power rail
 - 1.5 A x 1.8 V power rail
 - 1.5 A x 1.5 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User LED
- Evenly spread supply pins for good signal integrity

Extended device life cycle

Rugged for industrial applications





Overview

Xmod-USB-X is a universal USB adapter with 2 channels based on FTDI FT2232H USB2 HS Interface chip.

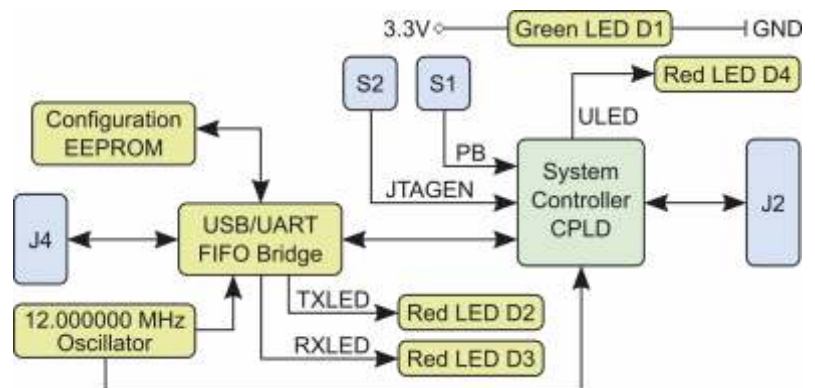
In the consigned default configuration Port A is JTAG and Port B is a serial interface. FT2232H port A and B are connected to small on-board programmable CPLD to allow flexible application specific remappings of FT2232H functions into 8 user I/O pins of single Xmod 12x8 Module.

Minimum PCB area on base board to support JTAG function 5 x 10 mm (does not include mounting hole space). TE0790 is compatible with Xilinx Tools in contrast to the TE0790-01L, that can be used flexibly.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

- Xmod form-factor
 - Supported base slots: 6 x 2, 8 x 4, 10 x 6, 12 x 8, 5 x 2, 5 x 3
 - Size: 20 x 25 mm
 - M3 mounting hole
- FT2232H
 - Channel B RX/TX LED's (on top, not visible from front)
 - Mini-USB connector (more rugged then micro-USB)
 - 93C56 EEPROM
- Lattice XO2-256 CPLD
 - On board programmable using Lattice tools
 - 8 universal I/O pins
 - VCCIO either 3.3 V or user supplied (1.8 to 3.3V)
 - Red user LED (front visible)
 - 12 MHz clock from on-board Oscillator
- LDO for optional USB power
- Green Power-on LED (front visible)
- User button (front accesible)
- 4 position DIP switch
 - Choose CPLD program mode
 - FTDI EEPROM disable (not implemented in PCB REV 1)
 - Use VIO same as VCC
 - Use VCC from USB



The Trenz Electronic Carrier Boards are base-boards for 4 x 5 SoMs, which exposes the modules B2B-connector-pins to accessible connectors and provides a whole range of on-board components to test and evaluate Trenz Electronic 4 x 5 SoMs.

TE0701

- Overvoltage-, undervoltage- and reversed- supply-voltage-protection
- Barrel jack for 12 V power supply
- Carrier Board System-Controller CPLD
- Mini CameraLink connector
- RJ45 Gigabit Ethernet MagJack
- FPGA Mezzanine Card (FMC) connector
- USB JTAG- and UART interface with Mini-USB connector
- HDMI transmitter with HDMI connector
- 8 x user LED's, 2 x user push buttons, 2 x DIP switch
- PMOD connectors, Micro SD card socket and Micro-USB interface



TE0703

- 2 x VG96 backplane connectors (mounting holes and solder pads)
- SDIO port expander with voltage-level translation
- Micro SD card socket
- 4 x user LED's, 1 x user-push button, 2 x user configurable DIP switches
- Mini USB connector (USB JTAG and UART interface)
- RJ45 Gigabit Ethernet socket with 4 integrated LED's.
- USB host connector
- Barrel jack for 5 V power supply input
- DC-DC step-down converter for 3.3 V power supply
- USB JTAG and UART interface



TE0705

TE0705 is a "downgraded" version of TE0701. As little as possible has been changed in functionality except the functionality that was removed.

Changes from TE0701

- PMOD connectors changed to IDC headers
- HDMI removed
- CL connector removed
- USB connector position changed
- 5 pin header support added on both USB interfaces
- 12 V DC power input connector changed to different type
- FMC connector removed and replaced by two dual row 100 mil pin headers



TE0706

- VG96 backplane connector and 50-pin IDC male connector socket
- SDIO port expander with voltage-level translation
- Micro SD card socket and a USB type A connector
- 1 x user push button, user configurable DIP switch
- 1 x RJ45 Gigabit Ethernet MagJack
- 1 x Ethernet PHY
- Barrel jack for 5 V power supply input
- DC-DC step- down converter for 3.3 V power supply
- JTAG pins on 12-pin header
- 3 x VCCIO selection jumper



TEBA0841

Mainly for the use with TE0841 and TE0741 modules.

- XMOD (TE0790) pin header
- SFP connector
- Micro USB
- 1 x pin header 16 pol. (JTAG, MGT-CLK, boot mode, RST, IOs)
- 1 x pin header 10 pol. (SD IOs)
- 2 x pin headers 50 pol. (FPGA bank IOs and power)
- 1 x pin header for FPGA bank power VCCIOA and 1 x for VCCIOD
- LDO voltage regulator 3.3 V to 2.5 V
- 2 x user LED's (Red/Green)



Following Trenz Electronic Carrier Boards are custom-built base boards for specific Trenz Electronic SoMs, which exposes the module's B2B-connector-pins to accessible connectors and provides a whole range of on-board components to test and evaluate Trenz Electronic SoMs.

TEBF0808

- Mini-ITX form factor
- ATX power supply connector (Important 12 V only supply required)
- optional 12 V standard power plug
- USB 3.0 with USB 3.0 HUB
- Gigabit Ethernet RJ45
- MicroSD Card (bootable) and eMMC (bootable)
- PCIe slot - one PCIe lane (16 Lane connector)

- Displayport Single Lane
- One SATA Connector
- Dual SFP+
- FMC HPC slot (1.8 V max VCCIO)
- Fan connectors, PC enclosure, FMC fan
- Intel front panel- and HDA audio-connector
- CAN FD transceiver (10 pin IDC connector)
- 20 pins ARM JTAG connector (PS JTAG0)
- One Samtec FireFly (4 GT lanes bidir)
- One Samtec FireFly connector for reverse loopback



TEB0728

- Trenz TE0728 module socket (3 x Samtec SEM connectors 80 pins)
- 2 x RJ45 Ethernet
- SD card slot
- Power supply with DC jack
- 3 x user LED's (red/yellow/green)
- User push button



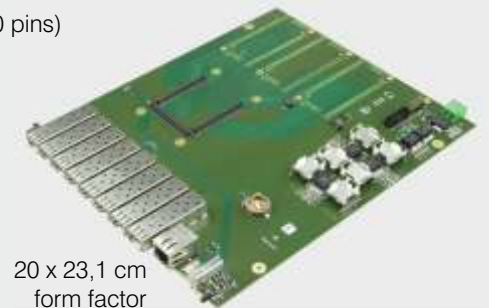
TEB0729

- Trenz TE0729 module socket (2 x Samtec BTE/BSE connectors 120 pins)
- 5 V board supply via DC jack
- 3 x RJ45 Ethernet
- 1 x MicroUSB and 1 x SD card connector
- 1 x 128K I2C CMOS Serial EEPROM
- 1 x 2K I2C Serial EEPROM
- XMOD (TE0790) pin header
- 2 x pin header FPGA bank power supply
- 1 x VBat pin header and 2 x VG96 pin header
- 1 x user push button, 1 x LED (red), user switch FPGA boot mode



TEB0745

- Trenz TE0745 module socket (3 x Samtec ST5 connectors 160 pins)
- 24 V power supply over ARKZ950/2 connecting terminal
- XMOD (TE0790) Pin Header (JTAG / UART)
- 1 x EMI Network Filter
- microSD connector
- RJ45 Ethernet connector
- USB Host connector
- 8 x SFP connector
- 6 x pin header 50 pol. (FPGA bank I/O's and power)
- 6 x pin header 12 pol. (FPGA bank I/O's and power)



TEBA0714

- Trenz TE0714 module socket (2 x Samtec LSHM connectors 100 pins)
- XMOD (TE0790) pin header
- 1 x pin header 16 pol. (JTAG, MGT-CLK, boot mode, XADC, I/O's)
- 1 x pin header 10 pol. (I/O's)
- SFP connector
- LDO voltage regulator 3.3 V to 2.5 V
- 2 x user LED's (red/green) and 1 x LED (red)
- 2 x pin headers 50 pol. (FPGA bank I/O's and power)
- 1 x pin header for FPGA bank power VCCIO34 (1.8 VOUT, 2.5 V, 3.3 VOUT)
- 1 x pin header for FPGA bank power V_CFG (1.8 VOUT, 2.5 V, 3.3 VOUT)



TE0728	TE0729	TE0741	TE0745	TE0782	TE803	TE0808 „UltraSOM+“	TE820	TE0841
Z-7020 (automotive)	Z-7020	70T 160T 325T 410T	Z-7030 Z-7035 Z-7045	Z-7035 Z-7045 Z-7100	ZU3EG, ZU5EV, (ZU2CG-ZU5CG, ZU2EG-ZU5EG, ZU4EV, ZU5EV)	ZU9EG (ZU6CG, ZU9CG, ZU6EG, ZU9EG, ZU15EG)	ZU3EG, ZU5EV, (ZU2CG-ZU5CG ZU2EG-ZU5EG ZU4EV, ZU5EV)	KU35 KU40
3 x Samtec SEM	2 x Samtec BTE	3 x Samtec LSHM	3 x Samtec ST5	3 x Samtec QTH	4 x Samtec ST5	4 x Samtec ST5	3 x Samtec LSHM	3 x Samtec LSHM
Artix-7	Artix-7	Kintex-7	Kintex-7	Kintex-7	UltraScale+	UltraScale+	UltraScale+	UltraScale
2 x Cortex A9	2 x Cortex A9	MicroBlaze	2 x Cortex A9	2 x Cortex A9	4 x Cortex A53+ 2 Cortex R5	4 x Cortex A53 + 2 Cortex R5	4 x Cortex A53+ 2 Cortex R5	MicroBlaze
512 DDR3	512 DDR3	-	1024 DDR3/L	1024 DDR3	8192 DDR4	8192 DDR4	4096 DDR4	4096 DDR4
16	32	32	32	32	64	64	64	32
8 KByte	3 x MAC	-	MAC	2 x MAC + 16 KByte	-	16 KByte	-	-
	4 - 64 GByte	-	-	4 - 64 GByte	-	-	4 - 64 GByte	-
2 x 100 MBit	2 x 100 MBit, 1 GBit	-	1 GBit	2 x 1 GBit	-	-	4 - 64 GByte	1 GBit
-	USB 2.0 OTG PHY	-	USB 2.0 OTG PHY	2 x USB 2.0 OTG	-	-	USB 2.0 OTG	-
124 + 34 MIO	136 + 14 MIO	144	250 + 6 MIO	250 + 2 MIO	156 + 65 MIO	204 + 65 MIO	132 + 14 MIO	144
-	-	8 x GTX	8 x GTX	16 x GTX	PS GTR 4	4 x GTR, 16 x GTH	PS GTR 4	8 x GTH
Automotive, RTC, CAN	RTC, Cooling Solution available	Programmable Clock Generator	RTC	Programmable Clock Generator, RTC, eMMC	Programmable Clock Generator	System Monitor, Programmable Clock Generator	Programmable Clock Generator, Real Time Clock, eMMC	Programmable Clock Generator

*modules with form factor 5.2. x 7.6 cm are not compatible with each other



oi710 - Quad 1.2GHz DAC

www.sundance.technology/oi710/

The oi710 a combination of a DAC module (SMT-FMC211) and the EMC²-DP. As such this system gives you four 1.25GHz DAC channels on a OneBank[®] PC/104 FPGA carrier card.

- Quad channel DAC (TI DAC3484)
- DAC control by Artix-7 FPGA (XC7A15T)
- 16-bit DDR3 local memory for DAC data
- I²C bus for control
- External clock and triggers



oi816 - Octal 16BIT ADC

www.sundance.technology/oi816/

A combination of an eight channel ADC module (FMC168) and the EMC²-DP. This system gives you eight 16-bit ADC channels at 250MSPS on a OneBank[®] PC/104 FPGA carrier card.

- Eight-channel 16-bit 250MSPS A/D conversion
- Available as air cooled and conduction cooled
- VITA 57.1-2010 compliant
- Based on TI ADS42LB69
- Coaxial front panel inputs on SSMC connectors
- Single ended AC or DC coupled analogue input
- Flexible clock tree enables:
 - internal clock
 - internal clock locked to an external reference
 - external clock
 - external sync / 1PPS



EMC²-DP stackable box

Coming soon!

Here's a preview of our stackable ruggedised case for the EMC²-DP. Currently in the R&D phase so please get in touch with any customisation requests!

**Sundance Multiprocessor Technology Ltd.
Unit 20 Chiltern House, Waterside,
Chesham, HP5 1PS.
United Kingdom.**

Phone: +44 (0) 1494 793 167

Email: enquiries@sundance.com

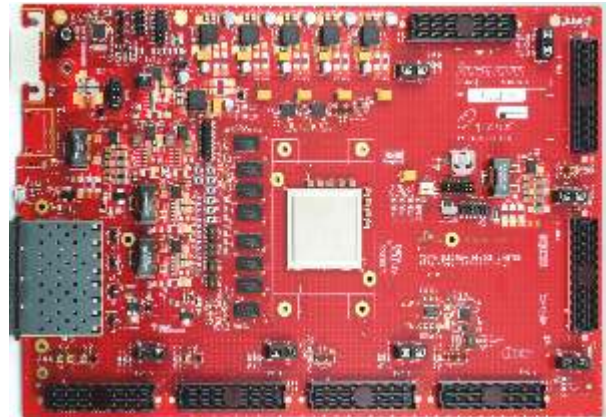
Since 1965, Tokyo Electron Device Limited (TED) has been focused on the semiconductor distribution business. Leveraging 40 years of industry experience, in 2004, TED began offering FPGA solutions under the “inrevium” brand name.

Today, inrevium offers FPGA platform solutions, market specific IP, technical support, and design services to customers worldwide. Inrevium's domain-specific expertise, market knowledge, and pre-qualified solutions, resulted in inrevium being adorned with the prestigious Xilinx Alliance Program Member designation.

With design and development centers in Japan, China, and Canada, and a global network of sales offices, inrevium remains uniquely positioned to provide high-value design services. In addition to services, the development centers also create market-specific multi-million gate LSI devices, FPGA evaluation boards, FMC option cards, ASIC prototyping boards, drivers, firmware, and IP, to support a wide range of worldwide applications.

FPGA Evaluation Platforms

The inrevium Xilinx FPGA Evaluation Kits are special-purpose FPGA kits intended for use by design professionals, innovating and delivering stunning 3D, Organic Light Emitting Diode (OLED), Quad HD (4K2K resolution) and many other digital display technologies, as well as 3D TV broadcasting.



KINTEX UltraSCALE : 8K4K Image Evaluation Platform

FPGA Mezzanine Card (FMC) Standard

Developed by a consortium of companies ranging from FPGA vendors to end users, the FPGA Mezzanine Card is an ANSI standard that provides a standard Mezzanine Card form factor, connectors and modular interface to an FPGA located on a base board.

FMC is VITA 57 standard, provides a specification describing an I/O mezzanine module with connection to an FPGA or other device with reconfigurable I/O capability.



USB3.0 FMC Connectivity mezzanine card

These are just selected examples of a wide variety of FPGA boards and FMC cards from inrevium. Please have a look in our online shop or contact us at sales@trenz.biz to get a quote for any available inrevium product.

cronologic GmbH & Co. KG

cronologic offers a family of high-resolution high-throughput PCIe analog-to-digital converters (ADCs)

All models share these characteristics:

- 4 analog input channels
- Additional digital trigger and/or gating inputs
- PCIe x4 or x8 half-size boards
- Gross DMA-bandwidth of 1 Gbyte/s or 2 Gbyte/s
- Arbitrary board combinations can be synchronized
- LEMO 00 series input connectors (adapter cables to SMA connector available)
- The DC-offset can be shifted to make optimal use of the ADC range for either positive or negative pulses

	Ndigo5G-10			Ndigo5G-8			Ndigo250M-14	Violet250M-14	Violet125M-14	unit
PCIe lanes	4			4			8	8	8	-
PCIe Bandwidth	800			800			1400	1400	1400	MB/s
Analog channels	4	2	1	4	2	1	4	4	4	-
Max. Sampling Rate	1250	2500	5000	1250	2500	5000	250	250	125	Msp/s
Max. Bandwidth	1000			1000			120 or 700	120 or 700	60 or 450	MHz
Max. individual sample length	26	26	26	26			32	hours	hours	µs
THD	58	58	58	58			73	73	86	dBc
SNR	51	50	50	45			64	64	74	dBc
SFDR incl.	61	60	60	58			74	74	86	dBc
SFDR excl.	74	64	63	57			76	76	TBD	dBc
SINAD	50	48	48	45			64	64	72	dBc
ENOB	8.0	7.7	7.7	7.2			10.3	10.3	11.0	-
Input type and coupling	AC single ended			AC single ended			DC single ended	DC single ended	DC single ended	-

Ndigo Series

It has been designed to acquire trains of pulses at high repetition rates. Employing an onboard zero suppression, the pulse data is recorded with pre- and post-cursors, omitting the data inbetween to reduce the requirements on bandwidth and pulse processing or averaging. There is no deadtime between samples as long as the sustained rate is lower than the available PCIe bandwidth. The first available instances of this series provide 5 Gbps at 10-bit resolution and 250 Msp/s at 14-bit resolution.

These boards are ideally suited for applications like

- Mass Spectrometry
- Photon Counting
- Lidar
- NMR

Violet Series

It has been designed to continuously stream samples data to host computer main memory at full rate. These boards are ideal for any applications that require unusually long samples at rates up to 250 Msps at a resolution of 14 bits.

Examles are:

- Software Defined RadioHigh Precision FFT Spectrum Analyzers



Time Tagger

Cronologic presents a new series of low cost, mid resolution time-to-digital converters.

Two new board are available featuring 500ps to 1ns single shot resolution at highest data bandwidths.

Time Taggers are ideally suitable in applications that do not require highest single shot timing resolution, but high data acquisition rates and lowest multiple hit deadtime. These include certain types of mass spectroscopy, time correlated single photon counting (TCSPC) and frequency counting applications.



Distributor list



China

Future Linking Solution Tech Co. Ltd
Unit 01 7/F, Bright Way Tower
33 Mong Kok Rd, KL
Hong Kong

Phone: +86-755-82914675
Fax: +86-755-82971302
Web: www.fulso.com
Email: liu@fulso.com



Czech Republic

DFC Design, s.r.o.
Strmá11B
61600 Brno
Czech Republic

Phone: +420 511 112 863
Fax: +420 549 241 114
Web: www.dfcdesign.cz
Email: info@dfcdesign.cz



France

Lextronic
36/40 Rue du Général de Gaulle
94510 La Queue en Brie
France

Phone: +33(0)1.45.76.83.88
Fax: +33 (0)1.45.76.81.41
Web: www.lextronic.fr
Email: lextronic@lextronic.fr



Hungary

ChipCad Ltd.
Könyves Kálmán körút 12-14.
1097 Budapest
Hungary

Phone: +36-1-231-7000
Fax: +36-1-231-7011
Web: www.chipcad.hu
Email: info@chipcad.hu



Italy

Mirifica s.r.l.
Piazza 11 Febbraio, 7
29012 Caorso PC
Italy

Phone: +39 0200611003
Fax: +39 02700446883
Web: www.mirifica.it
Email: store@mirifica.it



Japan

Tokushu Denshi Kairo Inc.
84-403 Kanda Sakuma Gashi
101-0026 Chiyoda
Japan

Phone: +81-3-3865-5381
Fax: +81-3-3865-5382
Web: www.tokudenkairo.co.jp
Email: [tonaki\(at\)tokudenkairo.co.jp](mailto:tonaki(at)tokudenkairo.co.jp)



Slovakia

DFC Design, s.r.o.
Strmá 11B
61600 Brno
Czech Republic

Phone: +420 511 112 863
Fax: +420 549 241 114
Web: www.dfcdesign.cz
Email: info@dfcdesign.cz



Spain

Cyberall Group
Aureli Capmany 2, Local 7
08195 Sant Cugat, Barcelona
Spain

Phone: +34 93 675 64 04
Fax: +34 93 975 68 44
Web: www.cyberallgroup.com
Email: sales@cyberallgroup.com



Taiwan

Future Linking Solution Tech Co. Ltd
Unit 01 7/F, Bright Way Tower
33 Mong Kok Rd, KL
Hong Kong

Phone: +86-755-82914675
Fax: +86-755-82971302
Web: www.fulso.com
Email: liu@fulso.com



UK

Sundance Multiprocessor Technology Ltd.
Chiltern House
Waterside
Chesham

Buckinghamshire HP5 1PS
United Kingdom
Phone: +44 1494 793167
Fax: +44 1494 793168
Web: www.sundance.com
Email: trenz@sundance.com



USA

Concurrent EDA LLC
5001 Baum Blvd Ste 640
Pittsburgh, PA 15213
United States of America

Phone: +1 412-687-8800
Web: www.concurrenteda.com
Email: trenz@concurrenteda.com



Worldwide

Digi-Key Electronics
701 Brooks Avenue South
Thief River Falls
MN 56701
United States of America

Phone: +1 218-681-6674
Fax: +1 800 587 0992
Web: <http://www.digikey.com>
Email: sales@digikey.com



Trenz Electronic GmbH

Holzweg 19A, 32257 Bünde, Germany

CEO: Thorsten Trenz

Local Court:

Amtsgericht Bad Oeynhausen | HRB7185

P +49 (0) 5223 65301-0

F +49 (0) 5223 65301-30

E info@trenz-electronic.de

<http://www.trenz-electronic.de>



facebook.com/trenzelectronicgmbh



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