



Welcome to E-XFL.COM

Understanding [Embedded - Microcontroller, Microprocessor, FPGA Modules](#)

Embedded - Microcontroller, Microprocessor, and FPGA Modules are fundamental components in modern electronic systems, offering a wide range of functionalities and capabilities. Microcontrollers are compact integrated circuits designed to execute specific control tasks within an embedded system. They typically include a processor, memory, and input/output peripherals on a single chip. Microprocessors, on the other hand, are more powerful processing units used in complex computing tasks, often requiring external memory and peripherals. FPGAs (Field Programmable Gate Arrays) are highly flexible devices that can be configured by the user to perform specific logic functions, making them invaluable in applications requiring customization and adaptability.

Applications of [Embedded - Microcontroller,](#)

Details

Product Status	Discontinued at Digi-Key
Module/Board Type	FPGA Core
Core Processor	Artix-7 A35T
Co-Processor	-
Speed	100MHz
Flash Size	32MB
RAM Size	-
Connector Type	50 Pin
Size / Dimension	2.87" x 1.38" (73mm x 35mm)
Operating Temperature	0°C ~ 70°C
Purchase URL	https://www.e-xfl.com/product-detail/trenz-electronic/te0725-02-35-2c



TE0725 TRM

Authors: Antti Lukats, Jan Kumann
Revision: V40
Date: 09-Dec-2016 11:37

Table of Contents

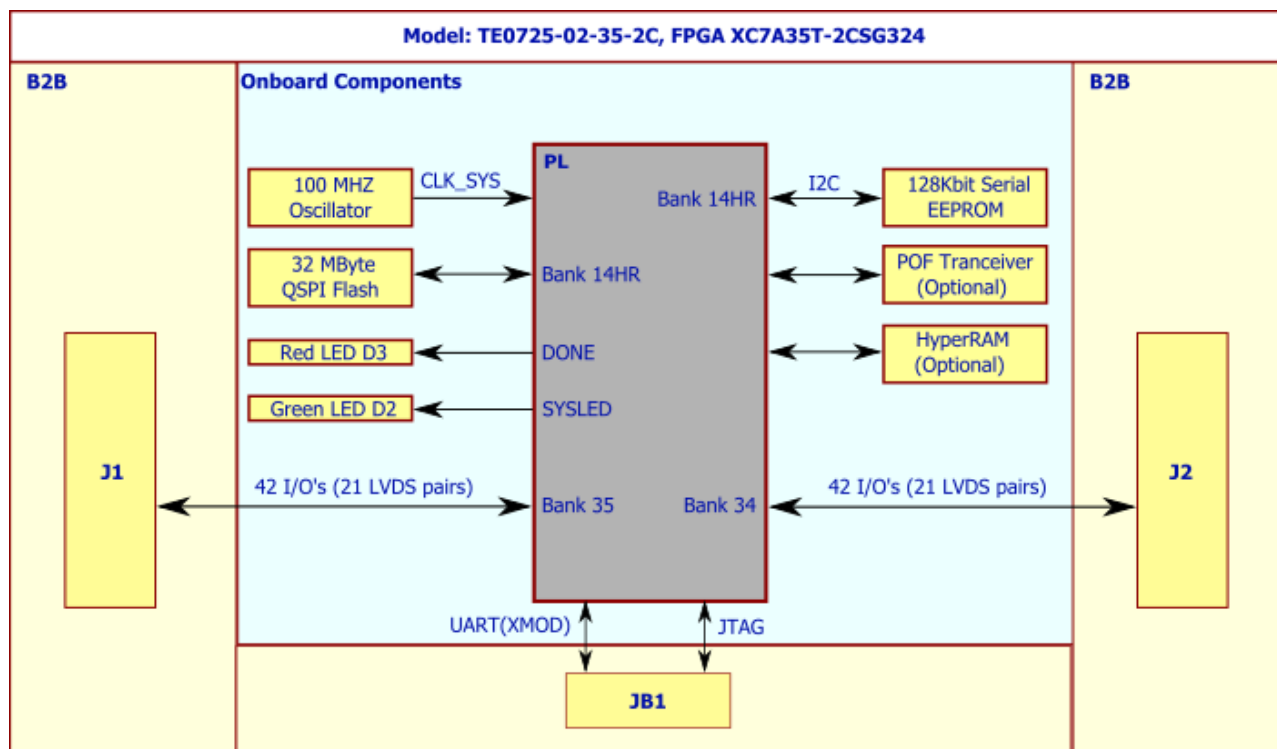
Overview	3
Block Diagram	3
Main Components	4
Key Features	5
Signals, Interfaces and Pins	6
I/O Banks	6
POF Transceiver	6
JTAG Interface	6
LED's	7
Connectors	7
Power and Power-On Sequence	8
Power Supply	8
Power Consumption	8
Technical Specifications	9
Absolute Maximum Ratings	9
Recommended Operating Conditions	9
Physical Dimensions	9
Operating Temperature Ranges	12
Weight	12
Revision History	13
Hardware Revision History	13
Document Change History	13
Disclaimer	14
Document Warranty	14
Limitation of Liability	14
Copyright Notice	14
Technology Licenses	14
Environmental protection	14
REACH	14
RoHS	15
WEEE	15

Overview

Refer to https://shop.trenz-electronic.de/de/Download/?path=Trenz_Electronic/TE0725 for online version of this manual and other documents available about the product.

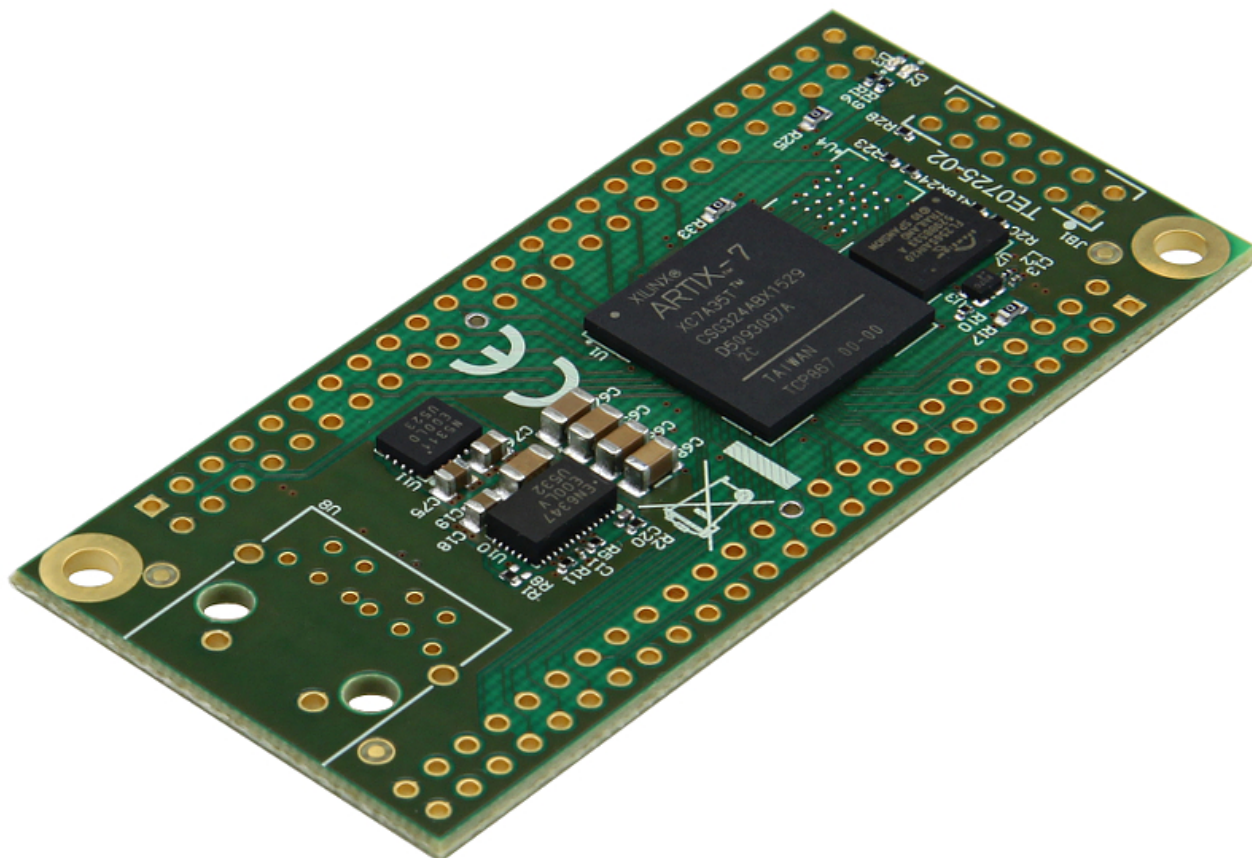
The Trenz Electronic TE0725 is a low cost small-sized FPGA module integrating a Xilinx Artix-7 (15-100T) and 32 MByte Flash memory for configuration and operation.

Block Diagram

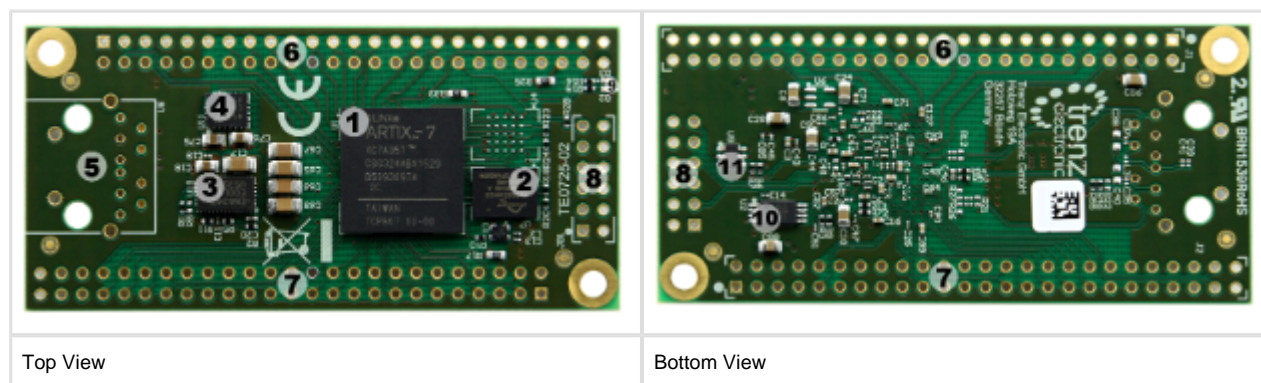


Main Components

Note that on the images below, there is no POF transceiver, HyperRAM, 50-pin headers and JTAG/UART header installed on the module.



The 2 x 50 pin headers with a 2,54mm standard pitch are perfect for breadboard or low cost dual PCB design.



1. Xilinx Artix-7 FPGA, U1
2. 32 MByte Flash Memory, U7
3. Enpirion EN6347 4A PowerSoC DC-DC Step Down Converter, U10
4. Enpirion EN5311 1A PowerSoC Synchronous Buck Regulator With Integrated Inductor, U11
5. POF Transceiver Placeholder, U8

6. 50-pin placeholder for breadboard connection, J1
7. 50-pin placeholder for breadboard connection, J2
8. JTAG/UART connector, JB1
9. Green LED D2(SYSLED) and Red LED D3(DONE)
10. 16K x 8 (128 Kbit) Serial EEPROM, U2
11. Ultralow Supply-Current Voltage Monitor With Optional Watchdog, U9

Key Features

- Xilinx Artix-7 XC7A35T (A15 to A100T)
- Commercial Temperature Grade (Industrial on Request)
- 32 MByte Flash Memory
- 2 x 50 Pin Headers with 2,54mm Pitch, Ideal for Breadboard Use
- 87 IO's (42 + 42 + 3)
- 100 MHz System Clock
- I2C EEPROM
- 3.3V Single Power Supply with On-Board Voltage Regulators
- Size 73 x 35 mm
- JTAG/UART Connector
- 2 LED's
- Optional HyperRAM (8 to 32 MByte)
- Optional POF Fiber Optical Adapter (125, 250 or 1000MBit/s)

Signals, Interfaces and Pins

I/O Banks

Bank	VCCIO	B2B I/O	Notes
0	3.3V	0	JTAG
14	3.3V	0 (3)	3 I/O in XMOD-JTAG - for use as UART
15	1.8V	0	used for optional hyper RAM
16	2.5V	0	used for optional optical fiber transceiver
34	User select	42	0R resistor option to select 3.3V
35	User select	42	0R resistor option to select 3.3V

POF Transceiver



Model	Bitrate MB/s	Notes
AFBR-59F2Z	250	

JTAG Interface

JTAG access to the Xilinx Artix-7 device is provided through connector JB1.

Signal	Pin Number
TCK	JB1: 4
TDO	JB1: 8
TDI	JB1: 10
TMS	JB1: 12

Connector JB1 (2 x 6 pin Header) is directly compatible to XMOD JTAG Adapter TE0790. This adapter can be inserted from top onto the TE0725, if JB1 is fitted with male pin header. Optionally JB1 can be fitted with pin header from bottom, in that case the JTAG cable connector must be on the base board.

When using XMOD-JTAG in JB1 then additionally USB UART is usable, and the push-button on XMOD works as configuration reset.

When using XMOD-JTAG please check the switch settings on XMOD to be sure the power and I/O reference are supplied correctly. TE0790 can in some case be used to power up TE0725, this is however not recommended. TE0790-01 has not enough power for TE0725 (LED Blink may work, for larger designs the XMOD delivered power may be not sufficient).

LED's

LED	Color	FPGA	Notes
D2	green	M16	
D3	red	DONE	Active Low

Connectors

All connectors are are for 100mil headers, all connector locations are in 100 mil grid.

LED	Color	FPGA	
D2	green	M16	
D3	red	DONE	Active Low

Power and Power-On Sequence

For startup, a power supply with minimum current capability of 1A is recommended.

There is no specific or special power-on sequence, single power source is needed as VIN, rest of the sequence is automatic.

Power Supply

Single 3.3V Power supply required.

Power Consumption

FPGA	Design	Typical Power, 25C ambient
A35T	not configured	TBD
A35T	LED Blinky	170mW (typical)
A100T	not configured	TBD

TBD - To Be Determined.

Technical Specifications

Absolute Maximum Ratings

Parameter	Min	Max	Units	Notes	Reference document
Vin supply voltage	-0.1	3.6	V		
I/O Bank supply voltage	-0.5	3.6	V		Xilinx document DS181
I/O input voltage for FPGA I/O banks	-0.4	VCCO_X+0.55	V		Xilinx document DS181
GT Transceiver	-0.5	1.26	V		Xilinx document DS181
Voltage on Module JTAG pins	-0.4	VCCO_0+0.55	V	VCCO_0 is 1.8V or 3.3V nominal	Xilinx document DS181
Storage Temperature	-40	+85	C		

Recommended Operating Conditions

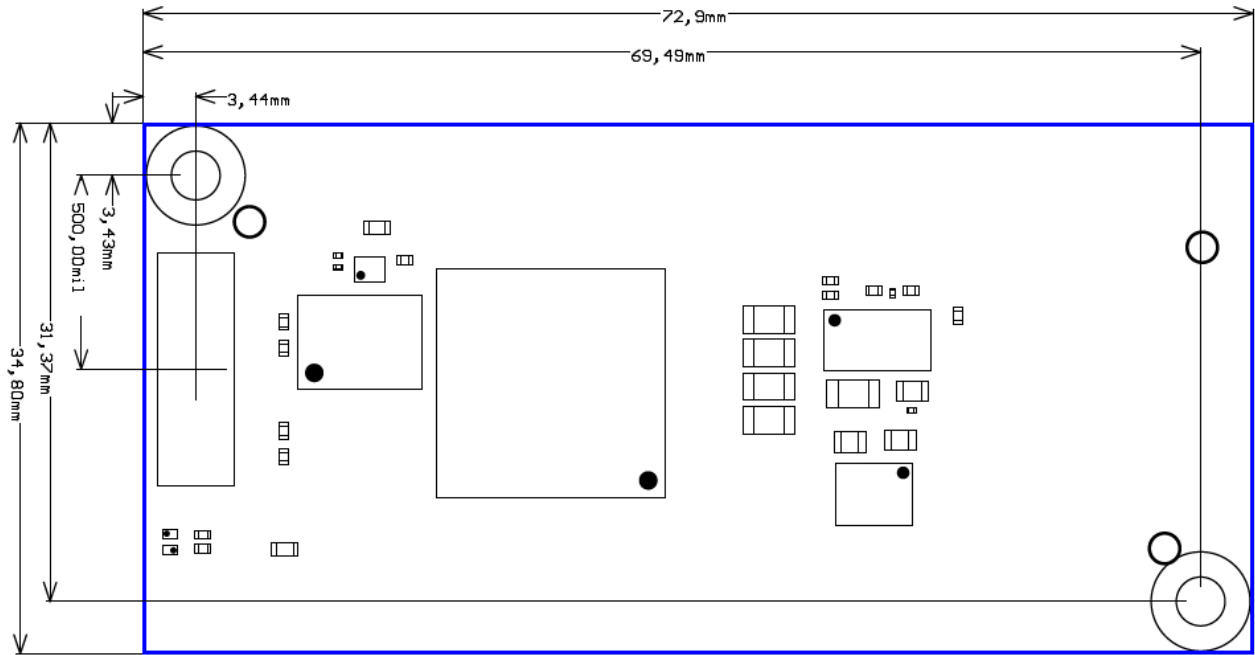
Parameter	Min	Max	Units	Notes	Reference document
Vin supply voltage	3.135	3.45	V		
IO Bank supply voltage for I/O banks	1.14	3.465	V		Xilinx document DS181
I/O input voltage for I/O banks	-0.20	VCCO + 0.20	V		Xilinx document DS181
Voltage on Module JTAG pins	3.135	3.465	V	For assembly variant with 3.3V CONFIG Bank Option	Xilinx document DS181



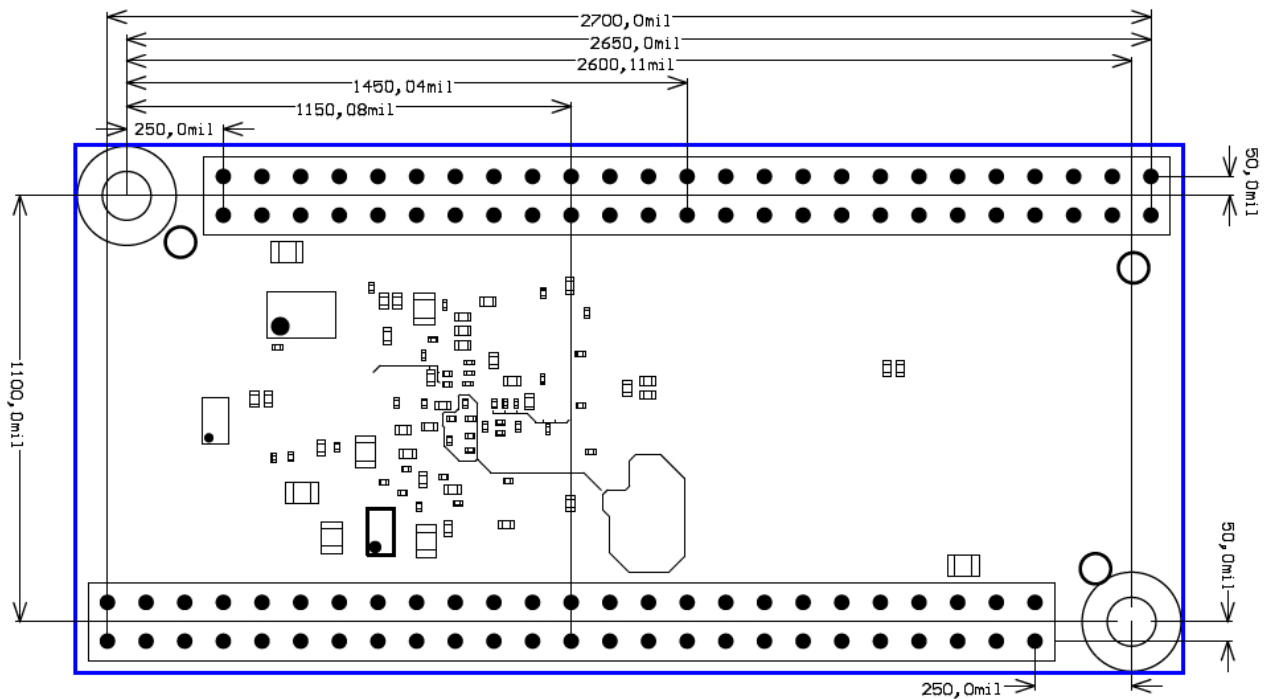
Please check Xilinx datasheet for complete list of absolute maximum and recommended operating ratings for the Artix-7 device (DS181).

Physical Dimensions

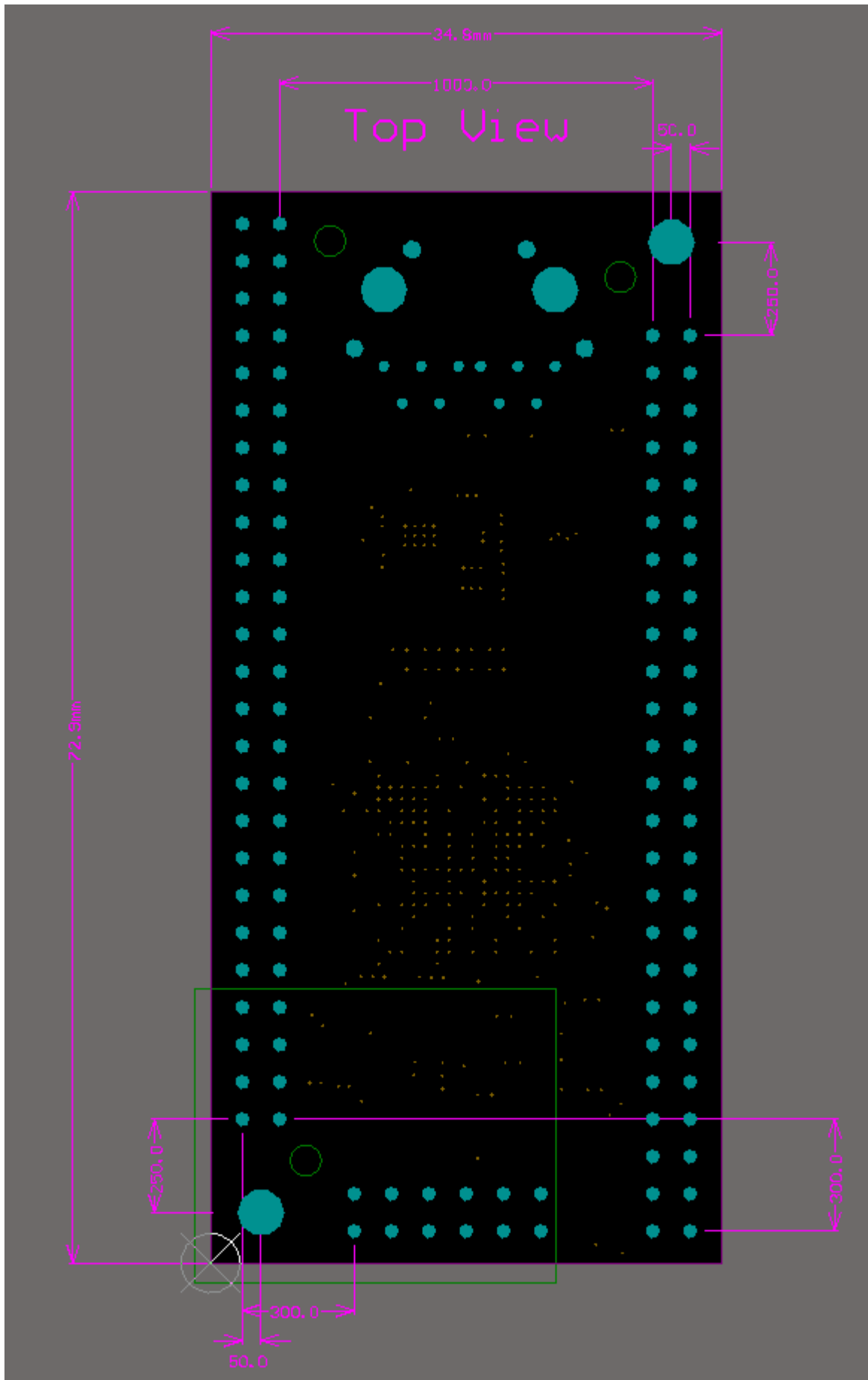
Please note that two different units are used on the figures below, SI system millimeters(mm) and imperial system thousandth of an inch(mil). This is because of the 100mil pin headers used, see also explanation below. To convert mil's to millimeters and vice versa use formula 100mil's = 2,54mm.



Top View



Bottom View



Measurements and placement of the connectors and mounting holes. All 100 mil pin headers are in 100 mil grid, the M3 mounting holes are in 50 mil grid aligned to the centers of the 100mil headers. The module is symmetrical, turning it 180 degrees will keep all I/O and Power pins in both 50 pin headers in compatible places.

Operating Temperature Ranges

Commercial grade modules

All parts are at least commercial temperature range of 0°C to +70°C. The module operating temperature range depends on customer design and cooling solution. Please contact us for options.

Industrial grade modules

All parts are at least industrial temperature range of -40°C to +85°C. The module operating temperature range depends on customer design and cooling solution. Please contact us for options.

Weight

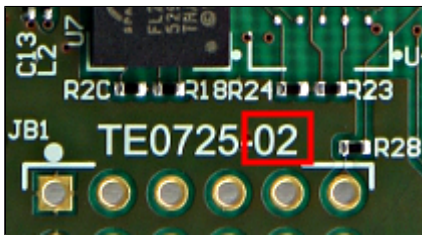
Variant	Weight g	Note
	8.5	Plain Module

Revision History

Hardware Revision History

Date	Revision	Notes	PCN	Documentation link
-	01	Prototypes		
-	02	First production release		TE0725 TRM

Hardware revision number is printed on the PCB board together with the module model number separated by the dash.



Document Change History

Date	Revision	Contributors	Description
2016-12-09	V40	Antti Lukats, Jan Kumann	Hardware Rev02 Block Diagram added
2016-12-02	V1	Antti Lukats, Jan Kumann	Initial version

Disclaimer

Document Warranty

The material contained in this document is provided “as is” and is subject to being changed at any time without notice. Trenz Electronic does not warrant the accuracy and completeness of the materials in this document. Further, to the maximum extent permitted by applicable law, Trenz Electronic disclaims all warranties, either express or implied, with regard to this document and any information contained herein, including but not limited to the implied warranties of merchantability, fitness for a particular purpose or non infringement of intellectual property. Trenz Electronic shall not be liable for errors or for incidental or consequential damages in connection with the furnishing, use, or performance of this document or of any information contained herein.

Limitation of Liability

In no event will Trenz Electronic, its suppliers, or other third parties mentioned in this document be liable for any damages whatsoever (including, without limitation, those resulting from lost profits, lost data or business interruption) arising out of the use, inability to use, or the results of use of this document, any documents linked to this document, or the materials or information contained at any or all such documents. If your use of the materials or information from this document results in the need for servicing, repair or correction of equipment or data, you assume all costs thereof.

Copyright Notice

No part of this manual may be reproduced in any form or by any means (including electronic storage and retrieval or translation into a foreign language) without prior agreement and written consent from Trenz Electronic.

Technology Licenses

The hardware / firmware / software described in this document are furnished under a license and may be used /modified / copied only in accordance with the terms of such license.

Environmental protection

To confront directly with the responsibility toward the environment, the global community and eventually also oneself. Such a resolution should be integral part not only of everybody's life. Also enterprises shall be conscious of their social responsibility and contribute to the preservation of our common living space. That is why Trenz Electronic invests in the protection of our Environment.

REACH

Trenz Electronic is a manufacturer and a distributor of electronic products. It is therefore a so called downstream user in the sense of [REACH](#). The products we supply to you are solely non-chemical products

(goods). Moreover and under normal and reasonably foreseeable circumstances of application, the goods supplied to you shall not release any substance. For that, Trenz Electronic is obliged to neither register nor to provide safety data sheet. According to present knowledge and to best of our knowledge, no [SVHC \(Substances of Very High Concern\) on the Candidate List](#) are contained in our products. Furthermore, we will immediately and unsolicited inform our customers in compliance with REACH - Article 33 if any substance present in our goods (above a concentration of 0,1 % weight by weight) will be classified as SVHC by the [European Chemicals Agency \(ECHA\)](#).

RoHS

Trenz Electronic GmbH herewith declares that all its products are developed, manufactured and distributed RoHS compliant.

WEEE

Information for users within the European Union in accordance with Directive 2002/96/EC of the European Parliament and of the Council of 27 January 2003 on waste electrical and electronic equipment (WEEE).

Users of electrical and electronic equipment in private households are required not to dispose of waste electrical and electronic equipment as unsorted municipal waste and to collect such waste electrical and electronic equipment separately. By the 13 August 2005, Member States shall have ensured that systems are set up allowing final holders and distributors to return waste electrical and electronic equipment at least free of charge. Member States shall ensure the availability and accessibility of the necessary collection facilities. Separate collection is the precondition to ensure specific treatment and recycling of waste electrical and electronic equipment and is necessary to achieve the chosen level of protection of human health and the environment in the European Union. Consumers have to actively contribute to the success of such collection and the return of waste electrical and electronic equipment. Presence of hazardous substances in electrical and electronic equipment results in potential effects on the environment and human health. The symbol consisting of the crossed-out wheeled bin indicates separate collection for waste electrical and electronic equipment.

Trenz Electronic is registered under WEEE-Reg.-Nr. DE97922676.