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What is "Embedded - Microcontrollers"?

"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "<u>Embedded - Microcontrollers</u>"

| Details | |
|----------------------------|--------------------------------------------------------------------------|
| Product Status | Active |
| Core Processor | PIC |
| Core Size | 8-Bit |
| Speed | 20MHz |
| Connectivity | - |
| Peripherals | Brown-out Detect/Reset, POR, WDT |
| Number of I/O | 24 |
| Program Memory Size | 3KB (2K x 12) |
| Program Memory Type | FLASH |
| EEPROM Size | 64 x 8 |
| RAM Size | 132 x 8 |
| Voltage - Supply (Vcc/Vdd) | 2V ~ 5.5V |
| Data Converters | A/D 8x8b |
| Oscillator Type | Internal |
| Operating Temperature | -40°C ~ 125°C (TA) |
| Mounting Type | Surface Mount |
| Package / Case | 28-SOIC (0.295", 7.50mm Width) |
| Supplier Device Package | 28-SOIC |
| Purchase URL | https://www.e-xfl.com/product-detail/microchip-technology/pic16f570-e-so |



PIC16F570

28-Pin, 8-Bit 'Enhanced Baseline' Microcontroller Product Brief

Description:

This document describes the 28-pin 'Enhanced Baseline' device with Flash program memory self-write capability, interrupts and op amps.

Processor Features:

- · Interrupt Capability
- · PIC16F570 Operating Speed:
 - DC 20 MHz Crystal oscillator
 - DC 200 ns Instruction cycle
- High Endurance Program and Flash Data Memory Cells:
 - 2048 x 12 user execution memory
 - 64 x 8 self-writable data memory
 - 100,000 write program memory endurance
 - 1,000.000 write Flash data memory endurance
 - Program and Flash data retention: >40 years
- · General Purpose Registers (SRAM):
 - 132 x 8 memory
- · Only 36 Single-Word Instructions to Learn:
 - Modified baseline CPU
 - Added RETURN and RETFIE instructions
 - Added MOVLB instruction
- All Instructions are Single-Cycle except for Program Branches which are Two-Cycle
- Four-Level Deep Hardware Stack
- Direct, Indirect and Relative Addressing modes for Data and Instructions

Peripheral Features:

- · Device Features:
 - 24 I/Os
 - Individual direction control
 - High-current source/sink
- 8-Bit Real-Time Clock/Counter (TMR0) with 8-Bit Programmable Prescaler
- In-Circuit Serial Programming™ (ICSP™) via Two External Pin Connections
- · Analog Comparator (CMP):
 - Two analog comparators
 - Absolute and programmable references
- Analog-to-Digital Converter (ADC):
 - 8-bit resolution
 - 8 external input channels
 - 0.6V reference input
- · Operational Amplifiers (op amps):
 - Two operational amplifiers
 - Fully-accessible visibility

Microcontroller Features:

- · Brown-out Reset (BOR)
- Power-on Reset (POR)
- Device Reset Timer (DRT)
- Watchdog Timer (WDT) with its own on-chip RC Oscillator for Reliable Operation
- Programmable Code Protection (CP)
- Power-Saving Sleep mode with Wake-up on Change Feature
- · Selectable Oscillator Options:
- INTOSC: Precision 4 or 8 MHz internal oscillator
- EXTRC: Low-cost external RC oscillator
- LP: Power-saving, low-frequency crystal
- XT: Standard crystal/resonator
- HS: High-speed crystal/resonator
- EC: High-speed external clock
- · Variety of Packaging Options:
 - 28-Lead SPDIP, SOIC, SSOP, QFN

CMOS Technology:

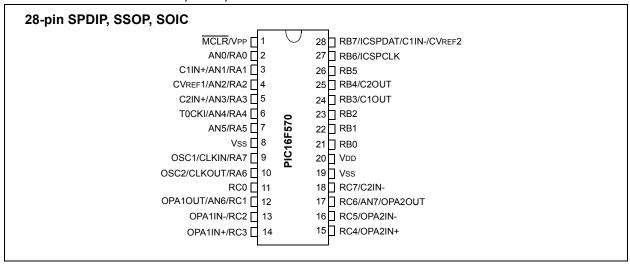
- · Low-Power, High-Speed CMOS Flash Technology
- · Fully-Static Design
- Wide Operating Voltage and Temperature Range:
 - Industrial: 2.0V to 5.5V
 - Extended: 2.0V to 5.5V
- · Operating Current:
 - 170 uA @ 2V, 4 MHz, typical
 - 15 uA @ 2V, 32 kHz, typical
- · Standby Current:
 - 100 nA @ 2V, typical

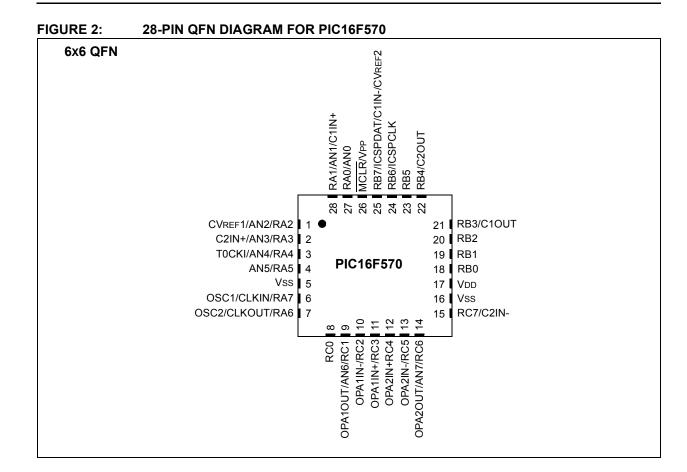
PIC16F570

PIC16F570 Family Types

| Device | Pins/I/O Pins | Flash | Data EE (B) | SRAM (B) | 8-Bit ADC Channels | Ор Атр | Comparator | 8-Bit Timers | BOR | Stack Levels | Interrupts | 8 MHz Int. Osc. | Interrupt-on-change Pins | Weak Pull-up Pins |
|-----------|---------------|-------|-------------|----------|-----------------------|--------|------------|--------------|-----|--------------|------------|-----------------|-----------------------------|-------------------|
| PIC16F570 | 28/24 | 2KW | 64 | 132 | 8 | 2 | 2 | 1 | Υ | 4 | Υ | Υ | 8 | 8 |

FIGURE 1: 28-PIN PDIP, SOIC, SSOP DIAGRAM FOR PIC16F570





PIC16F570

TABLE 1: PIC16F570 PIN SUMMARY

| 0/1 | 28-Pin SPDIP | 28-Pin QFN | A/D | Reference | Comparator | Ор Атр | Timers | 001 | Pull-up | Basic | |
|------|--------------|------------|-----|-----------|------------|---------|--------|-------|---------|----------------|--|
| MCLR | 1 | 26 | _ | _ | _ | _ | _ | N | Υ | MCLR VPP | |
| RA0 | 2 | 27 | AN0 | _ | | _ | _ | _ N N | | _ | |
| RA1 | 3 | 28 | AN1 | _ | C1IN+ | _ | _ | N | N | _ | |
| RA2 | 4 | 1 | AN2 | CVREF1 | _ | _ | _ | N | N | 1 — | |
| RA3 | 5 | 2 | AN3 | _ | C2IN+ | _ | _ | N | Ν | _ | |
| RA4 | 6 | 3 | AN4 | _ | _ | _ | T0CKI | N | N | _ | |
| RA5 | 7 | 4 | AN5 | _ | _ | _ | _ | N | N | _ | |
| RA6 | 10 | 7 | _ | _ | | _ | _ | N | N | OSC2 CLKOUT | |
| RA7 | 9 | 6 | _ | _ | _ | _ | _ | N | N | OSC1 CLKIN | |
| RB0 | 21 | 18 | _ | _ | | _ | _ | Υ | Υ | _ | |
| RB1 | 22 | 19 | _ | | _ | _ | _ | Υ | Υ | _ | |
| RB2 | 23 | 20 | _ | _ | _ | _ | _ | Υ | Υ | _ | |
| RB3 | 24 | 21 | _ | _ | C1OUT | _ | _ | Υ | Υ | _ | |
| RB4 | 25 | 22 | _ | _ | C2OUT | _ | _ | Υ | Υ | _ | |
| RB5 | 26 | 23 | _ | _ | _ | _ | _ | Υ | Υ | _ | |
| RB6 | 27 | 24 | _ | _ | _ | _ | _ | Υ | Υ | ICSPCLK | |
| RB7 | 28 | 25 | _ | CVREF2 | C1IN- | _ | _ | Υ | Υ | ICSPDAT | |
| RC0 | 11 | 8 | _ | _ | _ | _ | _ | N | Ν | _ | |
| RC1 | 12 | 9 | AN6 | _ | _ | OPA1OUT | _ | N | N | _ | |
| RC2 | 13 | 10 | _ | _ | _ | OPA1IN- | _ | N | N | _ | |
| RC3 | 14 | 11 | _ | _ | _ | OPA1IN+ | _ | N | Ν | _ | |
| RC4 | 15 | 12 | _ | _ | _ | OPA2IN+ | _ | N | N | _ | |
| RC5 | 16 | 13 | | | | OPA2IN- | | N | N | | |
| RC6 | 17 | 14 | AN7 | _ | _ | OPA2OUT | _ | N | N | _ | |
| RC7 | 18 | 15 | | | C2IN- | | | N | N | | |
| VDD | 20 | 17 | _ | _ | - | | | _ | _ | VDD | |
| Vss | 8, 19 | 5, 16 | _ | _ | _ | _ | _ | _ | _ | Vss | |

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