# E·XFL



#### Welcome to E-XFL.COM

### What is "Embedded - Microcontrollers"?

"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

### Applications of "<u>Embedded -</u> <u>Microcontrollers</u>"

### Details

Product Status	Not For New Designs
Core Processor	ARM® Cortex®-M4
Core Size	32-Bit Single-Core
Speed	100MHz
Connectivity	CANbus, EBI/EMI, I <sup>2</sup> C, IrDA, SD, SPI, UART/USART
Peripherals	DMA, I <sup>2</sup> S, LVD, POR, PWM, WDT
Number of I/O	56
Program Memory Size	512KB (512K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	128K x 8
Voltage - Supply (Vcc/Vdd)	1.71V ~ 3.6V
Data Converters	A/D 31x16b; D/A 1x12b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	Surface Mount
Package / Case	80-LQFP
Supplier Device Package	80-FQFP (12x12)
Purchase URL	https://www.e-xfl.com/product-detail/nxp-semiconductors/mk10dn512zvlk10r

Address: Room A, 16/F, Full Win Commercial Centre, 573 Nathan Road, Mongkok, Hong Kong





### **Target Applications**

- Remote sensors
- HVAC systems
- · Gaming controllers
- Flow meters



32-bit Microcontrollers

# **Kinetis K10 Family**

## Low-power, mixed-signal MCUs

### Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and software-compatible MCU families based on the ARM<sup>®</sup> Cortex<sup>™</sup>-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low power and mixed signal analog integration.

The K10 MCU family is the entry point into the Kinetis portfolio. Devices start from 32 KB of flash in a small-footprint 5x5 mm 32 QFN package extending up to 1 MB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals. Additionally, pin compatibility, flexible low-power capabilities and innovative FlexMemory help to solve many of the major pain points for system implementation.

## Kinetis K10 Family





### **One-Stop Enablement** Offering-MCU + IDE + RTOS

Freescale Tower System hardware development environment:

- Integrated development environments
  - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
  - IAR Embedded Workbench
  - Keil MDK
  - CodeSourcery Sourcery G++ (GNU)
- Runtime software and RTOS
  - Math, DSP and encryption libraries
  - Motor control libraries
  - Complimentary bootloaders (USB, Ethernet, RF, serial)
  - Complimentary Freescale embedded GUI
  - Complimentary Freescale MQX<sup>™</sup>
  - Cost-effective Nano<sup>™</sup> SSL/Nano<sup>™</sup> SSH for Freescale MQX RTOS
  - Micrium µC/OS-III
  - Express Logic ThreadX
  - SEGGER embOS
  - freeRTOS
  - Mocana (security)

Kto Family Ontions

• Full ARM ecosystem

#### **Benefits Features** ARM<sup>®</sup> Cortex<sup>™</sup>-M4 core with DSP Up to 120 MHz core supporting a broad range of processing instruction support and optional bandwidth needs single precision floating point unit Peripheral and memory servicing with reduced CPU loading. Up to 32-channel DMA. Up to 16 Optimized bus bandwidth and flash execution performance. Concurrent multi-master bus accesses for increased bus bandwidth KB of cache. Cross bar switch High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution • 32 KB-1 MB flash. Up to 128 KB and firmware updating of SRAM FlexMemory provides 32 byte-16 KB of user-segmentable byte write/ 32-512 KB FlexMemory erase EEPROM. In addition. FlexNVM from 32-512 KB for extra program code, data or EEPROM backup Peripheral activity and wake-up times can be optimized to suit 10 ultra-low-power modes with application requirements enabling extended battery life flash programming and analog (Stop currents of <500 nA, run currents of <200 µA/MHz, 4 µs wakeoperation down to 1.71V up from Stop) Low-power timer, low-power RTC, Continual device operation in reduced power states with flexible low-leakage wake-up unit wake-up options Fast, accurate signal conditioning capability with support for single or differential operation for improved noise rejection High-speed 16-bit ADCs. Support for small amplitude signal processing Programmable gain amplifiers Analog signal generation for audio applications 12-bit DAC. High-speed Fast, accurate motor over-current protection comparators Eliminates need for external voltage reference reducing overall On-chip voltage reference system cost Cryptographic acceleration Secure data transfer and storage. Faster than software unit (CAU) implementations and with minimal CPU loading. Supports a wide HW tamper detection unit variety of algorithms: DES, 3DES, AES, MD5, SHA-1, SHA-256 Random number generator Secure key storage with internal/external tamper detect for unsecured flash, temperature/clock/supply voltage variations and physical attack · Provides a modern upgrade from mechanical to touch keypad, rotary · Low-power capacitive touchand slider user interfaces and operates in all low-power modes with sensing interface minimal current added. Supports up to 16 inputs Up to six UARTs with IrDA support. Variety of data size, format and transmission/reception settings One UART with ISO 7816 support supported for multiple industrial communication protocols I2S interface, up to two CAN

Multiple communication interfaces for simple and efficient data exchange, industrial network bridging and audio system interfacing interfaces, up to two I2C interfaces

		м	lemor	у		Features										√ Packages											
Part Number	_		â			ion Unit	ction		CAN Secure Digital Host NAND Flash	_	External Bus Interface	12-bit DAC	Prog. Gain Amplifier	5V Tolerant I/O		FM	FT	LF	MP	LH	LK	LL	мс	LQ	MD		
	CPU (MHz)	Flash (KB)	Flex NVM (K	SRAM (KB)	Cache (KB	Single Precis Floating Point	Memory Protec	CAN		NAND Flas Controller					Other	32 QFN (5x5)	48QFN (7X7)	48LQFP (7X7)	64MAPBGA (5X5)	64LQFP (10X10)	80LQFP (12X12)	100LQFP (14X14)	121BGA (8x8)	144LQFP (20x20)	144BGA (13x13)		
MK10DN32Vyy5	50	32		8												1	1	1	$\checkmark$	$\checkmark$							
MK10DN64Vyy5	50	64		16												1	1	1	1	$\checkmark$							
MK10DN128Vyy5	50	128		16												1	1	1	$\checkmark$	$\checkmark$							
MK10DN512Vyy10	100	512		128			_ √	$\checkmark$	_ √		1	$\checkmark$	$\checkmark$	1							√	$\checkmark$	_√	$\checkmark$	1		
MK10FN1M0Vyy12	120	1 MB		128	16	$\checkmark$	_ √	$\checkmark$	1	√	1	$\checkmark$	$\checkmark$	1										1	1		
MK10DX32Vyy5	50	32v	32	8												√	√	√ √	$\checkmark$	$\checkmark$							
MK10DX64Vyy5	50	64	32	16												√	1	1	$\checkmark$	$\checkmark$							
MK10DX128Vyy5	50	128	32	16												$\checkmark$	√	1	$\checkmark$	$\checkmark$							
MK10DX64Vyy7	72	64	32	16				$\checkmark$			1	$\checkmark$	$\checkmark$	1						$\checkmark$	$\checkmark$		_√				
MK10DX128Vyy7	72	128	32	32				$\checkmark$			$\checkmark$	$\checkmark$	$\checkmark$	l √						$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$				
MK10DX256Vyy7	72	256	32	64				$\checkmark$			$\checkmark$	$\checkmark$	1	l √						√	√	$\checkmark$	√				
MK10DX128Vyy10	100	128	128	32			√	$\checkmark$	√		$\checkmark$	$\checkmark$	1	√										$\checkmark$	$\checkmark$		
MK10DX256Vyy10	100	256	256	64			√	√	√		1	$\checkmark$	1	√										√	$\checkmark$		
MK10FX512Vyy12	120	512	512	128	16	1	1	1	1	1	1	1	1	1										1	1		
MK11DX128Vyy5(R)	50	128	64	32								V			Encryption and Tamper Detect						$\checkmark$		$\checkmark$				
MK11DX256Vyy5(R)	50	256	64	32								$\checkmark$			Encryption and Tamper Detect						$\checkmark$		$\checkmark$				
MK11DN512Vyy5(R)	50	512		64								$\checkmark$			Encryption and Tamper Detect						$\checkmark$		$\checkmark$				
MK12DX128Vyy5(R)	50	128	64	32								$\checkmark$						$\checkmark$		$\checkmark$	$\checkmark$		$\checkmark$				
MK12DX256Vyy5(R)	50	256	64	32								1						1		$\checkmark$	1		1				
MK12DN512Vyy5	50	512		64								V								V	V		V				

modules, up to three DSPI

yy = package designator



### For current information about Kinetis products and documentation, please visit freescale.com/Kinetis

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