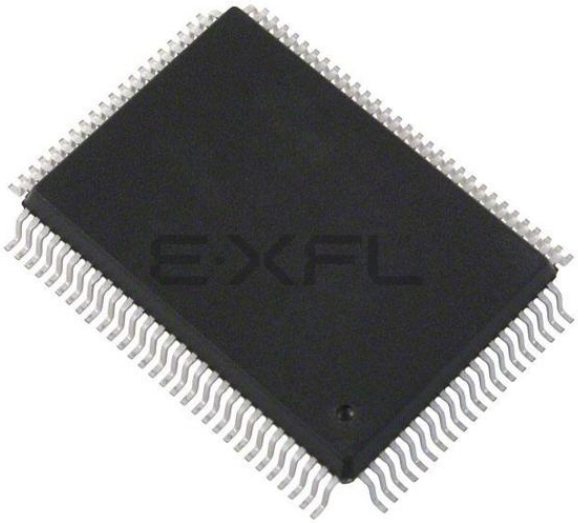

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What is "Embedded - Microcontrollers"?

"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "Embedded - Microcontrollers"

Details	
Product Status	Obsolete
Core Processor	-
Core Size	8/16-Bit
Speed	50MHz
Connectivity	EBI/EMI, UART/USART
Peripherals	DMA, POR, WDT
Number of I/O	32
Program Memory Size	-
Program Memory Type	ROMless
EEPROM Size	-
RAM Size	32K x 8
Voltage - Supply (Vcc/Vdd)	3V ~ 3.6V
Data Converters	-
Oscillator Type	Internal
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	100-BQFP
Supplier Device Package	100-PQFP (20x14)
Purchase URL	https://www.e-xfl.com/product-detail/analog-devices/ia18xerpqf100ir2

Table 83. Alphabetic Key to Waveform Parameters

No.	Name	Description
49	tARYCH	ardy Resolution Transition Setup Time
51	tARYCHL	ardy Inactive Holding Time
52	tARYLCL	ardy Setup Time
87	tAVBL	a Address Valid to whb_n/wlb_n Low
14	tAVCH	ad Address Valid to Clock High
12	tAVLL	ad Address Valid to ale Low
66	tAVRL	a Address Valid to rd_n Low
65	tAVWL	a Address Valid to wr_n Low
24	tAZRL	ad Address Float to rd_n Active
45	tCH1CH2	clkouta Rise Time
68	tCHAV	clkouta High to a Address Valid
38	tCHCK	x1 High Time
44	tCHCL	clkouta High Time
67	tCHCSV	clkouta High to lcs_n/usc_n Valid
18	tCHCSX	mcs_n/pcs_n Inactive Delay
22	tCHCTV	Control Active Delay 2
64	tCHCV	Command Lines Valid Delay (after Float)
63	tCHCZ	Command Lines Float Delay
8	tCHDX	Status Hold Time
9	tCHLH	ale Active Delay
11	tCHLL	ale Inactive Delay
79	tCHRFD	clkouta High to rfsh_n Valid
3	tCHSV	Status Active Delay
69	tCICOA	x1 to clkouta Skew
70	tCICOB	x1 to clkoutb Skew
39	tCKHL	x1 Fall Time
36	tCKIN	x1 Period
40	tCKLH	x1 Rise time
46	tCL2CL1	clkouta Fall Time
50	tCLARX	ardy Active Hold Time
5	tCLAV	ad Address Valid Delay
6	tCLAX	Address Hold
15	tCLAZ	ad Address Float Delay
43	tCLCH	clkouta Low Time
37	tCLCK	x1 Low Time
42	tCLCL	clkouta Period
80	tCLCLX	lcs_n Inactive Delay
81	tCLCSL	lcs_n Active Delay
16	tCLCSV	mcs_n/pcs_n Inactive Delay
30	tCLDOX	Data Hold Time