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What is "Embedded - Microcontrollers"?

"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "Embedded - Microcontrollers"

Details	
Product Status	Active
Core Processor	eZ8
Core Size	8-Bit
Speed	20MHz
Connectivity	-
Peripherals	Brown-out Detect/Reset, LED, POR, PWM, WDT
Number of I/O	17
Program Memory Size	12KB (12K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	256 x 8
Voltage - Supply (Vcc/Vdd)	2.7V ~ 3.6V
Data Converters	A/D 7x10b
Oscillator Type	Internal
Operating Temperature	0°C ~ 70°C (TA)
Mounting Type	Surface Mount
Package / Case	20-SSOP (0.209", 5.30mm Width)
Supplier Device Package	-
Purchase URL	https://www.e-xfl.com/product-detail/zilog/z8f1232hh020sg

Electrical Characteristics 184
Absolute Maximum Ratings 184
DC Characteristics 185
AC Characteristics 189
On-Chip Peripheral AC and DC Electrical Characteristics 190
General Purpose I/O Port Input Data Sample Timing 195
General Purpose I/O Port Output Timing 196
On-Chip Debugger Timing 197

Packaging 199

Ordering Information 200
Part Number Suffix Designations 205

Appendix A. Register Tables 208
General Purpose RAM 208
Timer 0 208
Analog-to-Digital Converter 213
Low Power Control 216
LED Controller 216
Oscillator Control 217
Comparator 0 218
Interrupt Controller 218
GPIO Port A 222
Watchdog Timer 226
Trim Bit Control 228
Flash Memory Controller 228

Index 231

Customer Support 239

Table 119. Flash Memory Electrical Characteristics and Timing 192
Table 120. Watchdog Timer Electrical Characteristics and Timing 192
Table 121. Nonvolatile Data Storage 193
Table 122. Analog-to-Digital Converter Electrical Characteristics and Timing 193
Table 123. Comparator Electrical Characteristics 194
Table 124. GPIO Port Input Timing 195
Table 125. GPIO Port Output Timing 196
Table 126. On-Chip Debugger Timing 197
Table 127. Power Consumption Reference Table 198
Table 128. Z8 Encore! XP F0830 Series Ordering Matrix 200
Table 129. Package and Pin Count Description 207
Table 130. Timer 0 High Byte Register (T0H) 208
Table 131. Timer 0 Low Byte Register (T0L) 209
Table 132. Timer 0 Reload High Byte Register (T0RH) 209
Table 133. Timer 0 Reload Low Byte Register (T0RL) 209
Table 134. Timer 0 PWM High Byte Register (T0PWMH) 209
Table 135. Timer 0 PWM Low Byte Register (T0PWML) 210
Table 136. Timer 0 Control Register 0 (T0CTL0) 210
Table 137. Timer 0 Control Register 1 (T0CTL1) 210
Table 138. Timer 1 High Byte Register (T1H) 210
Table 139. Timer 1 Low Byte Register (T1L) 211
Table 140. Timer 1 Reload High Byte Register (T1RH) 211
Table 141. Timer 1 Reload Low Byte Register (T1RL) 211
Table 142. Timer 1 PWM High Byte Register (T1PWMH) 211
Table 143. Timer 1 PWM Low Byte Register (T1PWML) 212
Table 144. Timer 1 Control Register 0 (T1CTL0) 212
Table 145. Timer 1 Control Register 1 (T1CTL1) 212
Table 146. ADC Control Register 0 (ADCCTL0) 213
Table 147. ADC Data High Byte Register (ADCD_H) 214
Table 148. ADC Data Low Bits Register (ADCD_L) 214

Table 149. ADC Sample Settling Time (ADCSST) 215
Table 150. ADC Sample Time (ADCST) 215
Table 151. Power Control Register 0 (PWRCTL0) 216
Table 152. LED Drive Enable (LEDEN) 216
Table 153. LED Drive Level High Register (LEDLVLH) 217
Table 154. LED Drive Level Low Register (LEDLVLL) 217
Table 155. Oscillator Control Register (OSCCTL) 217
Table 156. Comparator Control Register (CMP0) 218
Table 157. Interrupt Request 0 Register (IRQ0) 218
Table 158. IRQ0 Enable High Bit Register (IRQ0ENH) 219
Table 159. IRQ0 Enable Low Bit Register (IRQ0ENL) 219
Table 160. Interrupt Request 1 Register (IRQ1) 219
Table 161. IRQ1 Enable High Bit Register (IRQ1ENH) 219
Table 162. IRQ1 Enable Low Bit Register (IRQ1ENL) 220
Table 163. Interrupt Request 2 Register (IRQ2) 220
Table 164. IRQ2 Enable High Bit Register (IRQ2ENH) 220
Table 165. IRQ2 Enable Low Bit Register (IRQ2ENL) 220
Table 166. Interrupt Edge Select Register (IRQES) 221
Table 167. Shared Interrupt Select Register (IRQSS) 221
Table 168. Interrupt Control Register (IRQCTL) 221
Table 169. Port A GPIO Address Register (PAADDR) 222
Table 170. Port A Control Registers (PACTL) 222
Table 171. Port A Input Data Registers (PAIN) 222
Table 172. Port A Output Data Register (PAOUT) 223
Table 173. Port B GPIO Address Register (PBADDR) 223
Table 174. Port B Control Registers (PBCTL) 223
Table 175. Port B Input Data Registers (PBIN) 223
Table 176. Port B Output Data Register (PBOUT) 224
Table 177. Port C GPIO Address Register (PCADDR) 224
Table 178. Port C Control Registers (PCCTL) 224

Bit	Description (Continued)
[4] POFEN	**Primary Oscillator Failure Detection Enable** 1 = Failure detection and recovery of primary oscillator is enabled. 0 = Failure detection and recovery of primary oscillator is disabled.
[3] WDFEN	**Watchdog Timer Oscillator Failure Detection Enable** 1 = Failure detection of Watchdog Timer Oscillator is enabled. 0 = Failure detection of Watchdog Timer Oscillator is disabled.
[2:0] SCKSEL	**System Clock Oscillator Select** 000 = Internal Precision Oscillator functions as system clock at 5.53 MHz. 001 = Internal Precision Oscillator functions as system clock at 32 kHz. 010 = Crystal oscillator or external RC oscillator functions as system clock. 011 = Watchdog Timer Oscillator functions as system clock. 100 = External clock signal on PB3 functions as system clock. 101 = Reserved. 110 = Reserved. 111 = Reserved.

Hex Address: FD7

Table 176. Port B Output Data Register (PBOUT)

Bit	7	6	5	4	3	2	1	0
Field	POUT7	POUT6	POUT5	POUT4	POUT3	POUT2	POUT1	POUT0
RESET	0	0	0	0	0	0	0	0
R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Address	FD7H							

Hex Address: FD8

Table 177. Port C GPIO Address Register (PCADDR)

Bit	7	6	5	4	3	2	1	0
Field	PADDR[7:0]							
RESET	00H							
R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Address	FD8H							

Hex Address: FD9

Table 178. Port C Control Registers (PCCTL)

Bit	7	6	5	4	3	2	1	0
Field	PCTL							
RESET	00H							
R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Address	FD9H							

Hex Address: FDA

Table 179. Port C Input Data Registers (PCIN)

Bit	7	6	5	4	3	2	1	0
Field	PIN7	PIN6	PIN5	PIN4	PIN3	PIN2	PIN1	PIN0
RESET	X	X	X	X	X	X	X	X
R/W	R	R	R	R	R	R	R	R
Address	FDAH							

Hex Address: FF1

Table 186. Watchdog Timer Reload Upper Byte Register (WDTU)

Bit	7	6	5	4	3	2	1	0
Field	WDTU							
RESET	0	0	0	0	0	0	0	0
R/W	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*
Address	FF1H							
Note: *Read returns the current WDT count value; write sets the appropriate reload value.								

Hex Address: FF2

Table 187. Watchdog Timer Reload High Byte Register (WDTH)

Bit	7	6	5	4	3	2	1	0
Field	WDTH							
RESET	0	0	0	0	0	1	0	0
R/W	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*
Address	FF2H							
Note: *Read returns the current WDT count value; write sets the appropriate reload value.								

Hex Address: FF3

Table 188. Watchdog Timer Reload Low Byte Register (WDTL)

Bit	7	6	5	4	3	2	1	0
Field	WDTL							
RESET	0	0	0	0	0	0	0	0
R/W	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*	R/W*
Address	FF3H							
Note: *Read returns the current WDT count value; write sets the appropriate reload value.								

Hex Addresses: FF4–FF5

This address range is reserved.

Index

Symbols

@ 165
# 165
% 165

Numerics

10-bit ADC 4

A

absolute maximum ratings 184
AC characteristics 189
ADC 166
    block diagram 99
    overview 98
ADC Channel Register 1 (ADCCTL) 102
ADC Data High Byte Register (ADCDH) 103
ADC Data Low Bit Register (ADCDL) 103, 104, 105
ADCX 166
ADD 166
add - extended addressing 166
add with carry 166
add with carry - extended addressing 166
additional symbols 165
address space 14
ADDX 166
analog block/PWM signal synchronization 100
analog block/PWM signal zynchronization 100
analog signals 11
analog-to-digital converter
    overview 98
AND 169
ANDX 169
architecture
    voltage measurements 98
arithmetic instructions 166
assembly language programming 162
assembly language syntax 163

B

B 165
b 164
BCLR 167
binary number suffix 165
BIT 167
bit 164
    clear 167
    manipulation instructions 167
    set 167
    set or clear 167
    swap 167
    test and jump 169
    test and jump if non-zero 169
    test and jump if zero 169
bit jump and test if non-zero 166
bit swap 169
block diagram 3
block transfer instructions 167
BRK 169
BSET 167
BSWAP 167, 169
BTJ 169
BTJNZ 166, 169
BTJZ 169

C

calibration and compensation, motor control measurements 101
CALL procedure 169
capture mode 89, 90
capture/compare mode 89
cc 164
CCF 168
characteristics, electrical 184
clear 168
CLR 168
COM 169
compare 89