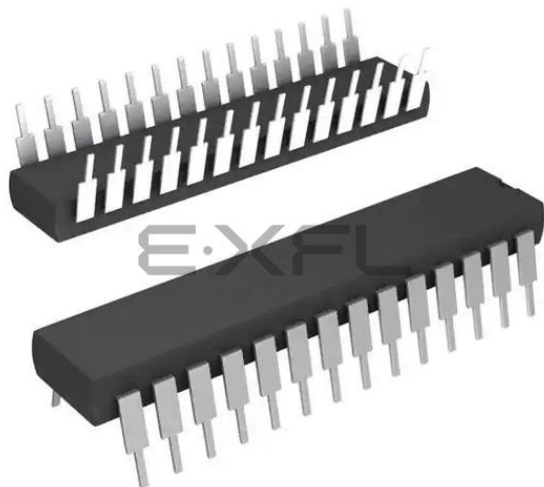




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Details

Product Status	Obsolete
Core Processor	Z8
Core Size	8-Bit
Speed	8MHz
Connectivity	-
Peripherals	LVD, POR, WDT
Number of I/O	23
Program Memory Size	32KB (32K x 8)
Program Memory Type	OTP
EEPROM Size	-
RAM Size	237 x 8
Voltage - Supply (Vcc/Vdd)	2.3V ~ 5.5V
Data Converters	-
Oscillator Type	Internal
Operating Temperature	0°C ~ 70°C (TA)
Mounting Type	Through Hole
Package / Case	28-DIP (0.600", 15.24mm)
Supplier Device Package	-
Purchase URL	https://www.e-xfl.com/product-detail/zilog/z86d8608psc





Table of Contents

Revision History	iii
Features	1
General Description	2
Pin Description	5
Absolute Maximum Ratings	6
Standard Test Conditions	7
Capacitance	7
DC Characteristics	8
AC Characteristics	10
Pin Functions (Standard Mode)	12
XTAL1 Crystal 1 (Time-Based Input)	12
XTAL2 Crystal 2 (Time-Based Output)	12
Port 0 (P07–P00)	12
Port 2 (P27–P20)	13
Port 3 (P37–P31)	14
Comparator Inputs	16
Comparator Outputs	16
Functional Description	18
Program Memory	18
RAM	18
Expanded Register File	18
Register File	32
Stack	33
Counter/Timer Registers	34
Counter/Timer Functional Blocks	43
Interrupts	54
Clock	56
Power-On Reset (POR)	57
HALT	58
STOP	58
Port Configuration Register (PCON)	59
Stop-Mode Recovery Register (SMR)	59
Stop-Mode Recovery Register 2 (SMR2)	64
Watch-Dog Timer Mode Register (WDTMR)	65
Mask Selectable Options	68

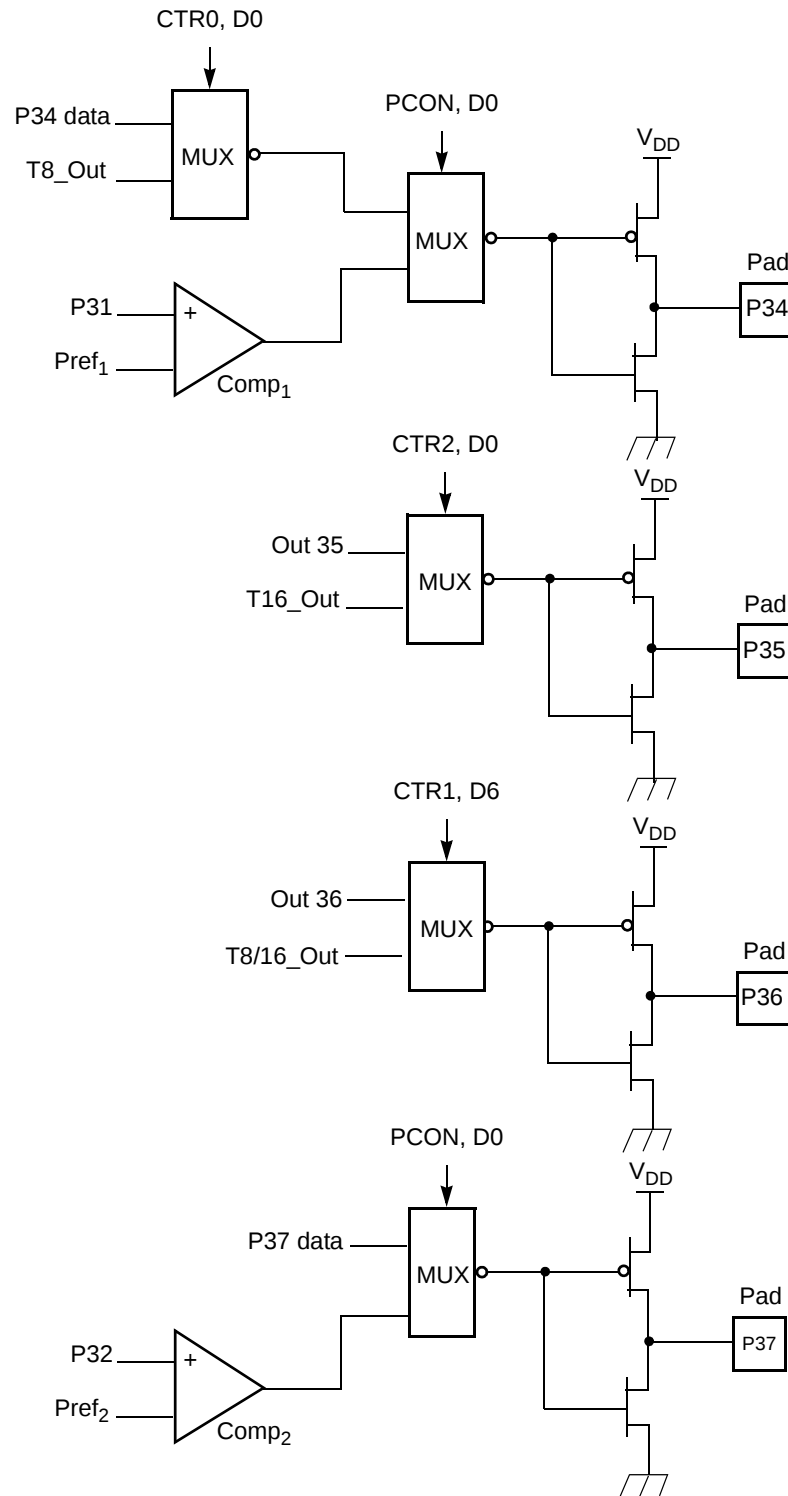


Figure 9. Port 3 Counter/Timer Output Configuration



Table 9. LBD(D)0C Low Battery Detection Register

Field	Bit Position	Description		
LBD	765432--	Reserved No effect		
	-----1-	R	1 0*	LB flag set LB flag reset
	-----0	R/W	1 0*	Enable LBD Disable LBD

Note:

*Default after POR

HI8(D)0Bh

This register (Table 10) holds the captured data from the output of the 8-bit Counter/Timer0. This register is typically used to hold the number of counts when the input signal is 1.

Table 10. HI8(D)0Bh

Field	Bit Position	Description		
T8_Capture_HI	76543210	R W	Captured Data No Effect	

L08(D)0Ah

This register (Table 11) holds the captured data from the output of the 8-bit Counter/Timer0. This register is typically used to hold the number of counts when the input signal is 0.

Table 11. L08(D)0Ah

Field	Bit Position	Description		
T8_Capture_L0	76543210	R W	Captured Data No Effect	

HI16(D)09h

This register (Table 12) holds the captured data from the output of the 16-bit Counter/Timer16. This register holds the MS-Byte of the data.

