



Welcome to **E-XFL.COM**

Understanding <u>Embedded - CPLDs (Complex Programmable Logic Devices)</u>

Embedded - CPLDs, or Complex Programmable Logic Devices, are highly versatile digital logic devices used in electronic systems. These programmable components are designed to perform complex logical operations and can be customized for specific applications. Unlike fixed-function ICs, CPLDs offer the flexibility to reprogram their configuration, making them an ideal choice for various embedded systems. They consist of a set of logic gates and programmable interconnects, allowing designers to implement complex logic circuits without needing custom hardware.

Applications of Embedded - CPLDs

Details	
Product Status	Obsolete
Programmable Type	EE PLD
Delay Time tpd(1) Max	15 ns
Voltage Supply - Internal	4.5V ~ 5.5V
Number of Logic Elements/Blocks	-
Number of Macrocells	12
Number of Gates	-
Number of I/O	-
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	28-LCC (J-Lead)
Supplier Device Package	28-PLCC (11.51x11.51)
Purchase URL	https://www.e-xfl.com/product-detail/lattice-semiconductor/gal26v12c-15lji

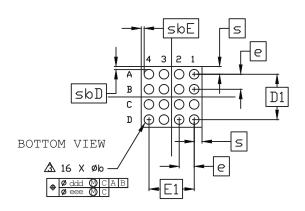
Email: info@E-XFL.COM

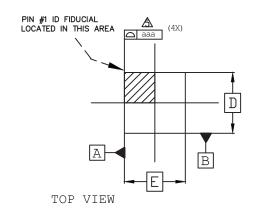
Address: Room A, 16/F, Full Win Commercial Centre, 573 Nathan Road, Mongkok, Hong Kong

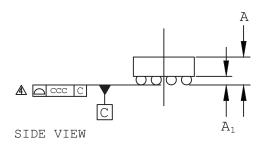


16-Ball WLCS Package Option 2: iCE40 UltraLite™

Dimensions in Millimeters







NOTES:

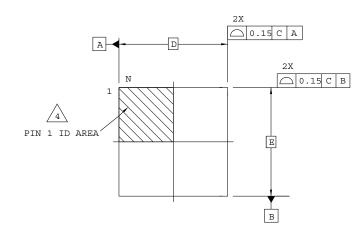
- 1. ALL DIMENSIONS AND TOLERANCE PER ASME Y 14.5M 1994.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.
- △ DIMENSION "b" IS MEASURES AT THE MAXIMUM BUMP DIAMETER PARALLEL TO PRIMARY DATUM C.
- A PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BUMPS.
- $\underline{\mathbb{A}}$ BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

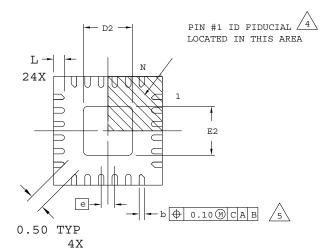
REF.	Min.	Nom.	Max.
Α	0.413	0.452	0.491
A1	0.122	0.152	0.182
b	0.188	0.218	0.248
D	1.	409 BS	С
Ε	1.	409 BS	С
D1		1.05 BSC)
E1		1.05 BSC)
е	(0.35 BS0)
S	-	0.180	-
sbD	0.067	0.071	0.072
sbE	0.067	0.071	0.072
۵۵۵	0.03		
CCC	0.03		
ddd	0.050		
eee		0.015	



24-Pin QFNS Package

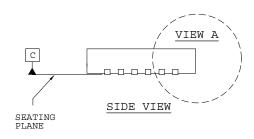
Dimensions in Millimeters

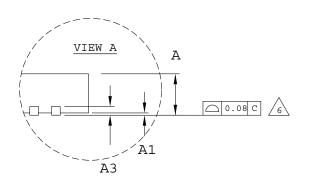




TOP VIEW

BOTTOM VIEW





NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.
- 3. DRAWING CONFORMS TO JEDEC MO-220, VARIATION VGGD-9.

 $\sqrt{4}$

EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.

5

DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM TERMINAL TIP.

6

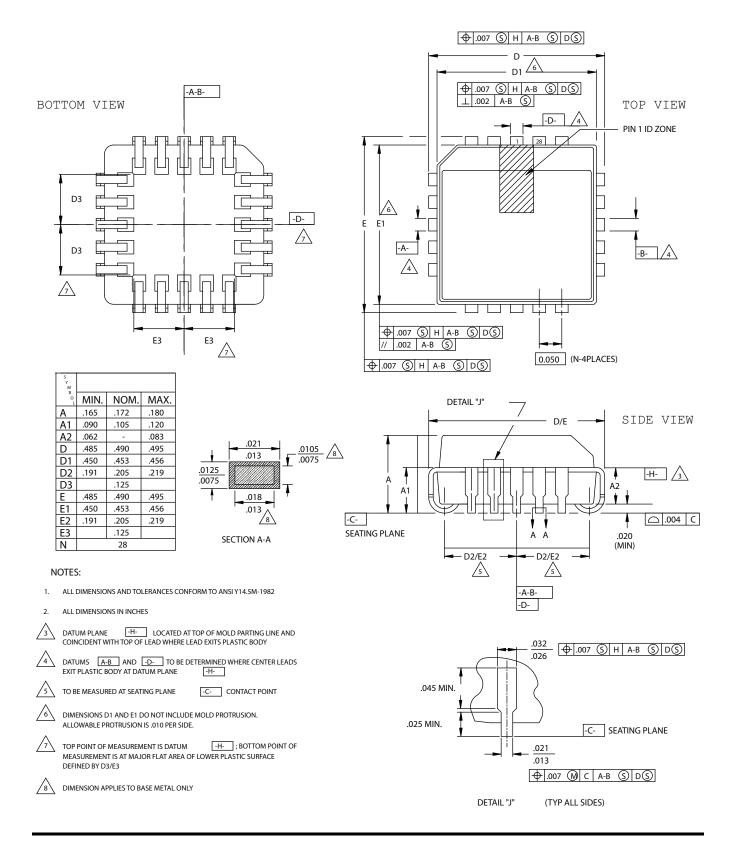
APPLIES TO EXPOSED PORTION OF TERMINALS.

SYMBOL	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	0.02	0.05
A3		0.2 REF	
D	4.0 BSC		
D2	1.05	-	2.45
E	4.0 BSC		
E2	1.05	-	2.45
b	0.18	0.25	0.30
е	0.50 BSC		
L	0.45	0.50	0.55



28-Pin PLCC Package

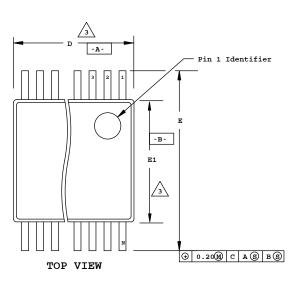
Dimensions in Inches

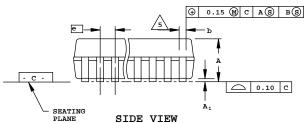


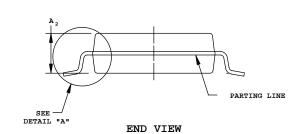


28-Pin SSOP Package

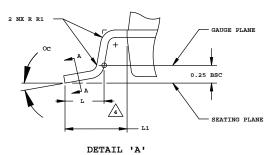
Dimensions in Millimeters

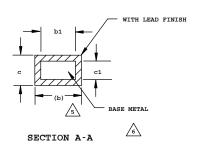






S Y	COMMON			
M B	DIMENSIONS			
O L	MIN.	NOM.	MAX.	
Α			2.0	
A	0.05			
A ₂	1.65	1.75	1.85	
b	0.22	-	0.38	
b ₁	0.22	0.30	0.33	
С	0.09		0.25	
Cı	0.09	0.15	0.21	
D	9.90	10.20	10.50	
E1	5.00	5.30	5.60	
е		0.65 BSC		
Е	7.40	7.80	8.20	
L	0.55	0.75	0.95	
L1	1.25 REF.			
N		28		
oc	0	4	8	
R1	0.09			





NOTES

- 1. CONTROLLING DIMENSION: MILLIMETERS.
- 2. DIMENSIONING & TOLERANCES PER ANSI.Y14.5M-1982.

"D" & "E1" DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, BUT DO INCLUDE MOLD MISMATCH AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.20mm PER SIDE.

4. TO BE DETERMINED AT THE SEATING PLANE

DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION/INTRUSION.
ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13mm TOTAL IN
EXCESS OF b DIMENSION AT MAXIMUM MATERIAL CONDITION.
DAMBAR INTRUSION SHALL NOT REDUCE DIMENSION b BY MORE
THAN 0.07mm AT LEAST MATERIAL CONDITION.

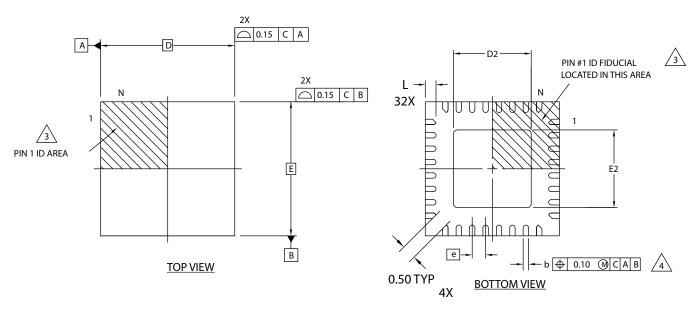
THESE DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.10 & 0.25mm FROM THE LEAD TIP

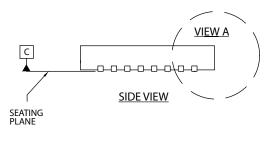
7. "N" IS THE NUMBER OF TERMINAL POSITIONS

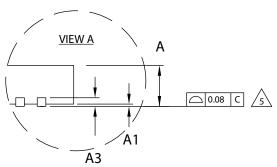


32-Pin QFN Package Option 1: Power Manager II, iCE40™

Dimensions in Millimeters







NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.

EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.

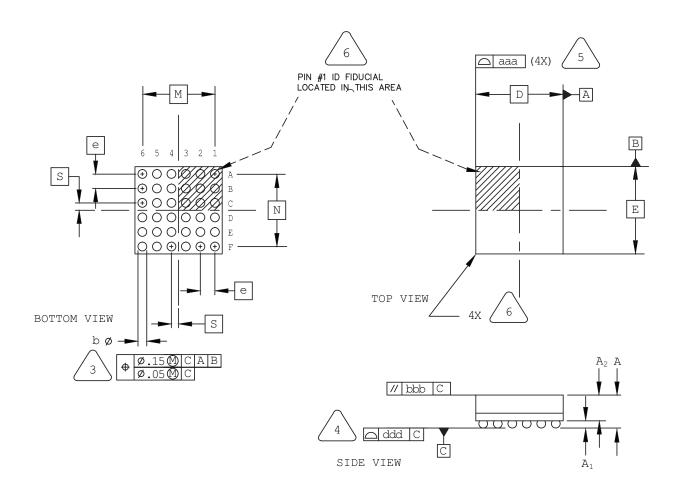
DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM TERMINAL TIP.

SYMBOL	MIN.	NOM.	MAX.
А	0.80	0.90	1.00
A1	0.00	0.02	0.05
A3		0.2 REF	
D	5.0 BSC		
D2	1.25	2.70	3.75
E	5.0 BSC		
E2	1.25	2.70	3.75
b	0.18	0.24	0.30
е	0.50 BSC		
L	0.30	0.40	0.50



36-Ball ucfBGA Package: iCE40 Ultra™

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.

2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C



PRIMARY DATUM C AND SEATING
PLANE ARE DEFINED BY THE SPHERICAL
CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.



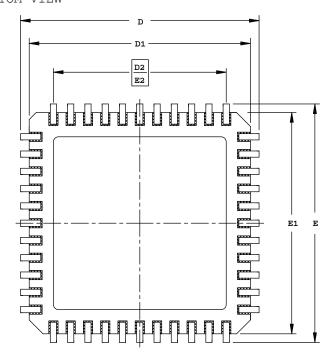
SYMBOL	MIN.	NOM.	MAX.
А	_	0.81	0.91
A1	0.12	_	1
A2	_	_	0.70
D/E	2.50 BSC		
M/N	2.00 BSC		
S	0.20 BSC		
b	0.20	0.25	0.30
е	0.40 BSC		
aaa	_	_	0.10
bbb	_	_	0.10
ddd	_	_	0.10

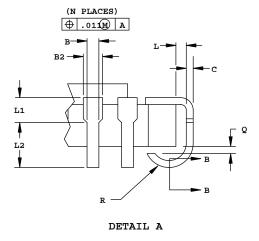


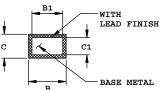
44-Pin JLCC Package

Dimensions in Inches

BOTTOM VIEW







DETAIL A SIDE VIEW SEATING .050 PLANE .020 MIN. D4 E4

SECTION B-B

NOTES:

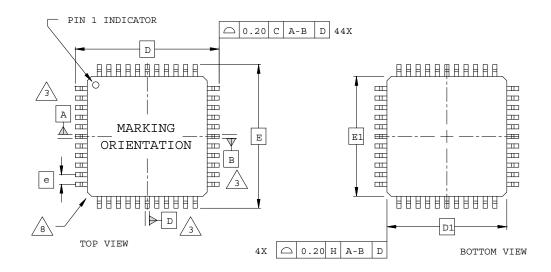
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M.
- ALL DIMENSIONS ARE IN INCHES.
- CORNER CHAMFERS AND/OR NOTCHES ARE OPTIONAL.

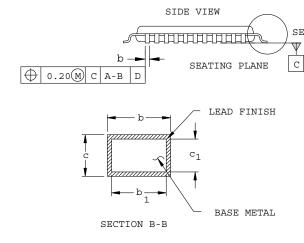
INCHES		
MIN.		MAX.
.115	ı	.190
. 0	65 RE	F
.013	-	.023
.013	ı	.020
.022	ı	.035
.007	1	.013
.007	ı	.010
.675	.690	.700
.620	ı	.660
.5	00 BS	C
. 6	30 BS	2
.005	ı	•
.020	-	-
.025	ı	ı
.003	ı	-
.020	-	.040
44		
	MIN115 .0 .013 .013 .022 .007 .007 .675 .620 .5 .605 .020 .025 .003	MIN115

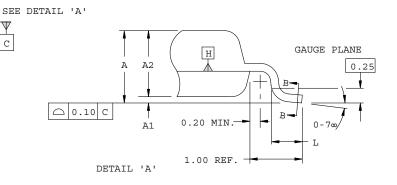


44-Pin TQFP Package (1.4 mm thick)

Dimensions in Millimeters







NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5 1982.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.

 $\stackrel{\textstyle >}{\scriptstyle 3}$ datums a, b and d to be determined at datum plane H.

- 4. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.254 MM ON D1 AND E1 DIMENSIONS.
- 5. THE TOP OF PACKAGE MAY BE SMALLER THAN THE BOTTOM OF THE PACKAGE BY 0.15 MM.
- 6. SECTION B-B:
 THESE DIMENSIONS APPLY TO THE FLAT SECTION OF THE
 LEAD BETWEEN 0.10 AND 0.25 MM FROM THE LEAD TIP.
- 7. A1 IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

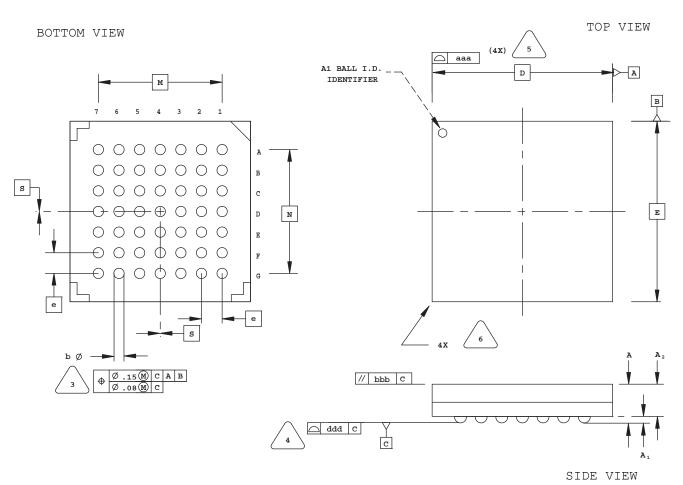
8 EXACT SHAPE OF EACH CORNER IS OPTIONAL.

SYMBOL	MIN.	NOM.	MAX.
A	=	=	1.60
A1	0.05	-	0.15
A2	1.35	1.40	1.45
D		12.00 BSC	
D1		10.00 BSC	
E		12.00 BSC	
E1		10.00 BSC	
L	0.45	0.60	0.75
N	44		
е	0.80 BSC		
b	0.30	0.37	0.45
b1	0.30	0.35	0.40
С	0.09	0.15	0.20
c1	0.09	0.13	0.16



49-Ball caBGA Package

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C



PRIMARY DATUM C AND SEATING
PLANE ARE DEFINED BY THE SPHERICAL
CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

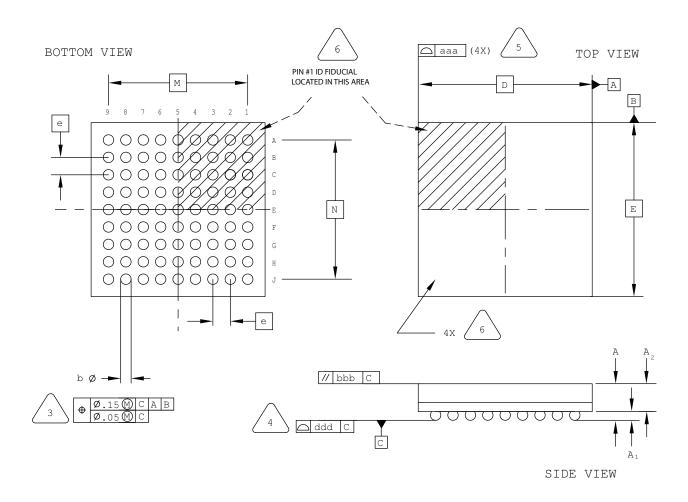


SYMBOL	MIN.	NOM.	MAX.	
A	1.30	1.40	1.50	
A1	0.31	0.36	0.41	
A2	0.99	1.04	1.09	
D/E	7	7.00 BSC		
M/N	4.80 BSC			
s		0 BSC		
b	0.40 0.46 0		0.52	
е	0	0.80 BSC		
aaa	0.10			
bbb	-	-	0.10	
ddd	-	-	0.12	



81-Ball csBGA Package

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C



PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

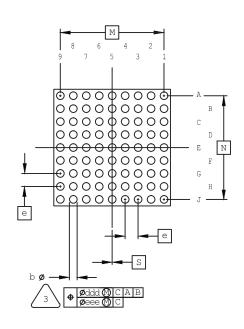


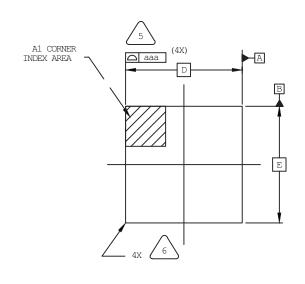
SYMBOL	MIN.	NOM.	MAX.
А	-	-	1.00
A1	0.10	_	-
A2	-	-	0.90
D/E	5.00 BSC		
M/N	4	.00 BSC	
b	0.20	0.25	0.30
е	O	0.50 BSC	
aaa	-	_	0.10
bbb	-	-	0.10
ddd	-	-	0.10

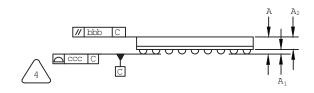


81-Ball csfBGA Package

Dimensions in Millimeters







NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C.



PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

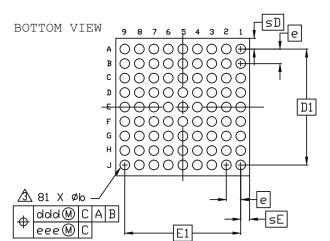


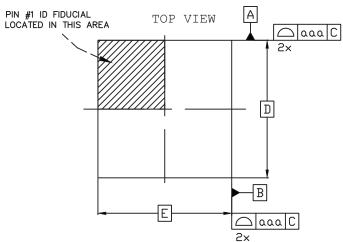
SYMBOL	MIN.	NOM.	MAX.
А	-	-	1.00
A1	0.11	-	-
A2	0.64	-	-
D/E		4.50 BSC	
M/N		4.00 BSC	
S	0.00 BSC		
b	0.20 0.25 0.30		
е	0.50 BSC		
aaa	0.10		
bbb	0.10		
ccc	0.08		
ddd	0.15		
eee		0.08	

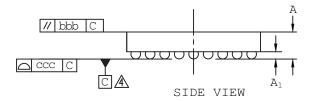


81-Ball WLCS Package

Dimensions in Millimeters







Notes:

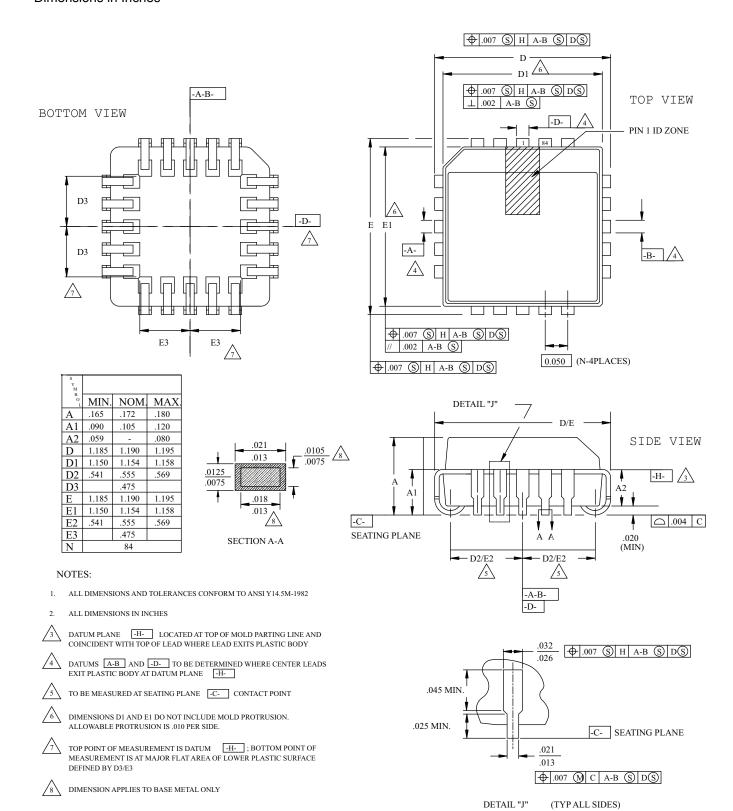
- 1 ALL DIMENSIONS AND TOLERANCE PER ASME Y 14.5M 1994.
- 2 ALL DIMENSIONS ARE IN MILLIMETERS.
- \triangle DIMENSION "b" IS MEASURED AT THE MAXIMUM BUMP DIAMETER PARALLEL TO PRIMARY DATUM $\boxed{\mathbb{C}}$.
- A PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BUMPS.

REF.	Min.	Nom.	Max.
A	0.510	0.543	0.567
A1	0.167	0.196	0.225
b	0.239	0.266	0.319
D	3	.797 BS	С
E	3	.693 BS	С
D1	3	.20 BS0	2
E1	3	.20 BS0	2
е	0	.40 BS0	2
sD	_	0.299	_
sE	_	0.247	-
aaa	0.025		
bbb	0.060		
ccc	0.030		
ddd	0.015		
eee		0.050	



84-Pin PLCC Package

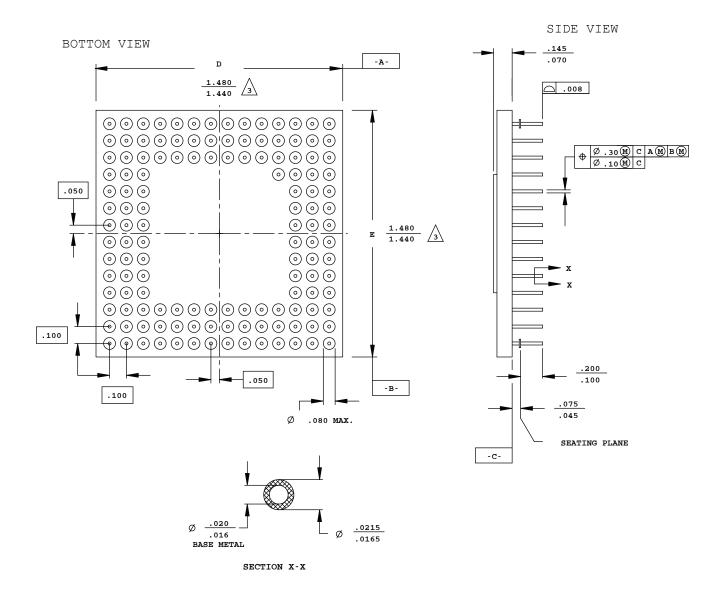
Dimensions in Inches





133-Pin CPGA Package

Dimensions in Inches



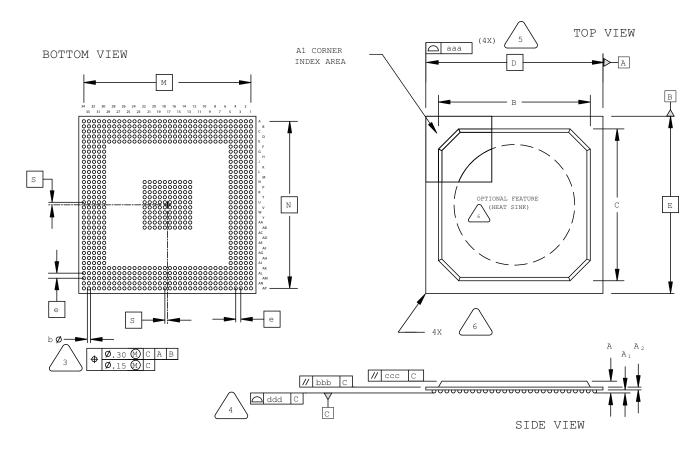
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN INCHES.
- DIMENSIONS D AND E MAY HAVE MATERIAL PROTRUSION OF
 .006 INCHES MAXIMUM ABOVE THE DIMENSION SHOWN
 NOT TO EXCEED .003 INCHES MAXIMUM PER SIDE.



680-Ball fpBGA Package

(with or without Internal Heat Spreader)
Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- 2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM \fbox{C}



PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

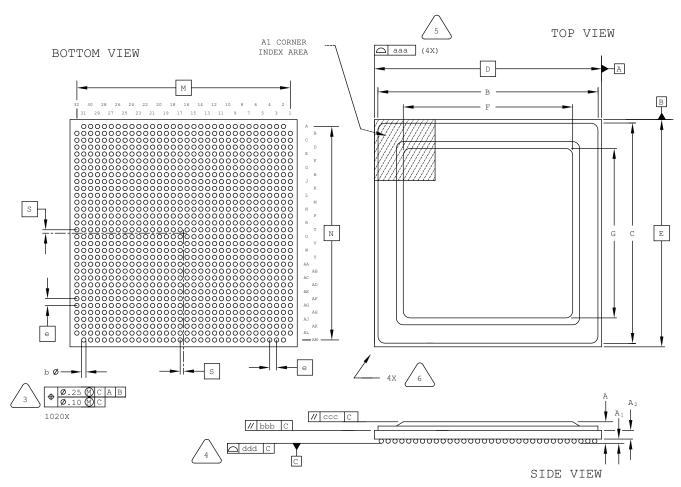


SYMBOL	MIN.	NOM.	MAX.
A	1.90	2.25	2.60
A1	0.30	0.50	0.70
A2	0.40	0.60	0.80
B/C	29.80	30.30	30.80
D/E	35.00 BSC		
M/N	33.00 BSC		
S	0.50 BSC		
b	0.50	0.60	0.70
е	1.00 BSC		
aaa	-	-	0.20
bbb	-	_	0.25
ccc	-	-	0.35
ddd	-	-	0.20



1020-Ball Organic fcBGA Package Rev. 2

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.

2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C



PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

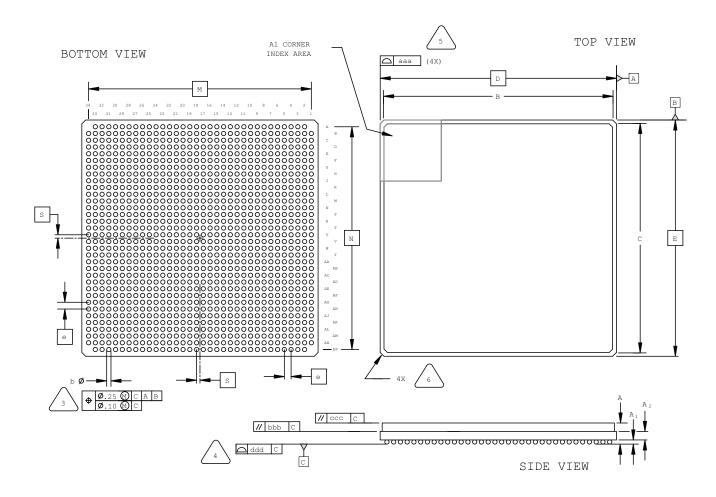


SYMBOL	MIN.	NOM.	MAX.
А	2.55	2.90	3.25
A1	0.40	0.50	0.60
A2	1.20 REF		
B/C	32.40	32.60	32.80
D/E	33.00 BSC		
F/G	24.50	24.60	24.70
M/N	31.00 BSC		
S	0.50 BSC		
b	0.50	0.60	0.70
е	1.00 BSC		
aaa	-	-	0.20
bbb	-	-	0.25
ccc	-	-	0.35
ddd	-	-	0.20



1152-Ball Ceramic fcBGA Package

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.

2. ALL DIMENSIONS ARE IN MILLIMETERS.

3

DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C





BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

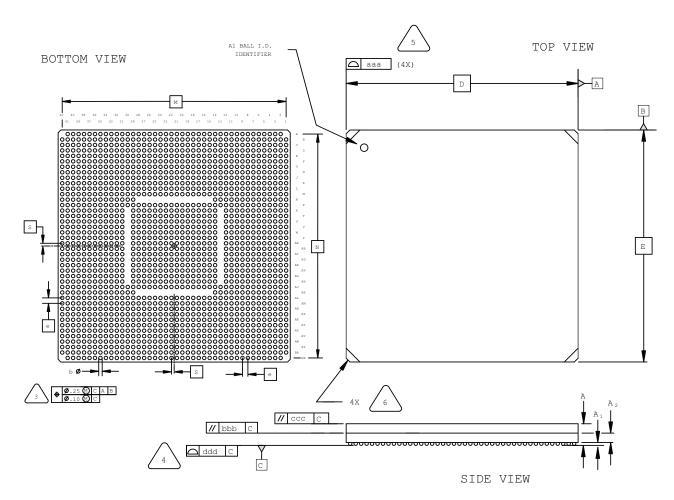


		1	
SYMBOL	MIN.	NOM.	MAX.
А	4.00	4.60	5.20
A1	0.30	0.50	0.70
A2	1.40 REF		
B/C	33.10	34.00	34.90
D/E	35.00 BSC		
M/N	33.00 BSC		
S	0.50 BSC		
b	0.50	0.60	0.70
е	1.00 BSC		
aaa	-	-	0.20
bbb	-	=	0.25
ccc	-	-	0.35
ddd	-	-	0.20



1704-Ball Ceramic fcBGA Package

Dimensions in Millimeters



NOTES: UNLESS OTHERWISE SPECIFIED

- DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C



PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.



BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.



PACKAGE BODY INCLUDES SUBSTRATE AND LID.





SYMBOL	MIN.	NOM.	MAX.
A	4.30	4.80	5.30
A1	0.30	0.50	0.70
A2	1.30	1.60	1.90
D/E	4:	2.50 BSC	
M/N	41.00 BSC		
S	0.50 BSC		
b	0.50	0.60	0.70
е	1.00 BSC		
aaa	-	-	0.20
bbb	-	-	0.25
ccc	-	-	0.35
ddd	-	-	0.20



Date	Version	Change Summary	
		Updated 48-Pin QFNS Package to 48-Pin QFN Package.	
		Added 48-Pin QFN Package Option 2.	
		Added 49-Ball WLCS Package.	
June 2014	4.4	Added 237-Ball ftBGA Package.	
		Added 285-Ball csfBGA Package.	
		Added 20-Ball WLCS Package.	
		Added 36-Ball WLCS Package.	
		Restored references to indicate top. bottom, and side views.	
Marrah 0014	0.4.0	Added 381-Ball caBGA Package.	
March 2014	04.3	Added 554-Ball caBGA Package.	
		Added 756-Ball caBGA Package.	
December 2013	04.2	Added "1" and "N" characters to 100-Pin TQFP Package Option 1: MachXO2, MachXO diagram (Top View).	
		Added 16-ball WLCS package.	
0	04.4	Revised 25-Ball WLCS Package title to 25-Ball WLCS Package (0.40mm Pitch).	
September 2013	04.1	Added 25-Ball WLCS Package (0.35mm Pitch).	
		Added references to indicate top. bottom, and side views.	
August 2013	04.0	Revised 144-pin TQFP package diagram.	
February 2013	03.9	Added 184-ball csBGA package.	
November 2012	03.8	Added iCE40 to the list of applicable products for the 32-pin QFNS Option 1 package.	
October 2012	03.7	Revised 324-ball ftBGA package drawing.	
September 2012	03.6	Nomenclature change – "iCE40 100-Pin TQFP Package Option 2" changed to "iCE40 100-Pin VQFP Package Option 2".	
August 2012	03.5	Added 36-ball ucBGA, 49-ball ucBGA, 81-ball ucBGA, 81-ball csBGA, 84-pin QFN, 100-pin TQFP Option 2, 121-ball csBGA, 121-ball ucBGA, 132-ball csBGA Option 2, 196-ball csBGA, 225-ball ucBGA, 284-ball csBGA packages.	
July 2012	03.4	Added 676-ball fcBGA package.	
March 2012	03.3	Added new 32-Pin QFNS Package Option 2 for MachXO2. Moved 32-pin QFN (punch singulated) package drawing to new Package Archive Appendix.	
February 2012	03.2	Updated document with new corporate logo.	
December 2011	03.1	Updated WLCS package offering.	
0.1.10044	03.0	Added 49-ball WLCS package and updated 25-ball WLCS package.	
October 2011		Added 328-ball csBGA package.	
July 2011	02.8	Included revised diagrams for the following packages: 56-ball csBGA, 100-ball csBGA and 132-ball csBGA. Added new 256-ball ftBGA Option 3 package.	
May 2011	02.7	Added MachXO2 to the list of applicable products for the 256 ftBGA Option 1 package outline.	
November 2010	02.6	Added 25-ball WLCS and 332-ball caBGA package drawings. Revised 100-pin PQFP, 120-pin PQFP, 128-pin PQFP, 160-pin PQFP and 208-pin PQFP package drawings. Removed obsolete packages including 144-, 240- and 304-pin PQFP packages.	
October 2010	02.5	Added 208-ball ftBGA package.	
September 2010	02.4	Revised maximum coplanarity values on Organic 1152 Flip Chip BGA – Option 2 and on Organic 1704 Flip Chip BGA from 0.20 mm to 0.23 mm.	
March 2010	02.3	Added new 1020-ball Organic fcBGA rev.2, 1152-ball Organic fcBGA, and 1704-ball Organic fcBGA package drawings. Removed obsolete 492-Ball BGA package.	
February 2010	02.2	Revised 256-ball caBGA nominal solder ball diameter from 0.5 mm to 0.45 mm to better match actual dimension.	
December 2009	02.1	Revised 256-ball caBGA package to specify correct JEDEC reference number.	
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Date	Version	Change Summary
May 2009	02.0	Added new 256-ball caBGA and 256-ball ftBGA (Option A) packages.
April 2009	01.9	Added 24-pin QFNS package diagram. Removed discontinued and obsolete packages (16 SOIC, 20 SOIC, 24 SOIC, 28 SOIC, 16 PDIP, 240 MQFP, 269 fcBGA, 304 MQFP, 600 SBGA).
December 2008	01.8	Added 32-pin QFNS, 48-pin QFNS and 64-pin QFNS package diagrams.
November 2008	01.7	Added 64-ball ucBGA and 132-ball ucBGA package diagrams.
April 2008	01.6	Added 64-ball csBGA and 144-ball csBGA package diagrams.
November 2007	01.5	Added 1152-ball fpBGA package diagram.
October 2007	01.4	Revised 1036 ftSBGA package diagram. Removed 1036 fpSBGA.
June 2007	01.3	Added 1036 ftSBGA package diagram.
February 2007	01.2	Revised 1704 fcBGA package drawing: removed lid dimension, clarified package body dimension as the combination of substrate and lid.
January 2007	01.1	Added Marking Orientation text for all TQFP packages (1.0 mm and 1.4 mm thick).
October 2006	01.0	Added 64-pin TQFP and 1704-ball fcBGA package diagrams.
_	_	Previous Lattice releases.