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What is "[Embedded - Microcontrollers](#)"?

"[Embedded - Microcontrollers](#)" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "[Embedded - Microcontrollers](#)"

Details

Product Status	Obsolete
Core Processor	eZ8
Core Size	8-Bit
Speed	5MHz
Connectivity	IrDA, UART/USART
Peripherals	Brown-out Detect/Reset, LED, POR, PWM, WDT
Number of I/O	22
Program Memory Size	1KB (1K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	256 x 8
Voltage - Supply (Vcc/Vdd)	2.7V ~ 3.6V
Data Converters	A/D 8x10b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	Through Hole
Package / Case	28-DIP (0.600", 15.24mm)
Supplier Device Package	-
Purchase URL	https://www.e-xfl.com/product-detail/zilog/z8f0123pj005eg

Revision History

Each instance in this document's revision history reflects a change from its previous edition. For more details, refer to the corresponding page(s) or appropriate links furnished in the table below.

Date	Revision Level	Chapter/Section	Description	Page No.
Sep	15	LED Drive Enable Register	Clarified statement surrounding the Alternate Function Register as it relates to the LED function; revised Flash Sector Protect Register description; revised Packaging chapter.	<u>51</u> , <u>144</u> , <u>210</u>
Mar 2008	14	n/a	Changed branding to Z8 Encore! XP F0823 Series where appropriate.	All
Dec 2007	13	Pin Description, General-Purpose Input/Output, Interrupt Controller, Watchdog Timer, Electrical Characteristics, and Ordering Information	Updated title from Z8 Encore! 8K and 4K Series to Z8 Encore! XP Z8F0823 Series. Updated Figure 3, Table 15, Table 35, Tables 59 through 61, Table 119 and Part Number Suffix Designations section.	<u>8</u> , <u>36</u> , <u>60</u> , <u>95</u> , <u>199</u> , and <u>220</u>
Aug 2007	12	Part Selection Guide, External Clock Setup, and Program Memory	Updated Table 1, Table 16, and Program Memory section.	<u>2</u> , <u>35</u> , and <u>13</u>
Jun 2007	11	n/a	Updated to combine Z8 Encore! 8K and Z8 Encore! 4K Series.	All
Dec 2006	10	Ordering Information	Updated Ordering Information chapter.	<u>211</u>

Table 70.	UART Address Compare Register (U0ADDR)	115
Table 71.	UART Baud Rate High Byte Register (U0BRH)	115
Table 72.	UART Baud Rate Low Byte Register (U0BRL)	115
Table 73.	UART Baud Rates	116
Table 74.	ADC Control Register 0 (ADCCTL0)	127
Table 75.	ADC Control/Status Register 1 (ADCCTL1)	129
Table 76.	ADC Data High Byte Register (ADCD_H)	130
Table 77.	ADC Data Low Bits Register (ADCD_L)	131
Table 78.	Comparator Control Register (CMP0)	133
Table 79.	Z8 Encore! XP F0823 Series Flash Memory Configurations	134
Table 80.	Flash Code Protection Using the Flash Option Bits	138
Table 81.	Flash Control Register (FCTL)	141
Table 82.	Flash Status Register (FSTAT)	142
Table 83.	Flash Page Select Register (FPS)	143
Table 84.	Flash Sector Protect Register (FPROT)	144
Table 85.	Flash Frequency High Byte Register (FFREQH)	145
Table 86.	Flash Frequency Low Byte Register (FFREQ_L)	145
Table 87.	Trim Bit Address Register (TRMADR)	148
Table 88.	Trim Bit Data Register (TRMDR)	149
Table 89.	Flash Option Bits at Program Memory Address 0000H	149
Table 90.	Flash Options Bits at Program Memory Address 0001H	150
Table 91.	Trim Options Bits at Address 0000H	151
Table 92.	Trim Option Bits at 0001H	152
Table 93.	Trim Option Bits at 0002H (TIPO)	152
Table 94.	ADC Calibration Bits	153
Table 95.	ADC Calibration Data Location	153
Table 96.	Serial Number at 001C–001F (S_NUM)	154
Table 97.	Serialization Data Locations	154
Table 98.	Lot Identification Number (RAND_LOT)	154
Table 99.	Randomized Lot ID Locations	155
Table 100.	OCD Baud-Rate Limits	160
Table 101.	OCD Commands	162
Table 102.	OCD Control Register (OCDCTL)	167
Table 103.	OCD Status Register (OCDSTAT)	168
Table 104.	Oscillator Configuration and Selection	169
Table 105.	Oscillator Control Register (OSCCTL)	172

Internal Precision Oscillator

The internal precision oscillator (IPO) is designed for use without external components. You can either manually trim the oscillator for a non-standard frequency or use the automatic factory-trimmed version to achieve a 5.53MHz frequency. The features of IPO include:

- On-chip RC oscillator that does not require external components
- Output frequency of either 5.53MHz or 32.8kHz (contains both a fast and a slow mode)
- Trimming possible through Flash option bits with user override
- Elimination of crystals or ceramic resonators in applications where high timing accuracy is not required

Operation

An 8-bit trimming register, incorporated into the design, compensates for absolute variation of oscillator frequency. Once trimmed the oscillator frequency is stable and does not require subsequent calibration. Trimming is performed during manufacturing and is not necessary for you to repeat unless a frequency other than 5.53MHz (fast mode) or 32.8kHz (slow mode) is required. This trimming is done at +30°C and a supply voltage of 3.3 V, so accuracy of this operating point is optimal.

Power down this block for minimum system power. By default, the oscillator is configured through the Flash Option bits. However, the user code can override these trim values, as described in the [Trim Bit Address Space](#) section on page 151.

Select one of the two frequencies for the oscillator: 5.53MHz and 32.8kHz, using the OSCSEL bits in the [Oscillator Control](#) chapter on page 169.

Index

Numerics

10-bit ADC 4

A

absolute maximum ratings 196

AC characteristics 200

ADC 178

architecture 121

block diagram 122

continuous conversion 124

control register 126, 129

control register definitions 126

data high byte register 130

data low bits register 131

electrical characteristics and timing 203

operation 122

single-shot conversion 123

ADCCTL register 126, 129

ADCDH register 130

ADC DL register 131

ADCX 178

ADD 178

add - extended addressing 178

add with carry 178

add with carry - extended addressing 178

additional symbols 177

address space 13

ADDX 178

analog signals 10

analog-to-digital converter (ADC) 121

AND 181

ANDX 181

arithmetic instructions 178

assembly language programming 174

assembly language syntax 175

B

B 177

b 176

baud rate generator, UART 108

BCLR 179

binary number suffix 177

BIT 179

bit 176

clear 179

manipulation instructions 179

set 179

set or clear 179

swap 179

test and jump 181

test and jump if non-zero 181

test and jump if zero 181

bit jump and test if non-zero 181

bit swap 181

block diagram 3

block transfer instructions 179

BRK 181

BSET 179

BSWAP 179, 181

BTJ 181

BTJNZ 181

BTJZ 181

C

CALL procedure 181

CAPTURE mode 89

CAPTURE/COMPARE mode 89

cc 176

CCF 180

characteristics, electrical 196

clear 180

CLR 180

COM 181

COMPARE 89

compare - extended addressing 178

COMPARE mode 89

compare with carry 178



part selection guide 2