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What is "Embedded - Microcontrollers"?

"Embedded - Microcontrollers" refer to small, integrated circuits designed to perform specific tasks within larger systems. These microcontrollers are essentially compact computers on a single chip, containing a processor core, memory, and programmable input/output peripherals. They are called "embedded" because they are embedded within electronic devices to control various functions, rather than serving as standalone computers. Microcontrollers are crucial in modern electronics, providing the intelligence and control needed for a wide range of applications.

Applications of "<u>Embedded - Microcontrollers</u>"

Details	
Product Status	Discontinued at Digi-Key
Core Processor	8051
Core Size	8-Bit
Speed	48 MIPS
Connectivity	I <sup>2</sup> C, SPI, UART/USART
Peripherals	Brown-out Detect/Reset, POR, PWM, Temp Sensor, WDT
Number of I/O	25
Program Memory Size	64KB (64K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	4.25K x 8
Voltage - Supply (Vcc/Vdd)	2.7V ~ 5.25V
Data Converters	A/D 21x10b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	32-LQFP
Supplier Device Package	-
Purchase URL	https://www.e-xfl.com/product-detail/silicon-labs/c8051f389-gq



## Full-Speed USB and Broad-Based Flash MCU Family

40/25 Port I/O; All 5 V tolerant with high sink current

Six general purpose 16-bit counter/timers

Low Frequency (80 kHz) Internal Oscillator

48-pin TQFP (C8051F380/2/4/6/8/A)

32-pin LQFP (C8051F381/3/5/7/9/B/C)5x5 mm 32-pin QFN (C8051F381/3/5/7/9/B/C)

Can switch between clock sources on-the-fly

External Memory Interface (EMIF)

ports all USB and UART modes

Pipelined instruction architecture: executes 70% of instructions in 1 or 2

Hardware enhanced SPI™, two I²C/SMBus™, and two enhanced UART

16-bit programmable counter array (PCA) wi h five capture/compare

Internal Oscillator: ±0 25% accuracy with clock recovery enabled. Sup-

External Oscillator: Crystal, RC, C, or clock (1 or 2 Pin modes)

High-Speed 8051 uC Core

Up to 48 MIPS operation

Expanded interrupt handler

system clocks

serial ports

Clock Sources

**Clock Sources** 

Die sales available

**Package** 

**Digital Peripherals** 

#### **Analog Peripherals**

### 10-Bit ADC (C8051F380/1/2/3/8/9/A/B/C Only)

- Up to 500 ksps
- Built-in analog multiplexer with single-ended and differen ial mode
- VREF from external pin, internal reference, or V<sub>DD</sub>
- Built-in temperature sensor
- External conversion start input option

#### **Two Comparators**

Internal Voltage Reference (C8051F380/1/2/3/8/9/A/B/C Only) Brown-Out Detector and POR Circuitry

#### USB Function Controller (C8051F380/1/2/3/4/5/6/7/C)

- USB specification 2.0 compliant
- Full-speed (12 Mbps) or low-speed (1 5 Mbps) operation
- Integrated clock recovery; no external crystal required for full-speed or low-speed
- Supports eight flexible endpoints
- 1 kB USB buffer memory
- Integrated transceiver; no external resistors required

#### **On-Chip Debug**

- On-chip debug circuitry facilitates full-speed, non-intrusive, in-system debug (no emulator required)
- Provides breakpoints, single stepping, inspect/modify memory, and registers
- Superior performance to emulation systems using ICE-chips, target pods, and sockets

#### Supply Voltage: 1.8 to 3.6 V

- Voltages from 2.7 to 5.25 V supported using on-chip voltage regulators

#### Memory

- 4352 or 2304 Bytes RAM
- 64, 32, or 16 kB Flash; In-system programmable in 512-byte sectors

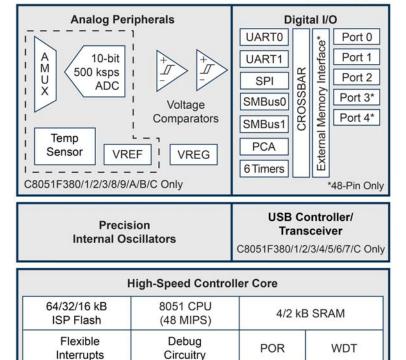
## 1.8 to 3.6 V Development Kits

## - C8051F380DK: full-featured kit for full evaluation

- ToolStick381DC: low-cost kit for quick and easy evaluation
- ToolstickBA: base adapter for use with ToolStick daughter cards

# Temperature Range: –40 to +85 °C

#### C8051F38x





## **Product Selection Guide**

				llator	or	KAM	or					. Array		(EMIF)					
Ordering Part Number	S (Peak)	Flash Memory (Bytes)	_	Calibrated Internal Oscillator	Frequency Oscillator	with 1k Endpoint RAM	Supply Voltage Regulator	SMBus/I2C	Enhanced SPI	Ts.	Timers (16-bit)	Programmable Counter Array	Digital Port I/O	External Mem Interface (EMIF)	it 500 ksps ADC	Femperature Sensor	oltage Reference	og Comparators	Package
Ordé	SAIW	-las	RAM	Calif	NO-	JSB	ldng	SME	≣nh	UARTs	Time	Proç	)igit	Exte	10-bit	Tem	/olta	Analog	acl
C8051F380-GQ	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	40	Yes	Yes	Yes	Yes	2	TQFP48
C8051F381-GQ	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	LQFP32
C8051F381-GM	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	QFN32
C8051F382-GQ	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	40	Yes	Yes	Yes	Yes	2	TQFP48
C8051F383-GQ	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	LQFP32
C8051F383-GM	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	QFN32
C8051F384-GQ	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	40	Yes	-	-	_	2	TQFP48
C8051F385-GQ	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	_	_	_	2	LQFP32
C8051F385-GM	48	64k	4352	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	_	_	_	2	QFN32
C8051F386-GQ	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	40	Yes	_	_	_	2	TQFP48
C8051F387-GQ	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	_	_	_	2	LQFP32
C8051F387-GM	48	32k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	_	_		2	QFN32
C8051F388-GQ	48	64k	4352	Yes	Yes	_	Yes	2	Yes	2	6	Yes	40	Yes	Yes	Yes	Yes	2	TQFP48
C8051F389-GQ	48	64k	4352	Yes	Yes	1	Yes	2	Yes	2	6	Yes	25	1	Yes	Yes	Yes	2	LQFP32
C8051F389-GM	48	64k	4352	Yes	Yes	_	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	QFN32
C8051F38A-GQ	48	32k	2304	Yes	Yes	_	Yes	2	Yes	2	6	Yes	40	Yes	Yes	Yes	Yes	2	TQFP48
C8051F38B-GQ	48	32k	2304	Yes	Yes	_	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	LQFP32
C8051F38B-GM	48	32k	2304	Yes	Yes	_	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	QFN32
C8051F38C-GQ	48	16k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	LQFP32
C8051F38C-GM	48	16k	2304	Yes	Yes	Yes	Yes	2	Yes	2	6	Yes	25	_	Yes	Yes	Yes	2	QFN32

# **Selected Electrical Specifications**

Parameter	Conditions	Min	Тур	Max	Units					
Global Electrical Characteristics										
Digital Supply Voltage		$V_{RST}$	3.3	3.6	V					
Digital Supply RAM Data Retention Voltage		_	1.5		V					
SYSCLK (System Clock)		0	_	48	MHz					
Specified Operating Temperature Range		-40	_	+85	°C					
Voltage Regulator (REG0)										
Input Voltage Range		2.7	_	5.25	V					
Output Voltage (V <sub>DD</sub> )	Output Current = 1 to 100 mA	3.0	3.3	3.6	V					
Output Current		_		100	mA					

Parameter	Conditions	Min	Тур	Max	Units				
ADC-DC Accuracy									
Resolution			bits						
SAR Conversion Clock		_	_	8.33	MHz				
Throughput Rate		_	_	500	ksps				
Voltage Reference									
Output Voltage	25 °C ambient	2.38	2.42	2.46	V				
VREF Short-Circuit Current		_	_	7	mA				
Comparators									
Response Time:	CP0+ - CP0- = 100 mV	_	100	_	ns				
Mode 0, Vcm* = 1.5 V	CP0+ - CP0- = -100 mV	_	250	_	ns				